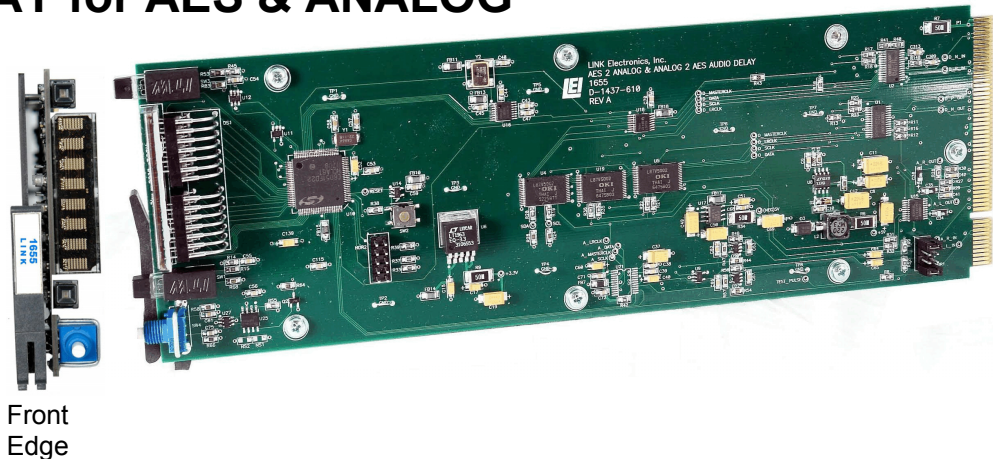


## AUDIO DELAY for AES & ANALOG

### Digiflex 1655



#### Features

- ◆ LED Set-up Display
- ◆ Lip Sync Correction
- ◆ Analog or AES Delay
- ◆ Delay up to 2 seconds
- ◆ Conversion, AtoD & DtoA
- ◆ Adjustable in steps of 1mS
- ◆ Mechanical Rotary Encoder
- ◆ Input Bal. or Un-Bal. AES or Bal. Analog
- ◆ Continuous outputs of AES and Analog Audio
- ◆ Selectable AES samples rates for Analog input
- ◆ Useable with different rear cells for AES Audio Distribution

The Digiflex 1655 is a single digital audio processor that can be placed into an AES or analog audio path to transparently add a selectable amount of delay of up to 2 seconds. The delay is adjustable in 1mS steps with a minimum delay through the card of 6.25 mS. This provides an easy solution to lip sync errors caused by delays from image processing within broadcast operations.

There is an eight character display for showing the function under adjustment, and a rotary encoder for user input of the delay setting and input type, located on the front card edge of the 1655 module. The two push-button switches allow you to either "Enter" a selection or "Escape" from that menu item.

There are three (3) rear cells that can be used to obtain audio delay and audio distribution for AES.

The 1655/1055 will up convert from Analog to AES as well as down convert AES to Analog. There are three continuous outputs of AES balanced, unbalanced, and balanced analog. The unit is compatible with AES at the following sample rates 32 KHz, 44.1 KHz, and 48 KHz. When converting analog to AES you can select the output sample rate.

The 1655 operates in the model 1000 DigiFlex, or 7200 frames, and comes with the rear cell. The rear cell has inputs for analog balanced, and AES, which is externally switchable for either balanced or unbalanced.

Up to ten modules can operate in the 1000/2 frame with dual power supplies. Performance is unmatched in the industry.

# DigiFlex 1655 AUDIO DELAY SPECIFICATIONS

## INPUT, AES:

Balanced: ..... One AES3-1992, twisted pair 110S, 3 pin connector  
 Level: ..... 4V Nominal  
 Unbalanced: ..... One AES3-ID 75 S BNC  
 Level: ..... 800mV to 1.2V  
 Sample Rates: ..... 32KHz, 44.1KHz, or 48KHz  
 Switch: ..... A two position switch on the rear I/O selects between Balanced and Unbalanced AES

## INPUT, ANALOG:

Balanced: ..... Two 3-Pin Connectors, Left & Right  
 Impedance: ..... 600S  
 Connector: ..... Weco, 3 pin balanced  
 Common Mode Rejection Ratio: ..... >55db

## OUTPUT, AES:

Balanced: ..... One AES3-1992, twisted pair 110S, 3 pin connector  
 Unbalanced: ..... One AES3-ID 75S BNC  
 Sample Rate: ..... 32KHz, 44.1KHz, or 48KHz  
 Coupling: ..... DC  
 Amplitude: ..... 4.2 volts p-p typical at 110S, 1V p-p typical at 75S  
 Jitter: ..... <0.1UI

## OUTPUT, ANALOG:

Balanced: ..... Two 3-Pin Connectors, Left & Right  
 Impedance: ..... 600S  
 Connector: ..... Weco, 3 pin balanced  
 Signal-to-Noise Ratio: ..... >55db  
 THD+N: ..... <.02

## PERFORMANCE:

Delay Resolution: ..... 1 mS  
 Minimum Delay: ..... 6.25mS  
 Maximum Delay: ..... 2.00625 seconds

## FRONT PANEL CONTROLS:

Switch, Enter: ..... Push Button  
 Switch, Escape: ..... Push Button  
 Mode & timing adjustments: ..... LED Display  
 Data Select: ..... Rotary Encoder

## ENVIRONMENTAL:

Temperature: ..... 0° to 50°C Ambient  
 Humidity: ..... 0% to 90% non-condensing  
 Power: ..... 3 Watts

## MECHANICAL:

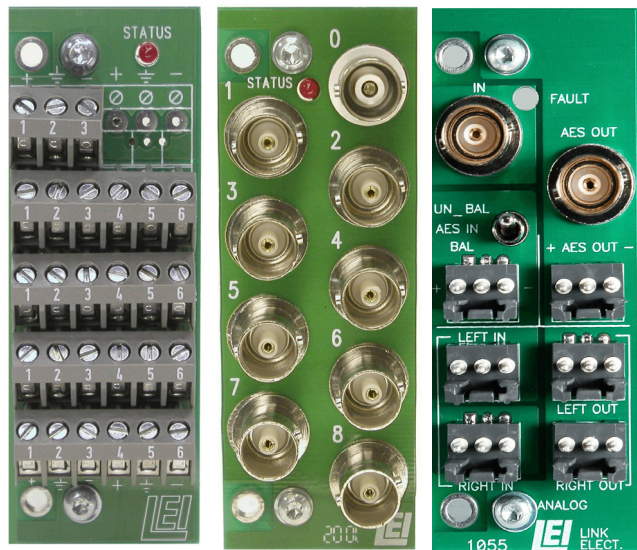
Height ..... 3.2 Inch  
 Width: ..... 1.0 Inch  
 Length: ..... 10 Inch

## REAR CELLS:

1016: ..... AES Bal in & 8 AES Bal outputs  
 1017: ..... AES UnBal in & 8 UnBal BNC out  
 1055: AES & Analog I/O, Conversion & delay

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Rear Cell 1016

Rear Cell 1017

Rear Cell 1055

SPECIFICATIONS SUBJECT TO CHANGE WITHOUT NOTICE

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