

# M38C29T-ADF

Temporary Target Board for M38C29RLFS

# User's Manual

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## 1. Things to Check When Unpacking

The M38C29T-ADF package consists of the following products. When unpacking your package, check to see that all of these components are included.

If you find any item missing or faulty, or any suggestion, contact your local distributor.

Item
M38C29T-ADF temporary target board
M38C29T-ADF User's Manual (this manual)

#### 2. Outline

The M38C29T-ADF is a temporary target board used to develop software with a compact emulator M38000T2-CPE or PC4701 emulator system. It supplies to the M38C29RLFS MCU the minimum required signals needed to operate the emulator MCU, including power supply voltage, clock signals and reset signals. Use it when the user system is not ready.

#### 3. Specifications

Table 3.1 lists the specifications of the M38C29T-ADF.

Table 3.1 Specifications

Applicable MCU	M38C29RLFS				
Clock	4.0MHz oscillator mounted (oscillator circuit board OSC-2 used)				
	32.768kHz (can be disconnected by SW1)				
Vcc	Supplied separately*				
Vss	Supplied separately				
RESET	- Reset circuit mounted (power-on reset and reset switch)				
	- Connects the reset output of an emulation pod				
External dimensions	Width	90mm			
	Depth	115mm			
Others	Universal part prepared				

\* Power cannot be supplied from the emulator (e.g. M38000T2-CPE). A separate power supply is needed.

## 4. Setting Up

This chapter describes how to set up the M38C29T-ADF.

- (1) Mount the emulator MCU on the M38C29T-ADF.
- After checking the position of the No. 1 pin of the emulator MCU, mount the emulator MCU referencing Figure 4.1. (2) Insert the connector on the tip of the emulator probe to the socket on the emulator MCU.

For the M38000T2-CPE, the converter board PCA4933 is required between the emulator probe and emulator MCU.

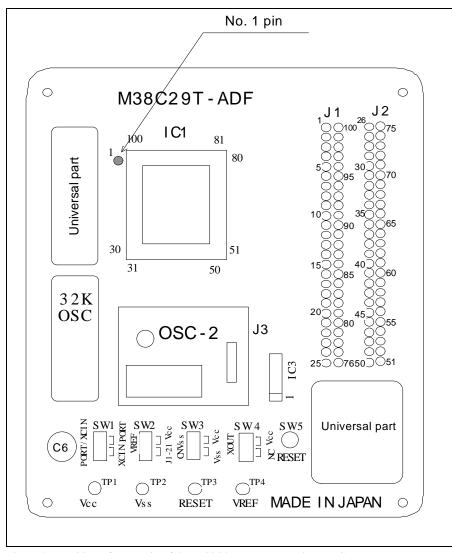


Figure 4.1 Position of No. 1 pin of the M38C29T-ADF's emulator MCU



- (3) Connect the RESET cable (white) and the GND cable (black) of the emulation pod to the RESET (TP3) pin and Vss (TP2) pin of the M38C29T-ADF respectively. And connect the Vcc cable (red) to the Vcc (TP1) pin.
- (4) Set the MCU type select switch of the M38000T2-CPE to the RLSS side.
- (5) Connect a power supply (not included) to the Vcc (TP1) pin of the M38C29T-ADF. And connect the GND output of the power supply to the Vss (TP2) pin. Use the power supply whose rising time is 10ms or less.

Table 4.1 lists the correspondence of the connector cables and signals, and Figure 4.2 shows the connection pattern.

Table 4.1 Connector cables of the M38000T2-CPE and applicable signals

Cable color	Signal
WHITE	RESET
BLACK	Vss
RED	Vcc (SENSE)

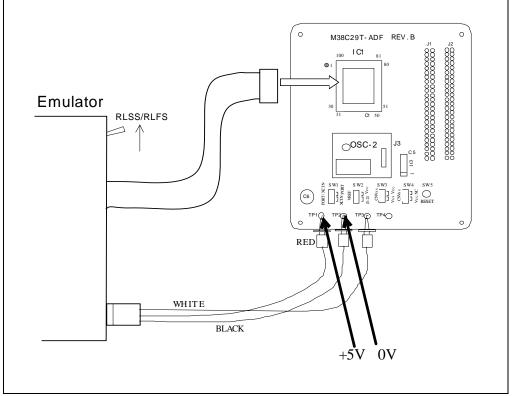


Figure 4.2 Connection pattern of the M38C29T-ADF

# (6) Setting SWSet the SW1 to SW4 according to your application. Table 4.2 describes the functions of the SW1 to SW4.

No.	Name	Function	Factory-setting
SW1	PORT/XCIN	Allows you to choose whether to supply the sub-clock signals (32.768kHz) to the	PORT
		MCU's XCIN pin (pin 43).	
		To use P61/XCIN as an output port, choose the PORT side.	
		To use the sub-clock signals, choose the XCIN side.	
SW2	VREF	Allows you to choose whether to apply Vcc or some other source to the MCU's VREF	Vcc
		pin (pin 21). In the case of the latter, choose the J1-21 side, and apply a voltage within	
		a prescribed range to the J1-21.	
SW3	CNVss	Allows you to choose which to connect, Vcc or Vss, to the MCU's CNVss pin (pin	Vss
		39).	
SW4	XOUT	Allows you to choose whether to pull up the MCU's XOUT pin (pin 46).	Vcc
		To use the oscillator circuit of OSC-2 on the temporary target board, or to input the	
		external clock, set SW4 to Vcc to pull up XOUT pin.	
		To use the oscillator circuit with a resonator between XIN and XOUT, set SW4 to NC.	

Table 4.2 Functions of the SW1, SW2, SW3 and SW4 of the M38C29T-ADF

## 5. Oscillator Circuit

#### 5.1 Oscillator Circuit Board

The M38C29T-ADF has a built-in oscillator circuit board on which a 4.0MHz oscillator is mounted.

Figure 5.1 shows an external view of the OSC-2 oscillator circuit board (bare board) and where connector pins are located. Figure 5.2 shows the circuitry of the OSC-2 oscillator circuit board (bare board). Use the number of oscillator circuits recommended by the oscillator manufacturer.

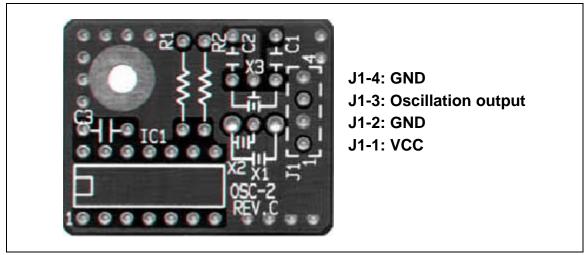


Figure 5.1 External view of the oscillator board (OSC-2) and connector pin assignment

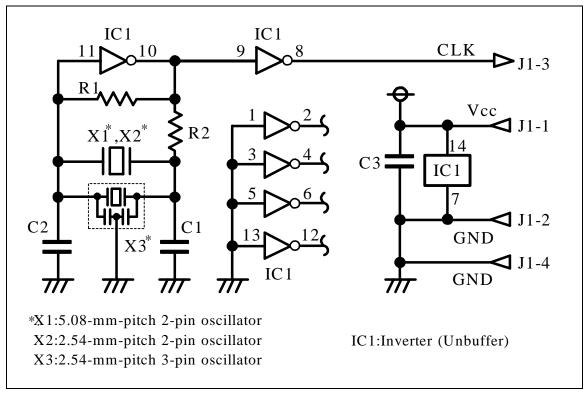


Figure 5.2 Circuit of the oscillator board (OSC-2)

#### 5.2 32.768kHz Oscillator Circuit

The 32.768kHz oscillator circuit is prepared on the M38C29T-ADF. When using the 32.768kHz oscillator circuit as a sub-clock, set SW1 to the XCIN side.

#### 6. Reset Circuit

Figure 6.1 shows the reset circuit and its waveform. In this product, reset is cleared in about 100ms from the instant Vcc passes about 2.1V after having started from 0 V. When Vcc drops down to about 2.1V, reset turns effective.

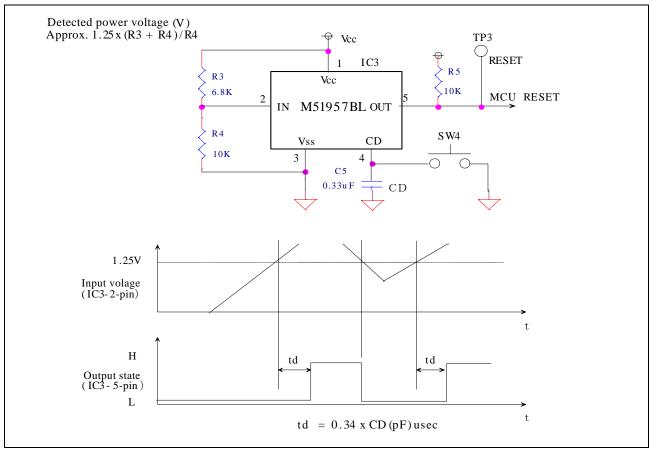


Figure 6.1 Reset circuit and its waveform

By using SW5, the MCU can be reset manually.

Table 6.1 Function of SW5 (reset switch)

No.	Name	Function
SW5	RESET	This is the reset switch. Press down this switch to reset the MCU.

# 7. Pin Layout

Figure 7.1 shows the pin layout of the M38C29T-ADF.

		IC	1	
	1			100
	2	NC	NC	99 J1-100
<u>J1-2</u>	3	NC	NC	98
<u>J1-3</u>	4	NC	P04	97 J1-98
<u>J1-4</u>	5	NC	P05	96
<u></u>	6	NC	P06	95 J1-96
	7	NC	P07	94
<u>J1-7</u>	8	NC	P10	93 J1-94
	9	P03	P11	92
<u>J1-9</u>	10	P02	P12	<u>J1-92</u>
<u>J1-10</u>	11	P01	P13	90
	12	P00	P14	70 89 11-90
	13	P57	P15 P16	88 J1-89 J1-88
	14	P56	P17	87 J1-88 J1-87
J1-14 J1-15	15	P55 P54	P20	86 J1-87
J1-16	16	P53	P20 P21	185
J1-17	17	P52	P22	84 J1-85
J1-18	18	P52 P51	P23	83 J1-84
J1-19	19	P51 P50	N C	82 J1-83
11.20	20	AVss	NC	81 J1-82
SW2 VREF TP4 O VREF $\frac{1}{2}$	21	VREF	NC	80
$J_{1-21} \bigcirc \bigcirc \bigcirc \bigvee_{Vcc} \bigcirc$			NC	79 J1-79
J1-22	22	P47	NC	78
J1-23	23	P46	NC	77
J1-24	24	NC	NC	76 J1-76
J1-25	25	NC		<u> </u>
	26			75
<u>J2-26</u>	27	NC	NC	74
<u>J2-27</u>	28	NC	NC	73
	29	NC	P24	72 72 72
<u>J2-29</u>	30	NC	P25 P26	71
	31	NC NC	P27	70 J2-70
12-31	32	NC	VL3	69 J2-69
J2-32 J2-33	33	P45	COM1	68 J2-68
J2-33 J2-34	34	P44	COM2	67 J2-67
J2-35	35	P43	COM2	66 J2-66
J2-36	36	P42	COM3	65 J2-65
J2-37	37	P41	P30	64 J2-64
12-38	38	P40	P31	63 J2-63
SW3 CNVss	39	CNVss	P32	62 J2-62
Vss 12-39			P33	61
J2-40	40	NC	P3/	60 12.60
TP3 QRESET	41		P35	59
J2-41	41	RESET	P36	38 12.58
RESET IC 10K			N C	157
	42	P62/XCOUT	NC	
<u> </u>		r02/ACOUT	NC	55 J2-55
SWI PORT/XCIN	43	P61/XCIN		154 152 12-54
		TUTACIN		153 152 12-53
32.768kHz XCIN PORT			NC	12 52
$\underline{\qquad}_{\text{TP2}} \wp_{\text{Vss}}$			NC	J2-51
< J2-44 →	44	Vs s	NC	50 J2-50
<i>m</i>	45		P37	
J2-45	-IJ	XIN	P60	48 J2-48
			Vc c	47 Vcc O TP1
OSC - 2 4 3				
$4 \text{MHz}$ $\begin{bmatrix} 2\\1 \end{bmatrix}$			NOTE	
			XOUT	
				J2-46

Figure 7.1 Pin layout of the M38C29T-ADF

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