## Re <br> INDUSTRIAL CONTROL COMMUNICAIIONS



MDB-100
MODBUS RTU / ASCII COMMUNICATIONS INTERFACE
FOR THE TOSHIBA VF-S7 SERIES
ADJUSTABLE SPEED DRIVE

## Introduction

Thank you for purchasing the ICC MDB-100 Modbus RTU / ASCII Communications Interface for the Toshiba VF-S7 Series Micro Adjustable Speed Drive. Before using the MDB-100 interface, please familiarize yourself with the product and be sure to thoroughly read the instructions and precautions contained in this manual. In addition, please make sure that this instruction manual is delivered to the end user of the drive units with which the MDB-100 interface is connected, and keep this instruction manual in a safe place for future reference or drive/interface inspection.

This instruction manual describes the device specifications, wiring methods, maintenance procedures, supported functions and usage methods for the MDB-100 Modbus RTU / ASCII communications interface.

In conjunction with this manual, the following manuals are supplied by Toshiba, and are essential both for ensuring a safe, reliable system installation as well as for realizing the full communication and control potential of the MDB-100 interface:

- Toshiba TOSVERT VF-S7 Series Instruction Manual
- Toshiba VF-S7 Industrial Inverter Serial Communications Option Manual

If you do not have copies of these documents available, please contact Toshiba or your local Toshiba distributor to obtain them.

## Usage Precautions

## Operating Environment

- Please use the MDB-100 only when the ambient temperature of the environment into which the MDB-100 is installed is within the following specified temperature limits:

$$
\begin{array}{ll}
\text { Operation: } & -10 \sim+40^{\circ} \mathrm{C}\left(+14 \sim+104^{\circ} \mathrm{F}\right) \\
\text { Storage: } & -25 \sim+65^{\circ} \mathrm{C}\left(-13 \sim+149^{\circ} \mathrm{F}\right)
\end{array}
$$

- Avoid installation locations that may be subjected to large shocks or vibrations.
- Avoid installation locations that may be subjected to rapid changes in temperature or humidity.


## Installation • Wiring

- Do not touch charged parts of the drive such as the terminal block while the drive's CHARGE lamp is lit. A charge will still be present in the drive's internal electrolytic capacitors, and therefore touching these areas may result in an electrical shock. Always turn all drive input power supplies OFF, and wait at least 5 minutes after the CHARGE lamp has gone out before connecting communication cables or motor wiring.
- Route all communication cables separate from the inverter input/output power wiring.
- To avoid the possibility of electric shock due to leakage currents, always ground the inverter unit's E/GND terminal and the motor. To avoid misoperation, do not connect the MDB-100's SHIELD terminal to either of the above-mentioned grounds or any other power ground.
- When making connections between the MDB-100 and S7 drives, do not use cables that exceed 5 meters in length.
- For further drive-specific precaution, safety and installation information, please refer to the appropriate Toshiba documentation supplied with your S7 drive.


## Other Precautions

- The drive's EEPROM has a life span of 10,000 write cycles. Do not write to the same holding register (other than registers 7A00 ~ 7A02 and FA00 ~ FA02 or write-only coils) more than 10,000 times.
- The MDB-100's EEPROM has a life span of 100,000 write cycles. Do not write to the same MDB-100 configuration register more than 100,000 times.
- Do not touch or insert a rod or any other item into the MDB-100's case while power is applied, as this may lead to electrical shock or device damage.
- Commission the disposal of the MDB-100 to a specialist.
- Do not assign the same network address to more than one MDB-100 station in the same network. For a detailed explanation of station addressing, refer to section 8.
- When the MDB-100 is configured to receive its Modbus RTU / ASCII station address from the drive on Channel A, be sure to reset the MDB-100 if the inverter number parameter on the drive on Channel A is changed. Refer to section 8 for more information.
- Because the MDB-100 derives its control power from the drive connected to Channel A, removing power from that drive will also cause the MDB-100 to lose power, even if power is still applied to the drive connected to channel B .
- When only 1 drive is connected to the MDB-100, it must be connected to Channel A.


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## 1. Interface Package Diagram



Note that the above diagram shows the MDB-100 with its cover removed. However, it is only necessary to remove the cover in order to initially configure the MDB-100. Once configured, the cover should be reinstalled to prevent damage and dust accumulation.

## 2. Feature Summary

The MDB-100 interface provides a wide array of network data access and drive control features. Combined with the flexible data access methods and universal acceptance of the Modbus network, this allows powerful networked control and monitoring systems to be designed. Some of the main features provided by the MDB-100 which allow for this control and configurability are briefly described here:

## Protocol

Modbus RTU and ASCII as specified in AEG Schneider Automation specification "Modicon Modbus Protocol Reference Guide PI-MBUS-300 Rev. J".

## Implementation

Modbus network is optically isolated RS485 half-duplex 2-wire + shield physical layer, allowing connection of up to 32 units (masters and slave MDB-100 units) to a single bus trunk.

## Network Baud Rates

Supports all Modbus RTU/ASCII baud rates from 300 baud to 38.4 kbaud .

## Drive Connections

The MDB-100 provides support for simultaneous connection of 2 VF-S7 drives. Both drives share a common Modbus station address. By supporting 2 drives per interface, the maximum number of drives that can be connected to 1 Modbus network segment without requiring repeaters increases from 31 ( 31 drives + 1 master) to 62 ( 31 MDB-100 units +1 master).

## Power Supply

Self-contained. Powered directly from the drive connected to the Channel A communications port. No external power supply devices or connections are required.

## Isolation

The MDB-100 has 3 separate isolated circuitry sections. Each drive is fully optically isolated from each other, and both drives are optically isolated from the Modbus network. By using optically isolated connections to the drives and the Modbus network, noise immunity is greatly improved and grounding differential problems become a thing of the past.

## Drive AutoScan Algorithm

Connections to the drives are automatically established and continuously monitored. No drive configuration needs to be performed to connect the MDB-100 and communicate via the Modbus RTU or ASCII network. Just plug it in - it’s that simple.

## Programmable Pointer Registers

32 programmable pointer registers are provided for user definition. By using these registers, frequently-accessed drive parameters that may be scattered throughout the register map can be grouped together and accessed with a single holding register read or write command.

## Response Delay Function

A programmable network response delay function is available which forces the MDB100 to wait a certain minimum time before generating network responses to Modbus commands. This function is useful when equipment such as radio modems (which require a finite time to switch from receive to transmit mode) are used as Modbus network bridges.

## Network Watchdog

A network watchdog function is available to detect if communication with the master controller has been interrupted. If an interruption is detected, several different drive actions can be programmed to ensure that the process under control can be safely and automatically driven to a desired state.

## Modbus Network Connector

The network interface is a 3-position pluggable terminal block with the following signals provided:

| Pin Number | Function |
| :---: | :--- |
| 1 | Modbus network RS485 "A" terminal |
| 2 | Modbus network RS485 "B" terminal |
| 3 | Network shield connection point |

No network termination is provided on the MDB-100. Be sure to install proper RS485 network termination on the MDB-100 units that exist on the extreme endpoints of the network bus. This can be accomplished by placing a $121 \Omega$ ( $\pm 5 \%$ ), $1 / 2 \mathrm{~W}$ resistor across the $A$ and $B$ network terminals of those 2 units.

## Drive Network Connectors

Uses standard RJ-45 style 8-pin modular connectors. Any standard category-5 ethernet cable (found in most electronics and office-supply stores) 5 meters or less in length can be used to connect the MDB-100 to the drives.

## Supported Commands

The MDB-100 presents and receives drive data via Modbus commands 01 (read coil status), 03 (read holding registers), 05 (force single coil), 06 (preset single register) and 16 (preset multiple registers).

## 3. Installing The MDB-100

The MDB-100 connects to each drive via the drive's communication port, located on the right-hand side of the drive enclosure under a small snap-on cover. Although no drive parameters need to be configured in order to use the MDB-100, it is advantageous to check that the drive's communication data rate is set to its maximum speed. Because the MDB-100 will communicate to each drive only at the drive's configured data rate, this will provide the fastest response time for drive-toModbus network data transfers. For information on checking the drive's communication data rate, refer to the appropriate manual supplied with your drive. Note that each drive's communication data rate settings are independent of the Modbus network data rate, which is configured solely by the Modbus network characteristics switch (refer to section 7). Also note that the data communication parameters of each drive are handled independently; the drive connected to Channel A may simultaneously communicate to the MDB-100 at completely different baud rates, parity settings, etc. than the drive connected to Channel B.

Installation of the MDB-100 Modbus RTU / ASCII interface should only be performed by a qualified technician familiar with the maintenance and operation of the connected drives. To install the MDB-100, complete the following steps:

1. 4 CAUTION! Verify that all input power sources to the drives to be connected have been turned OFF and are locked and tagged out.
2. $\boldsymbol{A}$ ANAERI, 4 wait at least 5 minutes for the drive's electrolytic capacitors to discharge before proceeding to the next step. Do not touch any internal parts with power applied to the drive, or for at least 5 minutes after power to the drive has been removed. A hazard exists temporarily for electrical shock even if the source power has been removed. Verify that the CHARGE LED has gone out before continuing the installation process.
3. Attach the MDB-100 to a sturdy, unmovable object (such as a wall) via the 2 case mounting holes located on the tabs at the top and bottom of the enclosure.
4. Remove the drive's communication port cover, located on the right-hand side of the drive (as viewed when facing the drive), by pressing against the side of the cover while sliding it toward the front of the drive. Do not discard this cover, as it should be reinstalled if the MDB-100 unit is ever disconnected from the drive.
5. Connect the drive's communication port to Channel A of the MDB-100 with the communication cable (communication cable is not included with the MDB-100 kit). When choosing cables for this connection, standard 24 AWG category-5 (CAT 5) unshielded twisted-pair (UTP) 8-conductor cables found in ethernet networks in most office environments can be used. The maximum allowable length for these cables is 5 meters. Although there are many varieties and styles of CAT-5 UTP cables available, ICC strongly recommends using only high-quality cables from reputable manufacturers to guarantee optimal noise immunity and cable longevity. Ensure that each end of the cable is fully seated into the modular connector, and route the cable such that it is located well away from any drive input power or motor wiring. Also take care to route the cable away from any sharp edges or positions where it may be pinched.
6. Repeat steps 1, 2, 4 and 5 above to connect another drive to Channel B on the MDB-100, if desired.
7. Remove the cover of the MDB-100 by removing the 4 cover screws.
8. Configure the Modbus network characteristics such as baud rate, parity etc. via 8position DIP switch SW1 (refer to section 7). If the MDB-100's station address is to be configured locally, select the desired station address via 8-position DIP switch SW2 (refer to section 8).
9. Reinstall the cover of the MDB-100.
10. Connect the Modbus network cable (2 signal wires + shield) to the 3-position pluggable terminal block marked "Network" on the left-hand side of the MDB-100. The terminal ordering for the network cable wiring is as shown in Figure 1.


Figure 1: Modbus RS485 Network Terminal Wiring

Note that some RS485 equipment manufacturers reference the "A" and "B" terminals reversed from the MDB-100's hardware, and some manufacturers use other labels, such as " + " and "-". If you cannot communicate with the MDB-100 after installation, try switching the "A" and "B" signal wires - swapped signal wires are the most common cause of communication difficulties in new RS485 network installations.

Ensure that the Modbus network cable is tightly screwed into the terminals, and route the cable such that it is located well away from any drive input power or motor wiring. Also take care to route all cables away from any sharp edges or positions where they may be pinched.
11. Take a moment to verify that the MDB-100 and all network cables have sufficient clearance from drives, motors, and power-carrying electrical wiring.
12. Turn the power sources to all connected drives ON, and verify that the drives function properly. If the drives do not appear to power up, or do not function properly, immediately turn power OFF. Repeat steps 1 and 2 to remove all power from the drives. Then, verify all connections. Contact ICC or your local drive distributor or manufacturer for assistance if the problem persists.

## 4. Equipment Specifications

| Item | Specification |
| :--- | :--- |
| Operating Environment | Indoors, less than 1000m above sea level, do not <br> expose to direct sunlight or corrosive / explosive gasses |
| Operating Temperature | $-10 \sim+40^{\circ} \mathrm{C}\left(+14 \sim+104^{\circ} \mathrm{F}\right)$ |
| Storage Temperature | $-25^{\circ} \mathrm{C} \sim+65^{\circ} \mathrm{C}\left(-13 \sim+149^{\circ} \mathrm{F}\right)$ |
| Relative Humidity | $20 \% \sim 90 \%$ (without condensation) |
| Vibration | $5.9 \mathrm{~m} / \mathrm{s}^{2}\{0.6 \mathrm{G}\}$ or less $(10 \sim 55 \mathrm{~Hz})$ |
| Grounding | SHIELD terminal connected to isolated network ground <br> via $200 \Omega$ resistance. |
| Cooling Method | Self-cooled |

## 5. Maintenance And Inspection

Preventive maintenance and inspection is required to maintain the MDB-100 Modbus interface in its optimal condition and to ensure a long operational lifetime. Depending on usage and operating conditions, perform a periodic inspection once every three to six months. Before starting inspections, always turn off all power supplies to connected drives, and wait at least five minutes after each drive's "CHARGE" lamp has gone out.


DANGER! 4
Do not touch any internal parts with power applied to the drives, or for at least 5 minutes after power to the drives has been removed. A hazard exists temporarily for electrical shock even if the source power has been removed.

## Inspection Points

- Check that the Modbus network connector screws and terminal block plug are not loose. Tighten if necessary.
- Check that the drive communication cables are not loose. Reinsert if necessary.
- Visually check all wiring and cables for damage. Replace as necessary.
- Clean off any accumulated dust and dirt.
- If use of the MDB-100 is discontinued for extended periods of time, apply power at least once every two years and confirm that the unit still functions properly.
- Do not perform hi-pot tests on the drives or MDB-100 interface, as they may damage the units.

Please pay close attention to all periodic inspection points and maintain a good operating environment.

## 6. Storage And Warranty

### 6.1 Storage

Observe the following points when the MDB-100 interface is not used immediately after purchase or when it is not used for an extended period of time.

- Avoid storing the MDB-100 in places that are hot or humid, or that contain large quantities of dust or metallic dust. Store the MDB-100 in a well-ventilated location.
- When not using the MDB-100 interface for an extended period of time, apply power at least once every two years and confirm that it still functions properly.


### 6.2 Warranty

The MDB-100 Modbus RTU / ASCII Communications Interface is covered under warranty by ICC for a period of 12 months from the date of installation, but not to exceed 18 months from the date of shipment from the factory. For further warranty or service information, please contact Industrial Control Communications or your local distributor.

## 7. Network Characteristics Configuration

The MDB-100 interface board uses an 8-position DIP switch (labeled SW1) to configure the Modbus network communication characteristics. The switch settings are only read during initialization, so if a change is made to any of the switches on SW1, the MDB-100 must be reset in order to enable the new settings. The various configuration settings of switch SW1 are as follows:

Communication Method:

| SW1 \# <br> $\mathbf{1}$ | Function |
| :---: | :--- |
| OFF | Modbus RTU (factory default) |
| ON | Modbus ASCII |

## Baud Rate:

| SW1 \# |  |  |  |
| :---: | :---: | :--- | :--- |
|  | $\mathbf{3}$ | $\mathbf{2}$ |  |
| OFF | OFF | OFF | 300 baud |
| OFF | OFF | ON | 600 baud |
| OFF | ON | OFF | 1200 baud |
| OFF | ON | ON | 2400 baud |
| ON | OFF | OFF | 4800 baud |
| ON | OFF | ON | 9600 baud (factory default) |
| ON | ON | OFF | 19.2 kbaud |
| ON | ON | ON | 38.4 kbaud |

Parity:

| SW1 \# |  |  |
| :---: | :---: | :--- |
| $\mathbf{6}$ | $\boldsymbol{5}$ |  |
| OFF | OFF | even parity (factory default) |
| OFF | ON | odd parity |
| ON | OFF | no parity (2 stop bits) |
| ON | ON | no parity (1 stop bit) - applies only to RTU mode |

Protocol:

| SW1 \# <br> $\mathbf{7}$ | Function |
| :---: | :--- |
| OFF | Modicon Modbus (factory default) |
| ON | DO NOT SELECT (reserved for future expansion) |

Station Number Origin: (refer to section 8)

| SW1 \# <br> $\mathbf{8}$ | Function |
| :---: | :--- |
| OFF | Drive A's inverter number parameter (factory default) |
| ON | Switch SW2 address setting |

## A Note About Modbus Communication Formats

According to the Modicon Modbus Protocol Reference Guide, the specifications for both Modbus ASCII and RTU communication modes are as follows:

## ASCII Mode

Coding System: ...... Hexadecimal, ASCII characters 0~9, A~F
One hexadecimal character contained in each ASCII character of the message

Bits per Byte: .......... 1 start bit
7 data bits, least significant bit sent first 1 bit for even/odd parity; no bit for no parity 1 stop bit if parity is used; 2 bits if no parity

Error Check Field:... Longitudinal Redundancy Check (LRC)

## RTU Mode

Coding System: ...... 8-bit binary, hexadecimal $0 \sim 9, \mathrm{~A} \sim$ F
Two hexadecimal characters contained in each 8-bit field of the message

Bits per Byte: .......... 1 start bit
8 data bits, least significant bit sent first 1 bit for even/odd parity; no bit for no parity
1 stop bit if parity is used; 2 bits if no parity
Error Check Field:... Cyclical Redundancy Check (CRC)

According to the Modicon Modbus specification, therefore, the number of bits per byte in ASCII mode is 1 start +7 data +1 parity +1 stop (if parity is used), or 1 start +7 data +2 stop (if parity is not used). The ASCII data frame is therefore fixed at 10 bits per byte. Similarly, the number of bits per byte in RTU mode is 1 start +8 data + 1 parity +1 stop (if parity is used), or 1 start +8 data +2 stop (if parity is not used), resulting in an RTU data frame that is fixed at 11 bits per byte.

In addition to these standard specified data frame sizes, the MDB-100 interface offers an optional configuration of providing for only 1 stop bit when no parity is selected in the RTU communication mode. As indicated on the previous page, by setting SW5 and SW6 both to "ON", the RTU data frame size is modified to consist of 1 start +8 data +1 stop bit $=10$ bits per byte. Please note that this setting is only valid when RTU mode is selected; if SW5 and SW6 are both set to "ON" when ASCII mode is selected, the MDB-100 will halt and will not communicate with the Modbus network.

## 8. Modbus Station Address Selection

The MDB-100 interface provides two different methods for configuring the node's station address. One method uses the inverter number parameter read from the drive connected to Channel A, and the other method uses a locally-configured address, set by DIP switches on the MDB-100 control PCB.

When shipped from the factory, the MDB-100's default configuration is to use the value set in the inverter number parameter (F802) of the drive connected to Channel A as its Modbus network station address. Depending on the software version of the drive connected to channel $A$, this parameter is adjustable from either $0 \sim 31$ or $0 \sim 63$.

Since Modbus addressing allows for station addresses to be set from 1~247, however, there may be instances where the upper limit of the inverter number parameter setting is not high enough to support a desired address (for example, if you want to set a MDB-100 station to address 95) In these instances, the locallyconfigured station address can be used, which supports the full Modbus station addressing range of $1 \sim 247$.

Note that the Modbus protocol specification states that allowable station addresses are 1 to 247. If a station address is configured with 0 (inverter number or local address) or 248 to 255 (local address only), the MDB-100 will halt and will not communicate with the network. To resolve this situation, correct the station number and reset the MDB-100. Pay particular attention to this point, as the default factory setting of the drive's "inverter number" parameter is 0 , which therefore must be changed if it is to be used as the MDB-100's station number.

The selection of whether to use the drive on Channel A's inverter number parameter or the locally-set station address is performed by switch SW1 \#8. When SW1 \#8 is OFF (factory default setting), the MDB-100's station address is retrieved from drive A's inverter number parameter. When SW1 \#8 is ON, the MDB-100's station address is obtained by the binary number set via DIP switch SW2 (factory default address setting = 1 ).

| SW1 \#8 | Meaning |
| :---: | :--- |
| OFF | Drive A's inverter number parameter (factory default) |
| ON | Switch SW2 address setting |

The values of switches SW1 and SW2 are read only during MDB-100 initialization. Therefore, if any of these switch settings are changed, the MDB-100 must be reinitialized to be made aware of the change by:

1. Momentarily powering-off drive A (from which the MDB-100 receives power), or
2. Issuing a reset command to the MDB-100 via the Modbus network by writing to holding register $0 \times 6111^{\text {NOTE }}$ 1, NOTE 2 . Writing data $0 \times 5$ A5A will reset the MDB-100, writing data 0xFEBA will reset the MDB-100 and return all MDB-100 -specific configuration registers (such as response delay time, loss of communications timeout time etc.) to their factory default values, and writing any other data will cause the MDB-100 to return a Modbus "ILLEGAL DATA VALUE" exception response (exception code 03). Reading from register 0x6111 will always return a value of 0 .

| Holding <br> Register | Write Data | Meaning |
| :--- | :---: | :--- |
| $0 \times 6111$ | $0 \times 5 A 5 A$ | Reset the MDB-100 |
|  | $0 \times F E B A$ | Reset the MDB-100 and return MDB-100 <br> configuration registers to factory default <br> values. |
|  | Any other value | Return "ILLEGAL DATA VALUE" <br> exception |

NOTE 1: Throughout this document, hexadecimal (base 16) numbers will be represented by a preceding " $0 x$ " or by a trailing " H " designator, decimal (base 10) numbers will be represented by a trailing subscripted "10" designator, and binary (base 2) numbers will be represented by a trailing " B " designator. Binary numbers will always be written with their most significant bit (MSB) at the left, and their least significant bit (LSB) to the right (next to the "B").

For example, $0 \times 6111=6111 \mathrm{H}=24849_{10}=0110000100010001 \mathrm{~B}$.
NOTE 2: The Modbus specification lists two addresses for each holding register (4X reference) and coil (0X reference). These are referred to as the "addressed as" and "known" as values. In all instances, registers and coils are addressed starting at zero; i.e. register 1 is addressed as 0 , and coil 15 is addressed as 14 , etc. Throughout this document, all addresses given for coils and holding registers will be their "addressed as" values unless otherwise specified. In other words, add 1 to the register/coil address given to obtain its "known as" value. For example, the S7 drive's inverter number parameter is $0802 \mathrm{H}\left(=0 \times 0802=2050_{10}\right.$.) Therefore, this register would be "known as" 2051. Refer to the Modbus Protocol Reference Guide for a more detailed explanation of this issue.

The station address set via switch SW2 must be set using binary encoding. In this case, SW2 \#1 represents bit \#0 of the address, and SW2 \#8 represents bit \#7. When one of the DIP switches of SW2 is turned ON, the corresponding address bit is 1 , and when one of the switches is turned OFF, the corresponding address bit is 0 .

For example, if an address of $169_{10}$ is desired, the conversion calculation would be performed as follows:
$169_{10}=0 x A 9=1010$ 1001B, therefore switches SW2 \#1, \#4, \#6 and \#8 should be ON, and \#2, \#3, \#5 and \#7 should be OFF.

Some more address configuration examples (factory default setting =1):

| Address <br> (Decimal) | Address <br> (Hex) | Address <br> (Binary) | "ON" <br> Switches | "OFF" <br> Switches |
| :---: | :---: | :---: | :---: | :---: |
| $1(\mathrm{~min})$ | $0 \times 01$ | 00000001 B | $\# 1$ | $\# 2 \sim \# 8$ |
| 63 | $0 \times 3 \mathrm{~F}$ | 00111111 B | $\# 1 \sim \# 6$ | $\# 7, \# 8$ |
| 200 | $0 x \mathrm{C} 8$ | 11001000 B | $\# 4, \# 7, \# 8$ | $\# 1 \sim \# 3, \# 5, \# 6$ |
| $247(\mathrm{max})$ | $0 x F 7$ | 11110111 B | $\# 1 \sim \# 3, \# 5 \sim \# 8$ | $\# 4$ |

## 9. Modicon Programming

### 9.1 Supported Modbus Commands

The MDB-100 interface supports 5 Modbus commands: command 1 ( $0 x 01$ : read coil status), command 3 ( $0 x 03$ : read holding registers), command 5 ( $0 \times 05$ : force single coil), command 6 ( $0 \times 06$ : preset single register) and command 16 ( $0 \times 10$ : preset multiple registers). Not all registers or coils support all commands (for example, read-only registers cannot be written to with a command 16). For more information, refer to section 10. The following limits represent the maximum number of registers and coils that can be read/written in one packet transaction:

| Command | RTU Mode <br> Read Max | RTU Mode <br> Write Max | ASCII Mode <br> Read Max | ASCII Mode <br> Write Max |
| :---: | :---: | :---: | :---: | :---: |
| 1 | 32 coils | N/A | 32 coils | N/A |
| 3 | 125 registers | N/A | 61 registers | N/A |
| 5 | N/A | 1 coil only | N/A | 1 coil only |
| 6 | N/A | 1 register only | N/A | 1 register only |
| 16 | N/A | 123 registers | N/A | 59 registers |

### 9.2 Programmable Pointer Register Function

MDB-100 registers $0 \times 6000 \sim 0 \times 601 \mathrm{~F}$ and $0 \times 6080 \sim 0 \times 609 \mathrm{~F}$ are used as programmable pointer and data registers, respectively. Registers $0 \times 6000 \sim 0 \times 601 \mathrm{~F}$ ( 32 total) are used to define other register addresses from which you would like to read or write, and registers 0x6080~0x609F (32 total) are the actual registers used to access the data located at the register addresses defined in registers 0x6000 ~ $0 x 601 \mathrm{~F}$. For example, if you would like to continuously read the data from registers 0xFE03, 0xFE04, 0xFE06, and 0xFD00, the standard register configuration would require 3 read commands to be issued: one reading 2 registers starting at register $0 x F E 03$, one reading 1 register starting at register 0xFE06, and one reading 1 register starting at register 0xFD00. To conserve network bandwidth and speed processing time, however, the programmable pointer registers can be used to allow the same information to be accessed, but by only issuing 1 command which reads 4 registers.

To configure this function, program as many registers as necessary (up to 32) in the range $0 \times 6000 \sim 0 \times 601 \mathrm{~F}$ with the register numbers of the registers you would like to continuously access. In this example, we would set register 0x6000 to 0xFE03 (the first register number we want to access), register 0x6001 to 0xFE04, register $0 \times 6002$ to $0 \times F E 06$, and register $0 \times 6003$ to $0 \times F D 00$. The data located at these drive status registers can then be obtained by accessing the corresponding registers in the range $0 \times 6080 \sim 0 \times 609 \mathrm{~F}$ (data register $0 \times 6080$ corresponds to address register 0x6000, data register $0 \times 6081$ corresponds to address register 0x6001, etc.) Therefore, the 4 registers that are to be monitored can now be accessed simply by issuing 1 holding register read command with a length of 4 starting from register 0x6080. The returned
data will be the data obtained from registers $0 x F E 03,0 x F E 04,0 x F E 06$, and $0 x F D 00$ (in that order).

Note that the settings of the programmable address registers (0x6000 ~ 0x601F) are stored in the MDB-100's nonvolatile EEPROM. Therefore, do not write to any of these registers more than 100,000 times. Typically, these registers would only be written to once, when the interface and Modbus network are first commissioned. When the programmable pointer registers are modified, the changes are made effective in the MDB-100 immediately (the MDB-100 does not need to be reset for the changed values to take effect).

### 9.3 Loss of Communications Timer Function

A configurable "loss of communications" (network watchdog) timer function is provided, which can detect communication losses and perform certain actions if a valid packet is not received and processed within a set time period.

MDB-100 register 0x6100 sets the loss of communication time value (adjustable from 100 ms to 60.000 s in 1 ms increments, factory setting $=10.000 \mathrm{~s}$ ). If a valid (exception-free) reception-response or exception-free broadcast does not take place within this time limit, the timer will expire. If the timer expires, 5 possible actions can occur for each connected drive, as set by the value of registers 0x6101 and 0xE101 (loss of communications action for drive A and drive B , respectively):

| Register <br> 0x6101 / 0xE101 <br> Setting | Action Taken Upon Timeout |
| :---: | :--- |
| 0 (default) | No action: ignore timeout |
| 1 | Decelerated stop |
| 2 | Coast stop |
| 3 | Trip "E" (emergency off) |
| 4 | Run a preset speed |

When a network timeout is detected, the timeout action is performed. When communications are once again reestablished, the timer function is automatically reset and will once again begin to monitor network receptions. However, any actions performed as a result of the timeout occurring will not be reversed by the MDB-100 once communications are reestablished; the Modbus master's application must explicitly modify the affected conditions to fully return the drive to its previous state.

Setting 0 is the default setting; when a communications timeout occurs, no action will be taken.

For setting 1 (decelerated stop), the drive will stop in a controlled manner if it was running. This action clears bit \#10 in the selected drive's communication command bit structure (register 0x7A00 / 0xFA00 - also the same data as write-only coil \#10 / \#26). Note that the "communication command valid" bit of register 0x7A00 / 0xFA00 (bit \#15 of each register, or write-only coil \#15 / \#31) must already be set by the master application for this timeout action to control the drive.

For setting 2 (coast stop), the drive will coast stop (free run) if it was running. This action clears bit \#10 in the selected drive's communication command bit structure (register 0x7A00 / 0xFA00 - also the same data as write-only coil \#10 / \#26) and sets bit \#11 (same as coil \#11 / \#27). Note that the "communication command valid" bit of register 0x7A00 / 0xFA00 (bit \#15 of each register, or write-only coil \#15 / \#31) must already be set by the master application for this timeout action to control the drive.

For setting 3 (trip "emergency off"), the drive will trip "E". This action clears bit \#10 in the selected drive's communication command bit structure (register 0x7A00 / 0xFA00 - also the same data as write-only coil \#10 / \#26) and sets bit \#12 (same as coil \#12 / \#28). Note that in this case the setting of the "communication command valid" bit of register 0x7A00 / 0xFA00 (coil \#15 / \#31) is irrelevant; the drive will trip regardless of its value.

Setting 4 (run a preset speed) will modify the settings of bits \#0 ~ \#3 of the selected drive's communication command bit structure (register 0x7A00 / 0xFA00 - also the same data as write-only coils \#0 ~\#3 / \#16 ~ \#19). The preset speed that is to run is selected by MDB-100 registers $0 \times 6102$ (for drive A) and 0xE102 (for drive B). The possible adjustment range for these registers is $0 \sim 15$, corresponding to "no action" $(0)$ and each of the drive's 15 possible preset speeds ( $1 \sim 15$ ). Note that the "communication command valid" and "run/stop" bits of register 0x7A00 / 0xFA00 (bits \#15 and \#10 of each register, or write-only coil \#15 \& \#10 / \#31 \& \#26) must already be set by the master application for this timeout action to control the drive. USE EXTREME CAUTION WHEN SELECTING THIS SETTING! Thoroughly verify that there is no possibility of personal injury or equipment damage due to the inverter running at the selected speed, especially with the possibility that network communications may not be able to be reestablished in a timely fashion (depending on what network condition caused the communications timeout in the first place).

Note that the settings of the timeout time, timeout action and timeout preset speed configuration registers are stored in the MDB-100's nonvolatile EEPROM. Therefore, do not write to any of these registers more than 100,000 times. Also note that when the setting of any one of these configuration registers is changed, the MDB-100 must be reset for the new value to be activated and the change to take effect.

### 9.4 Response Delay Timer Function

Register $0 \times 6110$ contains the setting for a response delay timer function. This function is useful for applications where it may not be desirable for the MDB-100 interface to respond immediately to the network after a read/write request, such as when a radio modem that must be switched from receive mode to transmit mode is being used.

The response delay timer is adjustable in 1 ms increments from 0 s to 2.000 s (factory setting $=0 \mathrm{~s}$ ). A response delay of 0 s means that there is no delay; response packets will be sent by the MDB-100 as soon as they are available. The delay timer starts when a complete packet is received by the MDB-100, and a response will not be sent until the timer has expired (at a minimum). Note that this time value only sets a minimum response delay value - depending on the quantity and location of registers read / written, much more time may actually be required before a complete response is formulated and ready to be returned to the network.

The response delay timer value is read only upon MDB-100 initialization, requiring the unit to be reset if the value is changed for the new value to take effect. This setting is also non-volatile (written to the MDB-100's EEPROM). Therefore, do not write to this register more than 100,000 times.

## 10. Modbus Programming Interface

### 10.1 Overview

Three types of Modbus data may be accessed via the MDB-100. These are holding registers ( 4 x reference), read-only coils ( $0 x$ reference) and write-only coils ( 0 x reference). In order to access 2 drives within the allocated holding register map ( $0 \times 0000 \sim 0 x F F F F$ ), each drive is assigned one half of the register space.

Drive A occupies the area from register $0 \times 0000$ to $0 x 7 F F F$. This area also contains several network and drive configuration registers (such as the programmable pointer and data registers, network timeout registers, etc.) that reside within the MDB-100 only. The registers which the MDB-100 uses for these functions are not accessible in the drive. Currently, these registers are unoccupied in the drive, and therefore no loss of access to any drive parameters exists. Most of the registers for drive A are mapped to the exact same "communication number" that they are allocated in the S7 drive by Toshiba. Refer to the VF-S7 Serial Communications Option Manual for a list of communication numbers. The only exception to this direct mapping are the registers that occupy communication numbers larger than $0 \times 8000$. For these parameters, their corresponding Modbus holding registers can be obtained by setting the MSB (bit \#15) of their communication number to 0 .

For example, the communication number for drive A's bus voltage parameter is 0xFE04 (1111 11100000 0100B). Setting bit \#15 to 0, we obtain the binary value 011111100000 0100B, or 0x7E04. Reading from Modbus holding register 0x7E04 ("known as" 0x7E05), therefore, will return the bus voltage from drive A.

Drive B occupies the area from register 0x8000 to 0xFFFF. This area also contains the MDB-100 -specific drive configuration registers that correspond to those available for drive A. Most of the registers for drive B are not mapped to the exact same "communication number" that they are allocated in the S7 drive by Toshiba. For these parameters, their corresponding Modbus holding registers can be obtained by setting the MSB (bit \#15) of their communication number to 1 .

For example, the communication number for drive B's deceleration time \#1 parameter is $0 \times 0010$ ( 0000000000010000 B ). Setting bit \#15 to 1 , we obtain the binary value 1000000000010000 B , or $0 \times 8010$. Writing to Modbus holding register $0 \times 8010$ ("known as" 0x8011), therefore, will modify the deceleration time \#1 parameter for drive $B$.

The only exception to this mapping modification requirement for drive $B$ are the registers that occupy communication numbers larger than $0 \times 8000$. For these parameters, the communication numbers given in the VF-S7 Serial Communications Option Manual correspond exactly to their assigned "addressed as" holding registers (for example, holding register 0xFE10 will access the past trip \#1 value from drive B).

Refer to Figure 2 on page 20 for a graphical representation of the holding register mapping that exists in the MDB-100 Modbus interface.

Figure 2: MDB-100 Holding Register Mapping


Note: Inaccessible (currently unused) areas are shaded.

Table 1: Holding Register Mapping General Overview

| Register \# | Name / Function | Factory Setting | Adjustment Range |
| :---: | :---: | :---: | :---: |
| 0000 ~ 05FF | Access drive A | -- | -- |
| 6000 ~ 601F | Non-volatile programmable pointer registers (32 total available) | $\begin{gathered} 0000 \\ (\text { all 32) } \end{gathered}$ | $\begin{gathered} \text { Any value other than } \\ 6000 \sim 601 \mathrm{~F} \text { or } \\ 6080 \sim 609 \mathrm{~F} \\ \hline \end{gathered}$ |
| 6080 ~ 609F | Programmable pointer data registers (each register corresponds to same one in 6000 ~ 601F range) | -- | Depends on registers pointed to |
| 6100 | Modbus network loss of communications timeout time | $\begin{gathered} \hline 10000 \\ (10.000 \mathrm{~s}) \\ \hline \end{gathered}$ | $\begin{gathered} 100 \sim 60000 \\ (0.100 s \sim 60.000 s) \\ \hline \end{gathered}$ |
| 6101 | Modbus network loss of communications timeout action for Drive A | 0 | $\begin{array}{\|l} \hline 0=\text { do nothing } \\ 1=\text { decelerated stop } \\ 2=\text { coast stop } \\ 3=\text { trip "E" } \\ 4=\text { run a preset } \\ \text { speed } \end{array}$ |
| 6102 | Loss of communications preset speed for Drive A (effective only when register 6101 is set to 4) | 0 | $0 \sim 15$ |
| 6110 | Modbus network response delay time | 0 | $\begin{gathered} 0 \sim 2000 \\ (0 \mathrm{~ms} \sim 2.000 \mathrm{~s}) \\ \hline \end{gathered}$ |
| 6111 | Reset/reinitialize MDB-100 interface | (read always returns 0) | 0x5A5A: reset only <br> 0xFEBA: return MDB-100 config registers to factory setting values |
| 6112 | MDB-100 firmware version/revision (read-only) | -- | $\begin{aligned} & \text { Version }=\text { MSB } \\ & \text { Revision }=\text { LSB } \end{aligned}$ |
| 6113 ~ 6FFF | Access drive A | -- | -- |
| 7000 ~ 7FFF | Access drive A communication numbers F000 ~ FFFF | -- | -- |
| 8000 ~E100 | Access drive B communication numbers $0000 \sim 6100$ | -- | -- |
| E101 | Modbus network loss of communications timeout action for Drive B | 0 | Same as register 6101 |
| E102 | Loss of communications preset speed for Drive B (effective only when register E101 is set to 4). | 0 | 0~15 |
| E103 ~ EFFF | Access drive B registers 6103~ 6FFF | -- | -- |
| F000 ~ FFFF | Access drive B | -- | -- |

Re
Sections 10.2 to 10.4 specify the communication number $\rightarrow$ holding register mappings and coil definitions for all current VF-S7 communication numbers. Note that the parameters and coils shown are those available at the time of this manual printing; if new parameters are added by the drive manufacturer and/or parameter adjustment limits are modified, etc., this will not affect the operation of the MDB-100 interface. As all available parameters, adjustment ranges etc. are determined solely by the connected drive, and not the MDB-100, future drive firmware versions will automatically be supported by the MDB-100 with no software upgrades required.

In the case of any discrepancies between the information in the following tables and the drive's documentation, the drive's documentation should always be followed.

Some other important coil and holding register notes:

- Remember that all registers and coils indicated in the following sections are shown with their "addressed as" coil \& register numbers. These coil \& register numbers are 1 less than their "known as" numbers.
- A value indicated by "*" in the following tables indicates that the setting is dependent on the connected drive's capacity. Refer to the drive's documentation for these values.
- If frequency command values (registers 0x7A01 / 0xFA01) higher than each respective drive's FH or UL parameters are written, the write will be acknowledged, but the drive will not change its frequency to this invalid value.
- All writes to holding registers use the drive's RAM / EEPROM data write ("W") command except for registers 0x7A00 ~ 0x7A02 and 0xFA00 ~ 0xFA02, which use the drive's RAM data write ("P") command.
- If an attempt is made to access non-existent registers or coils, an ILLEGAL DATA ADDRESS exception (code 02) will be returned.
- If an attempt is made to write invalid data to a register or coil, an ILLEGAL DATA VALUE exception (code 03) will be returned.
- If a drive connected to the MDB-100 goes "offline", all coils will hold their last state, with the exception of read-only coils \#115 and/or \#131, which will indicate "offline". Once communication with the drive(s) is reestablished, these coil(s) will again indicate "online".
- If a drive connected to the MDB-100 goes "offline", attempts to access any register in that drive's register space will return a Modbus "SLAVE FAILURE" error (code 04), except for the following registers, which will hold their last state:

1. Communication command (communication number FA00)
2. Communication frequency command (communication number FA01)
3. Output frequency (communication number FD00)
4. Status (communication number FE01)
5. Output current (communication number FE03)
6. Output voltage (communication number FE05)

- Note that the MDB-100 does not assign drive register and coil functions; they are entirely drive-dependent, and managed by the drive manufacturer.
- Each drive's write-only coils are mapped to the corresponding drive's communication command parameter (communication number \#FA00). Also,
the MDB-100 does not place any restrictions on coils marked as "Reserved". These coils may be written to at any time. The data or function associated with these coils is entirely defined by the connected drive.
- With the exception of coils \#115 and \#131, each drive's read-only coils are mapped to the corresponding drive's communication status register (communication number \#FE01). Also, the MDB-100 does not place any restrictions on coils marked as "Reserved". These coils may be read from at any time. The data or function associated with these coils is entirely defined by the connected drive.


### 10.2 Holding Registers

| Drive | Reg. \# (hex) | Title | Function | Max (hex:dec) | $\begin{gathered} \operatorname{Min} \\ \text { (hex:dec) } \end{gathered}$ | Run Mode Modify |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{aligned} & \hline \mathrm{A} \\ & \mathrm{~B} \end{aligned}$ | $\begin{aligned} & \hline 0000 \\ & 8000 \end{aligned}$ | AUI | Automatic acceleration and deceleration time | 1 | 0 | Yes |
| A | $\begin{aligned} & \hline 0001 \\ & 8001 \end{aligned}$ | AU2 | Automatic torque boost | 3 | 0 | No |
| A | $\begin{aligned} & \hline 0002 \\ & 8002 \\ & \hline \end{aligned}$ | AUB | Automatic environment setting | 2 | 0 | No |
| A | $\begin{aligned} & \hline 0003 \\ & 8003 \end{aligned}$ | CMDD | Command mode selection | 1 | 0 | Yes |
| A | $\begin{aligned} & 0004 \\ & 8004 \end{aligned}$ | FMDD | Frequency setting mode selection | 2 | 0 | Yes |
| $\begin{aligned} & \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & 0005 \\ & 8005 \end{aligned}$ | FMEL | FM terminal function selection | 1 | 0 | Yes |
| A | $\begin{aligned} & 0006 \\ & 8006 \end{aligned}$ | FM | Connected meter adjustment | FF:255 | 0 | Yes |
| A | $\begin{aligned} & 0007 \\ & 8007 \end{aligned}$ | tYP | Standard setting mode selection | 6 | 0 | No |
| A | $\begin{aligned} & \hline 0008 \\ & 8008 \end{aligned}$ | Fr | Forward/reverse selection (panel) | 1 | 0 | Yes |
| A | $\begin{aligned} & 0009 \\ & 8009 \end{aligned}$ | ACC | Acceleration time \#1 (s) | 8CA0:3600 | A:1.0 | Yes |
| A | $\begin{aligned} & 0010 \\ & 8010 \end{aligned}$ | dEC | Deceleration time \#1 (s) | 8CA0:3600 | A:1.0 | Yes |
| $\begin{aligned} & \hline \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & \hline 0011 \\ & 8011 \end{aligned}$ | FH | Maximum frequency | 7D00:320.0 | BB8:30.0 | No |
| A | $\begin{aligned} & 0012 \\ & 8012 \end{aligned}$ | UL | Upper limit frequency | FH | 32:0.5 | Yes |
| A | $\begin{aligned} & \hline 0013 \\ & 8013 \end{aligned}$ | LL | Lower limit frequency | UL | 0:0.0 | Yes |
| $\begin{aligned} & \hline \mathrm{A} \\ & \mathrm{~B} \end{aligned}$ | $\begin{aligned} & \hline 0014 \\ & 8014 \\ & \hline \end{aligned}$ | vL | Base frequency | 7D00:320.0 | 9C4:25.0 | Yes |
| $\begin{aligned} & \hline \mathrm{A} \\ & \mathrm{~B} \end{aligned}$ | $\begin{aligned} & \hline 0015 \\ & 8015 \end{aligned}$ | Pt | V/F pattern | 3 | 0 | No |
| $\begin{aligned} & \hline \mathrm{A} \\ & \mathrm{~B} \end{aligned}$ | $\begin{aligned} & \hline 0016 \\ & 8016 \end{aligned}$ | vb | Voltage boost | BB8:30.0 | 0 | Yes |
| $\begin{aligned} & \hline \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & 0017 \\ & 8017 \end{aligned}$ | OM | OL selection | 7 | 0 | Yes |
| $\begin{aligned} & \hline \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & \hline 0018 \\ & 8018 \end{aligned}$ | Sr 1 | Preset speed 1 | UL | LL | Yes |
| $\begin{aligned} & \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & 0019 \\ & 8019 \\ & \hline \end{aligned}$ | Sr 2 | Preset speed 2 | UL | LL | Yes |
| A | $\begin{aligned} & \hline 0020 \\ & 8020 \\ & \hline \end{aligned}$ | Sr 3 | Preset speed 3 | U | LL | Yes |
| A | $\begin{aligned} & \hline 0021 \\ & 8021 \end{aligned}$ | Sr 4 | Preset speed 4 | UL | LL | Yes |
| A | $\begin{aligned} & \hline 0022 \\ & 8022 \end{aligned}$ | Sr 5 | Preset speed 5 | U | LL | Yes |


| Drive | Reg. \# (hex) | Title | Function | Max (hex:dec) | $\begin{gathered} \text { Min } \\ \text { (hex:dec) } \end{gathered}$ | Run Mode Modify |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A | $\begin{aligned} & \hline 0023 \\ & 8023 \end{aligned}$ | Sr 6 | Preset speed 6 | U | LL | Yes |
| A | $\begin{aligned} & \hline 0024 \\ & 8024 \end{aligned}$ | Sr 7 | Preset speed 7 | UL | LL | Yes |
| A | $\begin{aligned} & 0100 \\ & 8100 \end{aligned}$ | F100 | Low speed signal output frequency | FH | 0:0.0 | Yes |
| A | $\begin{aligned} & 0101 \\ & 8101 \\ & \hline \end{aligned}$ | F101 | Speed reach signal output frequency | FH | 0:0.0 | Yes |
| A | $\begin{aligned} & 0102 \\ & 8102 \end{aligned}$ | F102 | Speed reach detection band | FH | 0:0.0 | Yes |
| A | $\begin{aligned} & 0103 \\ & 8103 \end{aligned}$ | F103 | ST signal selection | 2 | 0 | Yes |
| A | $\begin{aligned} & 0104 \\ & 8104 \end{aligned}$ | F104 | RST signal selection | 1 | 0 | Yes |
| A | $\begin{aligned} & 0110 \\ & 8110 \\ & \hline \end{aligned}$ | F110 | Always active function selection | 25:37 | 0 | Yes |
| A | $\begin{aligned} & 0111 \\ & 8111 \end{aligned}$ | F111 | Input terminal selection \#1 | 25:37 | 0 | Yes |
| A | $\begin{aligned} & 0112 \\ & 8112 \end{aligned}$ | F112 | Input terminal selection \#2 | 25:37 | 0 | Yes |
| A | $\begin{aligned} & 0113 \\ & 8113 \end{aligned}$ | F113 | Input terminal selection \#3 | 25:37 | 0 | Yes |
| A | $\begin{aligned} & 0114 \\ & 8114 \end{aligned}$ | F114 | Input terminal selection \#4 | 25:37 | 0 | Yes |
| A | $\begin{aligned} & 0115 \\ & 8115 \end{aligned}$ | F115 | Input terminal selection \#5 | 25:37 | 0 | Yes |
| A | $\begin{aligned} & 0130 \\ & 8130 \\ & \hline \end{aligned}$ | F130 | Output terminal selection \#1 | 9 | 0 | Yes |
| A | $\begin{aligned} & 0131 \\ & 8131 \end{aligned}$ | F131 | Output terminal selection \#2 | 9 | 0 | Yes |
| A | $\begin{aligned} & 0200 \\ & 8200 \end{aligned}$ | F200 | Frequency priority selection | 1 | 0 | Yes |
| A | $\begin{aligned} & 0201 \\ & 8201 \end{aligned}$ | F201 | VIA reference point \#1 | 64:100 | 0 | Yes |
| A | $\begin{aligned} & \hline 0202 \\ & 8202 \\ & \hline \end{aligned}$ | F202 | VIA point \#1 frequency | 7D00:320.0 | 0:0.0 | Yes |
| A | $\begin{aligned} & 0203 \\ & 8203 \end{aligned}$ | F203 | VIA reference point \#2 | 64:100 | 0 | Yes |
| A | $\begin{array}{r} 0204 \\ 8204 \\ \hline \end{array}$ | F204 | VIA point \#2 frequency | 7D00:320.0 | 0:0.0 | Yes |
| A | $\begin{array}{r} 0210 \\ 8210 \\ \hline \end{array}$ | F210 | VIB reference point \#1 | 64:100 | 0 | Yes |
| A | $\begin{array}{r} 0211 \\ 8211 \\ \hline \end{array}$ | F211 | VIB point \#1 frequency | 7D00:320.0 | 0:0.0 | Yes |
| A | $\begin{aligned} & \hline 0212 \\ & 8212 \\ & \hline \end{aligned}$ | F212 | VIB reference point \#2 | 64:100 | 0 | Yes |
| A | $\begin{array}{r} 0213 \\ 8213 \\ \hline \end{array}$ | F213 | VIB point \#2 frequency | 7D00:320.0 | 0:0.0 | Yes |
| A | 0240 8240 | F240 | Start-up frequency | 3E8:10.0 | 32:0.5 | Yes |


| Drive | Reg. \# (hex) | Title | Function | $\begin{gathered} \text { Max } \\ \text { (hex:dec) } \end{gathered}$ | $\begin{gathered} \operatorname{Min} \\ \text { (hex:dec) } \end{gathered}$ | Run Mode Modify |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\bar{A}$ | $\begin{aligned} & \hline 0241 \\ & 8241 \end{aligned}$ | F241 | Run frequency | FH | 0:0.0 | Yes |
| A | $\begin{aligned} & 0242 \\ & 8242 \end{aligned}$ | F242 | Run frequency hysteresis | FH | 0:0.0 | Yes |
| A | $\begin{array}{r} 0250 \\ 8250 \\ \hline \end{array}$ | F250 | DC injection starting frequency | FH | 0:0.0 | Yes |
| A | $\begin{aligned} & \hline 0251 \\ & 8251 \end{aligned}$ | F251 | DC injection current | 64:100 | 0 | Yes |
| A | $\begin{aligned} & 0252 \\ & 8252 \end{aligned}$ | F252 | DC injection time | C8:20.0 | 0:0.0 | Yes |
| A | $\begin{aligned} & 0260 \\ & 8260 \end{aligned}$ | F260 | Jog run frequency | 7D0:20.0 | 0:0.0 | Yes |
| A | $\begin{aligned} & 0261 \\ & 8261 \end{aligned}$ | F261 | Jog stop control | 2 | 0 | Yes |
| A | $\begin{aligned} & 0270 \\ & 8270 \end{aligned}$ | F270 | Jump frequency \#1 | U | LL | Yes |
| A | $\begin{aligned} & 0271 \\ & 8271 \end{aligned}$ | F271 | Jump frequency band \#1 | BB8:30.0 | 0:0.0 | Yes |
| A | $\begin{aligned} & 0272 \\ & 8272 \end{aligned}$ | F272 | Jump frequency \#2 | UL | LL | Yes |
| A | $\begin{aligned} & \hline 0273 \\ & 8273 \end{aligned}$ | F273 | Jump frequency band \#2 | BB8:30.0 | 0:0.0 | Yes |
| A | $\begin{aligned} & 0274 \\ & 8274 \end{aligned}$ | F274 | Jump frequency \#3 | UL | LL | Yes |
| A | $\begin{aligned} & 0275 \\ & 8275 \end{aligned}$ | F275 | Jump frequency band \#3 | BB8:30.0 | 0:0.0 | Yes |
| A | $\begin{aligned} & 0280 \\ & 8280 \end{aligned}$ | F280 | Preset speed 1 | UL | LL | Yes |
| A | $\begin{aligned} & \hline 0281 \\ & 8281 \end{aligned}$ | F281 | Preset speed 2 | UL | LL | Yes |
| A | $\begin{aligned} & 0282 \\ & 8282 \end{aligned}$ | F282 | Preset speed 3 | UL | LL | Yes |
| A | $\begin{aligned} & 0283 \\ & 8283 \end{aligned}$ | F283 | Preset speed 4 | UL | LL | Yes |
| A | $\begin{aligned} & 0284 \\ & 8784 \end{aligned}$ | F284 | Preset speed 5 | UL | LL | Yes |
| A | $\begin{aligned} & 0285 \\ & 8285 \end{aligned}$ | F285 | Preset speed 6 | U | LL | Yes |
| A | $\begin{aligned} & \hline 0286 \\ & 8286 \end{aligned}$ | F286 | Preset speed 7 | UL | LL | Yes |
| $\begin{aligned} & \hline \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & \hline 0287 \\ & 8287 \end{aligned}$ | F287 | Preset speed 8 | U | LL | Yes |
| A | $\begin{aligned} & \hline 0288 \\ & 8288 \end{aligned}$ | F288 | Preset speed 9 | U | LL | Yes |
| A | $\begin{aligned} & \hline 0289 \\ & 8289 \end{aligned}$ | F289 | Preset speed 10 | UL | LL | Yes |
| A | $\begin{aligned} & \hline 0290 \\ & 8290 \end{aligned}$ | F290 | Preset speed 11 | UL | LL | Yes |
| A | $\begin{aligned} & \hline 0291 \\ & 8291 \\ & \hline \end{aligned}$ | F291 | Preset speed 12 | U | LL | Yes |


| Drive | Reg. \# (hex) | Title | Function | Max (hex:dec) | Min (hex:dec) | Run Mode Modify |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\overline{\mathrm{A}}$ | $\begin{aligned} & \hline 0292 \\ & 8292 \end{aligned}$ | F292 | Preset speed 13 | UL | LL | Yes |
| A | $\begin{aligned} & \hline 0293 \\ & 8293 \end{aligned}$ | F293 | Preset speed 14 | UL | LL | Yes |
| $\begin{aligned} & \hline \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & \hline 0294 \\ & 8294 \end{aligned}$ | F294 | Preset speed 15 | UL | LL | Yes |
| A | $\begin{aligned} & \hline 0300 \\ & 8300 \end{aligned}$ | F300 | PWM carrier frequency (kHz) | * | 16:2.2 | Yes |
| A | $\begin{aligned} & \hline 0301 \\ & 8301 \end{aligned}$ | F301 | Auto-restart (motor speed search) | 3 | 0 | Yes |
| A | $\begin{aligned} & \hline 0302 \\ & 8302 \end{aligned}$ | F302 | Regeneration power ridethrough control | 1 | 0 | Yes |
| A | $\begin{aligned} & \hline 0303 \\ & 8303 \end{aligned}$ | F303 | Retry selection: number of times | A:10 | 0 | Yes |
| $\begin{aligned} & \hline \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & \hline 0304 \\ & 8304 \end{aligned}$ | F304 | Regenerative braking selection | 1 | 0 | Yes |
| $\begin{aligned} & \hline \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & \hline 0305 \\ & 8305 \end{aligned}$ | F305 | Overvoltage stall protection | 1 | 0 | Yes |
| A | $\begin{aligned} & \hline 0306 \\ & 8306 \end{aligned}$ | F306 | Output voltage adjustment | 78:120 | 0 | Yes |
| A | $\begin{aligned} & \hline 0307 \\ & 8307 \end{aligned}$ | F307 | Line voltage compensation | 1 | 0 | No |
| A | $\begin{aligned} & \hline 0400 \\ & 8400 \end{aligned}$ | F400 | Auto-tuning | 2 | 0 | No |
| $\begin{aligned} & \hline \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & \hline 0401 \\ & 8401 \end{aligned}$ | F401 | Slip frequency gain | FF:255 | 0 | Yes |
| A | $\begin{aligned} & \hline 0402 \\ & 8402 \end{aligned}$ | F402 | Motor constant 1: R1 gain | FF:255 | 0 | No |
| A | $\begin{aligned} & \hline 0403 \\ & 8403 \end{aligned}$ | F403 | Motor constant 2: R2 gain | FF:255 | 0 | No |
| A | $\begin{aligned} & \hline 0404 \\ & 8404 \end{aligned}$ | F404 | Motor constant 3: M gain | FF:255 | 0 | No |
| $\begin{aligned} & \hline \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & \hline 0405 \\ & 8405 \end{aligned}$ | F405 | Load inertia | 3 | 0 | Yes |
| A | $\begin{aligned} & \hline 0406 \\ & 8406 \end{aligned}$ | F406 | Auto torque boost no load current | 1E:30 | 0 | Yes |
| A | $\begin{aligned} & 0407 \\ & 8407 \end{aligned}$ | F407 | Auto torque boost maximum boost | 1E:30 | 0 | Yes |
| $\begin{aligned} & \hline \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & \hline 0500 \\ & 8500 \\ & \hline \end{aligned}$ | F500 | Acceleration time \#2 | 8CA0:3600 | 1:0.1 | Yes |
| $\begin{aligned} & \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & \hline 0501 \\ & 8501 \end{aligned}$ | F501 | Deceleration time \#2 | 8CA0:3600 | 1:0.1 | Yes |
| $\begin{aligned} & \hline \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & 0502 \\ & 8502 \end{aligned}$ | F502 | Acceleration \& deceleration pattern \#1 | 2 | 0 | Yes |
| $\begin{aligned} & \hline \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & \hline 0503 \\ & 8503 \end{aligned}$ | F503 | Acceleration \& deceleration pattern \#2 | 2 | 0 | Yes |
| $\begin{aligned} & \hline \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & \hline 0504 \\ & 8504 \\ & \hline \end{aligned}$ | F504 | Acceleration \& deceleration pattern \#1 \& \#2 | 1 | 0 | Yes |


| Drive | Reg. \# (hex) | Title | Function | $\begin{gathered} \text { Max } \\ \text { (hex:dec) } \end{gathered}$ | $\begin{gathered} \text { Min } \\ \text { (hex:dec) } \end{gathered}$ | Run Mode Modify |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{aligned} & \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & 0505 \\ & 8505 \end{aligned}$ | F505 | Acceleration \& deceleration pattern \#1 \& \#2 switching frequency | UL | 0:0.0 | Yes |
| $\begin{aligned} & \hline \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & 0600 \\ & 8600 \end{aligned}$ | F600 | Motor overload protection level | 64:100 | * | Yes |
| $\begin{aligned} & \hline \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & 0601 \\ & 8601 \end{aligned}$ | F601 | Stall protection level | C8:200 | * | Yes |
| $\begin{aligned} & \hline \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & 0602 \\ & 8602 \end{aligned}$ | F602 | Fault trip saving | 1 | 0 | Yes |
| $\begin{aligned} & \mathrm{A} \\ & \mathrm{~B} \end{aligned}$ | $\begin{aligned} & \hline 0603 \\ & 8603 \end{aligned}$ | F603 | Emergency stop selection | 2 | 0 | Yes |
| $\begin{aligned} & \mathrm{A} \\ & \mathrm{~B} \end{aligned}$ | $\begin{aligned} & 0604 \\ & 8604 \end{aligned}$ | F604 | Emergency stop DC injection time | C8:20.0 | 0:0.0 | Yes |
| $\begin{aligned} & \mathrm{A} \\ & \mathrm{~B} \end{aligned}$ | $\begin{aligned} & 0605 \\ & 8605 \end{aligned}$ | F605 | Open phase detection parameter | 2 | 0 | Yes |
| $\begin{aligned} & \text { A } \\ & \text { B } \end{aligned}$ | $\begin{array}{r} 0700 \\ 8700 \\ \hline \end{array}$ | F700 | Parameter setting disable selection | 2 | 0 | Yes |
| $\begin{aligned} & \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & 0701 \\ & 8701 \end{aligned}$ | F701 | Unit selection | 3 | 0 | Yes |
| $\begin{aligned} & \mathrm{A} \\ & \mathrm{~B} \end{aligned}$ | $\begin{aligned} & 0702 \\ & 8702 \end{aligned}$ | F702 | Frequency units multiplication factor | 4E20:200.0 | 64:0.01 | Yes |
| $\begin{aligned} & \hline \mathrm{A} \\ & \mathrm{~B} \end{aligned}$ | $\begin{aligned} & \hline 0800 \\ & 8800 \end{aligned}$ | F800 | Communication speed | 3 | 0 | Yes |
| $\begin{aligned} & \mathrm{A} \\ & \mathrm{~B} \end{aligned}$ | $\begin{aligned} & 0801 \\ & 8801 \end{aligned}$ | F801 | Parity | 2 | 0 | Yes |
| $\begin{aligned} & \mathrm{A} \\ & \mathrm{~B} \end{aligned}$ | $\begin{aligned} & 0802 \\ & 8802 \end{aligned}$ | F802 | Inverter number | 1F:31 | 0 | Yes |
| $\begin{aligned} & \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & 0803 \\ & 8803 \end{aligned}$ | F803 | Communication error trip time | 64:100 | 0 | Yes |
| $\begin{aligned} & \mathrm{A} \\ & \mathrm{~B} \end{aligned}$ | $\begin{aligned} & \text { 7A00 } \\ & \text { FA00 } \end{aligned}$ | FA00 | Communication command | FFFF:65535 | 0 | Yes |
| $\begin{aligned} & \\ & \hline A \\ & B \end{aligned}$ | $\begin{aligned} & \text { 7A01 } \\ & \text { FA01 } \end{aligned}$ | FA01 | Communication frequency command | FH | 0:0.0 | Yes |
| $\begin{aligned} & \hline \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & \hline \text { 7A02 } \\ & \text { FA02 } \end{aligned}$ | FA02 | Panel frequency command | FH | 0:0.0 | Yes |

Status Monitoring Parameters (read-only)

| Drive | Comm \# <br> (hex) | Function |
| :---: | :---: | :---: |
| A | 7 D00 | Current output frequency |
| B | FD00 | Output Frequency (saves trip |
| A | 7 frequency) |  |
| B | FE00 | (saves trip status) |
| A | $7 E 01$ | Status (sand |
| B | FE01 |  |
| A | 7 E02 | Current frequency command |
| B | FE02 |  |
| A | 7 E03 | Output current display |
| B | FE03 |  |
| A | $7 E 04$ | Bus voltage |
| B | FE04 |  |
| A | $7 E 05$ | Output voltage |
| B | FE05 |  |
| A | $7 E 06$ | Input terminal data |
| B | FE06 |  |
| A | $7 E 07$ | Output terminal data |
| B | FE07 |  |
| A | 7 E08 | CPU version |
| B | FE08 |  |
| A | $7 E 09$ | EEPROM version |
| B | FE09 |  |
| A | $7 E 10$ | Past trip 1 |
| B | FE10 |  |
| A | $7 E 11$ | Past trip 2 |
| B | FE11 |  |
| A | $7 E 12$ | Past trip 3 |
| B | FE12 |  |
| A | $7 E 13$ | Past trip 4 |
| B | FE13 |  |
| A | $7 E 14$ | Cumulative run time |
| B | FE14 |  |

## Trip Data Codes

| Display | Data |  |
| :---: | :---: | :--- |
| nEr r | 0000 | No trip |
| OC1 | 0001 | Overcurrent during acceleration |
| OC | 0002 | Overcurrent during deceleration |
| OC3 | 0003 | Overcurrent during constant-speed run |
| OC | 0004 | Load-end short circuit trip during start-up |
| OCA | 0005 | Phase short circuit |
|  | 0006 | reserved |
|  | 0007 | reserved |
| EPH1 | 0008 | reserved |
| EPH0 | 0009 | Loss of output phase |
| OP1 | 000 A | Overvoltage during acceleration |
| OP2 | 000 B | Overvoltage during deceleration |
| OP3 | 000 C | Overvoltage during constant-speed run |
| OL1 | 000 D | Inverter overload trip |
| OL2 | 000 E | Motor overload trip |
| OLr | 000 F | DBR overload |
| OH | 0010 | Overheat trip |
| E | 0011 | Emergency stop |
| EEP1 | 0012 | EEPROM fault: write error |
|  | 0013 | reserved |
|  | 0014 | reserved |
| Err 2 | 0015 | RAM fault |
| Err 3 | 0016 | ROM fault |
| Err 4 | 0017 | CPU fault |
| Err 5 | 0018 | Communication interruption error |
|  | $0019 \sim 0024$ | reserved |
| OCr | 0025 | Dynamic braking resistor trip |
|  | $0026 \sim 0027$ | reserved |
| Et n | 0028 | Auto-tuning error |
| EtYP | 0029 | Inverter typeform error |
|  |  |  |
|  |  |  |

### 10.3 Write-Only Coils

| $\begin{aligned} & \text { 毋 } \\ & \stackrel{\text { D }}{ \pm} \end{aligned}$ | Coil \# | S7 ASD Function | OFF | ON |
| :---: | :---: | :---: | :---: | :---: |
|  | 31 | Command source | Local | Network |
|  | 30 | Frequency command source | Local | Network |
|  | 29 | Fault reset | N/A | Reset |
|  | 28 | Emergency OFF command | N/A | Trip "E" |
|  | 27 | Coast stop command | N/A | Coast stop |
|  | 26 | Run / stop command | Stop | Run |
|  | 25 | Forward / reverse selection | Forward | Reverse |
|  | 24 | Jog command | N/A | Jog |
|  | 23 | DC injection braking | N/A | DC injection command |
|  | 22 | Accel / decel \#1/\#2 selection | \#1 | \#2 |
|  | 21 | Reserved | -- | -- |
|  | 20 | Reserved | -- | -- |
|  | 19 | Preset speed 4 | OFF | ON |
|  | 18 | Preset speed 3 | OFF | ON |
|  | 17 | Preset speed 2 | OFF | ON |
|  | 16 | Preset speed 1 | OFF | ON |
| $\begin{aligned} & \text { 【 } \\ & \stackrel{\text { D }}{1} \end{aligned}$ | 15 | Command source | Local | Network |
|  | 14 | Frequency command source | Local | Network |
|  | 13 | Fault reset | N/A | Reset |
|  | 12 | Emergency OFF command | N/A | Trip "E" |
|  | 11 | Coast stop command | N/A | Coast stop |
|  | 10 | Run / stop command | Stop | Run |
|  | 9 | Forward / reverse selection | Forward | Reverse |
|  | 8 | Jog command | N/A | Jog |
|  | 7 | DC injection braking | N/A | DC injection command |
|  | 6 | Accel / decel \#1/\#2 selection | \#1 | \#2 |
|  | 5 | Reserved | -- | -- |
|  | 4 | Reserved | -- | -- |
|  | 3 | Preset speed 4 | OFF | ON |
|  | 2 | Preset speed 3 | OFF | ON |
|  | 1 | Preset speed 2 | OFF | ON |
|  | 0 | Preset speed 1 | OFF | ON |

## 10．4 Read－Only Coils

| $\begin{aligned} & \text { 毋 } \\ & \stackrel{\text { ® }}{亠 1} \end{aligned}$ | Coil \＃ | S7 ASD Function | OFF | ON |
| :---: | :---: | :---: | :---: | :---: |
|  | 131 | Drive B online／offline status | Offline | Online |
|  | 130 | Reserved | Always＂ 0 ＂ |  |
|  | 129 | Reserved | Always＂0＂ |  |
|  | 128 | Reserved | Always＂0＂ |  |
|  | 127 | Reserved | Always＂0＂ |  |
|  | 126 | Run／stop status | Stopped | Running |
|  | 125 | Forward／reverse status | Forward | Reverse |
|  | 124 | Jog status | Not jogging | Jogging |
|  | 123 | DC injection braking status | Not DC injection braking | DC injection braking |
|  | 122 | Accel／decel \＃1／\＃2 status | \＃1 | \＃2 |
|  | 121 | Reserved | Always＂0＂ |  |
|  | 120 | Reserved | Always＂ 0 ＂ |  |
|  | 119 | Reserved | Always＂ 0 ＂ |  |
|  | 118 | Reserved | Always＂ 0 ＂ |  |
|  | 117 | Reserved | Always＂0＂ |  |
|  | 116 | Reserved | Always＂ 0 ＂ |  |
| $\begin{aligned} & \mathbb{Q} \\ & \stackrel{y}{亠 幺} \end{aligned}$ | 115 | Drive A online／offline status | Offline | Online |
|  | 114 | Reserved | Always＂0＂ |  |
|  | 113 | Reserved | Always＂0＂ |  |
|  | 112 | Reserved | Always＂0＂ |  |
|  | 111 | Reserved | Always＂ 0 ＂ |  |
|  | 110 | Run／stop status | Stopped | Running |
|  | 109 | Forward／reverse status | Forward | Reverse |
|  | 108 | Jog status | Not jogging | Jogging |
|  | 107 | DC injection braking status | Not DC injection braking | DC injection braking |
|  | 106 | Accel／decel \＃1／\＃2 status | \＃1 | \＃2 |
|  | 105 | Reserved | Always＂0＂ |  |
|  | 104 | Reserved | Always＂0＂ |  |
|  | 103 | Reserved | Always＂0＂ |  |
|  | 102 | Reserved | Always＂ 0 ＂ |  |
|  | 101 | Reserved | Always＂ 0 ＂ |  |
|  | 100 | Reserved | Always＂0＂ |  |

11. Notes
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