AW9D-MAX AW9D

#### **Motherboard** Socket 775

Intel Core 2 Duo (Extreme Edition) Intel Pentium Extreme Edition Intel Pentium D Intel Pentium 4

### **User's Manual**



Introduction

Hardware Setup

#### About this Manual:

This user's manual contains all the information you may need for setting up this motherboard. To read the user's manual of PDF format (readable by Adobe Reader), place the "Driver & Utility CD" into the CD-ROM drive in your system. The auto-run screen will appear, click the "Manual" tab to enter its submenu. If not, browse the root directory of the CD-ROM via the File Manager, and double click the "AUTORUN" file.

- LGA775 ATX Motherboard Intel 975X / ICH7R 1066MHz FSB Dual DDR2 800 Dual PCI-E X16 Graphics Slots Dual GbE LAN (AW9D-MAX) 8x SATA 3Gb/s (AW9D-MAX)
- **IEEE 1394** (AW9D-MAX)
- HD 7.1 Audio
- □ Silent OTES<sup>™</sup> Technology

#### □ uGuru<sup>™</sup> Technology

Appendix

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#### AW9D-MAX, AW9D

User's Manual English, 1<sup>st</sup> Edition August, 2006

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If you do not properly set the motherboard settings, causing the motherboard to malfunction or fail, we cannot guarantee any responsibility.

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**BIOS Setup** 

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# 1. Introduction

#### **1.1 Features & Specifications**

#### CPU

- Designed for Intel Pentium 4 LGA775 processor with 1066/800 MHz FSB
- Supports Intel Core 2 Duo(Extreme Edition), Pentium Extreme Edition, Pentium D, & Pentium 4 Processors
- Supports Enhanced Intel Speedstep Technology (EIST)
- Supports Intel Extended Memory 64 Technology (EM64T)
- Supports Intel Virtualization Technology
- Supports Intel Execute Disable Bit capability
- Supports Intel Hyper-Threading Technology

#### Chipset

• Intel 975X / Intel ICH7R

#### Memory

- Four 240-pin DIMM slots
- Supports Dual Channel DDR2 800/667 Un-buffered Non-ECC memory
- Supports maximum memory capacity up to 8GB

#### Graphics

• Supports Dual PCI-Express X16 slots (Dual ATI CrossFire VGA cards)

#### LAN

- Onboard dual PCI-E Gigabit LAN controllers support 10/100/1000Mb Ethernet (For model "AW9D-MAX" only)
- Onboard PCI-E Gigabit LAN controller supports 10/100/1000Mb Ethernet (For model "AW9D" only)

#### Audio

- ABIT AudioMAX HD 7.1 CH
- Supports auto jack sensing and optical S/PDIF In/Out
- Dolby Master Studio Certificated

#### **Expansion Slots**

- 2x PCI-E X16 slots
- 2x PCI-E X1 slots
- 1x PCI slot
- 1x AudioMAX slot

#### Internal I/O Connectors

- 1x Floppy port
- 1x ATA 100/66/33 IDE connector

- 7x SATA connectors (For model "AW9D-MAX" only)
- 4x SATA connectors (For model "AW9D" only)
- 2x USB 2.0 headers
- 2x IEEE1394 headers

#### Rear Panel I/O

- 1x PS/2 Keyboard connector
- 1x PS/2 Mouse connector
- 1x eSATA connector (For model "AW9D-MAX" only)
- 4x USB 2.0 connectors
- 2x RJ-45 Gigabit LAN connectors (For model "AW9D-MAX" only)
- 1x RJ-45 Gigabit LAN connector (For model "AW9D" only)

#### Serial ATA

- Connector "SATA1" ~ "SATA4":
  - 4x SATA 3Gb/s offered by Intel ICH7R supports RAID 0/1/0+1/5.
  - Supports SATA AHCI, providing native command queuing and native hot plug.
- Connector "SATA5" and "SATA6": (For model "AW9D-MAX" only)
  - 2x SATA 3Gb/s offered by Silicon Image 3132 supports RAID 0/1
- Connector "SATA7" and "eSATA1": (For model "AW9D-MAX" only)
  - 2x SATA 3Gb/s offered by Silicon Image 3132 supports RAID 0/1

#### IEEE 1394 (For model "AW9D-MAX" only)

• Supports 2 Ports IEEE 1394 at 400Mb/s transfer rate

#### **ABIT Engineered**

- ABIT uGuru<sup>™</sup> Technology
- ABIT Silent OTES<sup>™</sup> Technology
- ABIT AudioMAX Technology

#### Miscellaneous

- ATX form factor (305mm x 245mm)
- **%** Specifications and information contained herein are subject to change without notice.

Introduction

#### **1.2 Motherboard Layout**

#### 1.2.1 AW9D-MAX



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#### 1.2.2 AW9D



## 2. Hardware Setup

In this chapter we will elaborate all the information you need upon installing this motherboard to your computer system.

Always power off the computer and unplug the AC power cord before adding or removing any peripheral or component. Failing to so may cause severe damage to your motherboard and/or peripherals. Plug in the AC power cord only after you have carefully checked everything.

The motherboard and its component layouts illustrated in this chapter were based mainly on model "AW9D-MAX", unless specifically stated.

#### 2.1 Choosing a Computer Chassis

- This motherboard carries an ATX form factor of 305 x 245 mm. Choose a chassis big enough to install this motherboard.
- As some features for this motherboard are implemented by cabling connectors on the motherboard to indicators and switches or buttons on the chassis, make sure your chassis supports all the features required.
- If there is possibility of adopting some more hard drives, make sure your chassis has sufficient power and space for them.
- Most chassis have alternatives for I/O shield located at the rear panel. Make sure the I/O shield of the chassis matches the I/O port configuration of this motherboard. You can find an I/O shield specifically designed for this motherboard in its package.

#### 2.2 Installing Motherboard

Most computer chassis have a base with many mounting holes to allow the motherboard to be securely attached, and at the same time, prevent the system from short circuits. There are two ways to attach the motherboard to the chassis base:

- 1. With studs,
- 2. Or with spacers

In principle, the best way to attach the board is with use studs. Only if you are unable to do this should you attach the board with



spacers. Line up the holes on the board with the mounting holes on the chassis. If the holes line up and there are screw holes, you can attach the board with studs. If the holes line up and there are only slots, you can only attach with spacers. Take the tip of the spacers and insert them into the slots. After doing this to all the slots, you can slide the board into position aligned with slots. After the board has been positioned, check to make sure everything is OK before putting the chassis back on.

#### To install this motherboard:

- 1. Locate all the screw holes on the motherboard and the chassis base.
- 2. Place all the studs or spacers needed on the chassis base and have them tightened.
- 3. Face the motherboard's I/O ports toward the chassis's rear panel.
- 4. Line up all the motherboard's screw holes with those studs or spacers on the chassis.
- 5. Install the motherboard with screws and have them tightened.



**\*\*** To prevent shorting the PCB circuit, please REMOVE the metal studs or spacers if they are already fastened on the chassis base and are without mounting-holes on the motherboard to align with.

#### 2.3 Checking Jumper Settings

For a 2-pin jumper, plug the jumper cap on both pins will make it CLOSE (SHORT). Remove the jumper cap, or plug it on either pin (reserved for future use) will leave it at OPEN position.







For 3-pin jumper, pin  $1\sim 2$  or pin  $2\sim 3$  can be shorted by plugging the jumper cap in.



# Hardware Setup

#### 2.3.1 CMOS Memory Clearing Header and Backup Battery

The time to clear the CMOS memory occurs when (a) the CMOS data becomes corrupted, (b) you forgot the supervisor or user password preset in the BIOS menu, (c) you are unable to boot-up the system because the CPU ratio/clock was incorrectly set in the BIOS menu, or (d) whenever there is modification on the CPU or memory modules.

This header uses a jumper cap to clear the CMOS memory and have it reconfigured to the default values stored in BIOS.

- Pins 1 and 2 shorted (Default): Normal operation.
- Pins 2 and 3 shorted: Clear CMOS memory.

#### To clear the CMOS memory and load in the default values:

- 1. Power off the system.
- 2. Set pin 2 and pin 3 shorted by the jumper cap. Wait for a few seconds. Set the jumper cap back to its default settings --- pin 1 and pin 2 shorted.
- 3. Power on the system.
- 4. For incorrect CPU ratio/clock settings in the BIOS, press <Del> key to enter the BIOS setup menu right after powering on system.
- 5. Set the CPU operating speed back to its default or an appropriate value.
- 6. Save and exit the BIOS setup menu.



#### CMOS Backup Battery:

An onboard battery saves the CMOS memory to keep the BIOS information stays on even after disconnected your system with power source. Nevertheless, this backup battery exhausts after some five years. Once the error message like **"CMOS BATTERY HAS FAILED**" or **"CMOS checksum error**" displays on monitor, this backup battery is no longer functional and has to be renewed.



#### To renew the backup battery:

- 1. Power off the system and disconnect with AC power source.
- 2. Remove the exhausted battery.
- 3. Insert a new CR2032 or equivalent battery. Pay attention to its polarity. The "+" side is its positive polarity.
- 4. Connect AC power source and power on the system.
- 5. Enter the BIOS setup menu. Reconfigure the setup parameters if necessary.

#### CAUTION:

- **%** Danger of explosion may arise if the battery is incorrectly renewed.
- **※** Renew only with the same or equivalent type recommended by the battery manufacturer.
- **※** Dispose of used batteries according to the battery manufacturer's instructions.

#### 2.3.2 Wake-up Header

These headers use a jumper cap to enable/disable the wake-up function.

#### • USB-PWR1:

Pin 1-2 shorted (Default): Disable wake-up function support at USB1 port. Pin 2-3 shorted: Enable wake-up function support at USB1 port.

#### • USB-PWR2:

Pin 1-2 shorted (Default): Disable wake-up function support at USB2 port. Pin 2-3 shorted: Enable wake-up function support at USB2 port



#### 2.4 Connecting Chassis Components

#### 2.4.1 ATX Power Connectors

These connectors provide the connection from an ATX power supply. As the plugs from the power supply fit in only one orientation, find the correct one and push firmly down into these connectors.



#### ATXPWR1: ATX 24-Pin Power Connector

The power supply with 20-pin or 24-pin cables can both be connected to this 24-pin connector. Connect from pin-1 for either type. However, a 20-pin power supply may cause the system unstable or even unbootable for the sake of insufficient electricity. A minimum power of 300W or higher is recommended.

#### ATX12V1: ATX 12V 8-Pin Power Connector

This connector supplies power to CPU. The system will not start without connecting power to this one.

#### ATX4P1: Auxiliary 12V Power Connector

This connector provides an auxiliary power source for devices added on PCI Express slots.

#### 2.4.2 Front Panel Switches & Indicators Headers

This header is used for connecting switches and LED indicators on the chassis front panel.

Watch the power LED pin position and orientation. The mark "+" align to the pin in the figure below stands for positive polarity for the LED connection. Please pay attention when connecting these headers. A wrong orientation will only cause the LED not lighting, but a wrong connection of the switches could cause system malfunction.



- HLED (Pin 1, 3): Connects to the HDD LED cable of chassis front panel.
- RST (Pin 5, 7):

Connects to the Reset Switch cable of chassis front panel.

• SPKR (Pin 13, 15, 17, 19):

Connects to the System Speaker cable of chassis.

- SLED (Pin 2, 4): Connects to the Suspend LED cable (if there is one) of chassis front panel.
- **PWR (Pin 6, 8):** Connects to the Power Switch cable of chassis front panel.
- PLED (Pin 16, 18, 20): Connects to the Power LED cable of chassis front panel.

#### 2.4.3 FAN Power Connectors

These connectors each provide power to the cooling fans installed in your system.

- CPUFAN1: CPU Fan Power Connector
- NBFAN1: Chipset Fan Power Connector
- SYSFAN1: System Fan Power Connector
- AUXFAN1~3, OTESFAN1~2: Auxiliary Fan Power Connector
- **\*** These fan connectors are not jumpers. DO NOT place jumper caps on these connectors.



#### 2.5 Installing Hardware

- DO NOT scratch the motherboard when installing hardware. An accidentally scratch of a tiny surface-mount component may seriously damage the motherboard.
- **%** In order to protect the contact pins, please pay attention to these notices:
  - 1. A maximum 20 cycles of CPU installation is recommended.
  - 2. Never touch the contact pins with fingers or any object.
  - 3. Always put on the cap when the CPU is not in use.

#### 2.5.1 CPU Socket 775

- 1. Place the board so as to let the lever-hook of the socket is on your left side. Use your left thumb and forefinger to hold the lever hook, pull it away from the retention tab. Rotate the lever to fully open position.
- 2. Use your right thumb on the bottom-right side of the load plate and lift it up to fully open position.
- Use your right thumb and forefinger to grasp the CPU package. Be sure to grasp on the edge of the substrate, and face the Pin-1 indicator toward the bottom-left side. Aim at the socket and place the CPU package vertical down into the socket.



4. Visually inspect if the CPU is seated well into the socket. The alignment key must be located in the notch of package.

- 5. Use your left hand to hold the load plate, and use your right thumb to peel the cap off.
- **%** The cap plays an important role in protecting contact pins. In order to prevent bent pin, PUT ON the cap after operation or testing.
- 6. Lower the plate onto the CPU package. Engage the load lever while gently pressing down the load plate.

7. Secure the lever with the hook under retention tab.









8. Place the heatsink and fan assembly onto the socket. Align the four fasteners toward the four mounting holes on the motherboard.

9. Press each of the four fasteners down into the mounting holes. Rotate the fastener clock-wise to lock the heatsink and fan assembly into position.

10. Attach the four-pin power plug from the heatsink and fan assembly to the CPU FAN connector.

- The installation procedures vary with different types of CPU fan-and-heatsink assembly. The one shown here is served for demo only. For detailed information on how to install the one you bought, refer to its installation guidelines.
- **\*** A higher fan speed will be helpful for better airflow and heat-dissipation. Nevertheless, stay alert to touch any heatsink since the high temperature generated by the working system is still possible.

#### 2.5.2 DDR2 Memory Slots

This motherboard provides four 240-pin DIMM slots for Dual Channel DDR2 800/667 memory modules with memory expansion size up to 8GB.



- To reach the optimum performance in dual-channel configurations, install identical DDR2 DIMM pairs for each channel.
- Install DIMMs with the same CAS latency. To reach the optimum compatibility, obtain memory modules from the same vendor.
- Due to chipset resource allocation, the system may detect less than 4GB of system memory in the installation of four 1GB DDR2 memory modules.

There are several methods of different DDR2 configurations depending on how the DIMMs are populated on each system memory channel:

Method	Channel A		Channel B		
metriou	DIMM1	DIMM2	DIMM3	DIMM4	
1	512MB	-	-	-	
2	-	512MB	-	-	
3	-	-	512MB	-	
4	-	-	-	512MB	
5	512MB	512MB	-	-	
6	-	-	512MB	512MB	

• [Single Channel]: only one channel is populated.

Hardware Setup

• **[Dual Channel Asymmetric]:** both channels are populated, but each channel has a different amount of total memory. (Channel A≠Channel B)

Mathad	Channel A		Channel B		
Method	DIMM1	DIMM2	DIMM3	DIMM4	
1	512MB	-	256MB	-	
2	-	256MB	-	512MB	
3	512MB	-	-	256MB	
4	-	256MB	512MB	-	
5	256MB	256MB	256MB	-	
6	256MB	256MB	-	256MB	
7	256MB	-	256MB	256MB	
8	-	256MB	256MB	256MB	
9	256MB	256MB	512MB	512MB	
10	256MB	256MB	256MB	512MB	

• [Dual Channel Symmetric]: both channels are populated where each channel has the same amount of total memory. (Channel A=Channel B)

Mathed	Chan	nel A	Chan	nel B
Method	DIMM1	DIMM2	DIMM3	DIMM4
1	512MB	-	512MB	-
2	-	512MB	-	512MB
3	512MB	-	-	512MB
4	-	512MB	512MB	-
5	256MB	256MB	512MB	-
6	256MB	256MB	-	512MB
7	512MB	-	256MB	256MB
8	-	512MB	256MB	256MB
9	512MB	256MB	512MB	256MB
10	256MB	512MB	256MB	512MB

#### To install system memory:

- 1. Power off the computer and unplug the AC power cord before installing or removing memory modules.
- 2. Locate the DIMM slot on the board.
- Hold two edges of the DIMM module carefully, keep away from touching its connectors.
- 4. Align the notch key on the module with the rib on the slot.
- 5. Firmly press the module into the slots until the ejector tabs at both sides of the slot automatically snap into the mounting notch. Do not force the DIMM module in with extra force as the DIMM module only



with extra force as the DIMM module only fits in one direction.

- 6. To remove the DIMM modules, push the two ejector tabs on the slot outward simultaneously, and then pull out the DIMM module.
- **\*\*** Static electricity can damage the electronic components of the computer or optional boards. Before starting these procedures, ensure that you are discharged of static electricity by touching a grounded metal object briefly.

#### 2.5.3 PCI Express X16 Add-on Slots (Install Graphics Card)

These slots support the connections of graphics cards that comply with PCI Express specifications. This motherboard provides dual PCI-Express X16 slots for one or two graphics cards installation:

#### One PCIE graphics card installation (Normal Mode):

Insert your PCIE graphics card into [PCIEXP1] or [PCIEXP2] slot.

One PCIE graphics card installation on the [PCIEXP2] slot supports the speed up to x8 only.



#### Two PCIE graphics cards installation (CrossFire Mode):

Install one CrossFire<sup> $\mathbb{M}$ </sup> Edition graphics card into [Master] slot (the **PCIEXP1** slot on this motherboard), and one CrossFire<sup> $\mathbb{M}$ </sup> Compatible graphics card into [Slave] slot (the **PCIEXP2** slot on this motherboard).

※ The ATI CrossFire<sup>™</sup> technology currently supports the Microsoft Windows XP with Service Pack 2 only.



To enable CrossFire Mode, you will need to:

- Prepare one CrossFire<sup>™</sup> Edition graphics card and one CrossFire<sup>™</sup> Compatible graphics card.
- Make sure the graphics card driver supports the ATI CrossFire<sup>™</sup> technology. Download the latest driver from ATI website (<u>http://www.ati.com</u>).
- Make sure your power supply unit is sufficient to provide the minimum power required.
- Insert the CrossFire<sup>™</sup> Edition graphics card [Master Card] into the [PCIEXP1] master slot on motherboard.
- Insert the CrossFire<sup>™</sup> compatible PCI Express graphics card (Slave Card) into the [PCIEXP2] slave slot.
- 3. There are 3 connectors on the DMS-59<sup>™</sup>-to-interconnect cable:

Connector 1: [DMS-59<sup>™</sup>] Male connector

Connector 2: [DVI-I] Female connector

Connector 3: [DVI-I] Male connector

- Connect the DMS-59<sup>™</sup> Male plug to the DMS-59<sup>™</sup> connector on Master Card.
- 5. Connect the DVI-I Male-end to the DVI-I connector on Salve Card.
- Connect the video output to your monitor from the remaining DVI-I Female-end.
  - board in this illustration is served for demonstration only, may not be
- \* The motherboard in this illustration is served for demonstration only, may not be the same type or model as the one described in this user's manual.



#### 2.5.4 AudioMAX Connection Slot

This slot provides the audio input/output connection over the rear I/O part through an add-on daughter-card. Find your "**AudioMAX**" daughter-card and its driver in the motherboard package.



- **S/PDIF Out:** This connector provides an S/PDIF-Out connection through optical fiber to digital multimedia devices.
- S/PDIF In: This connector provides an S/PDIF-In connection through optical fiber from digital multimedia devices.
  Line-In: Connects to the line out from external audio sources.
- Mic-In: Connects to the plug from external microphone.
- Line-Out: Connects to the front left and front right channel in the 7.1-channel or regular 2-channel audio system.
- **Cen/Sub:** Connects to the center and subwoofer channel in the 7.1-channel audio system.
- R.L./R.R. (Rear Left / Rear Right): Connects to the rear left and rear right channel in the 7.1-channel audio system.
- S.L./S.R. (Surround Left / Surround Right): Connects to the surround left and surround right channel in the 7.1-channel audio system.
- CD1: This connector connects to the audio output of internal CD-ROM drive or add-on card.



# Hardware Setup

#### S/PDIF Connection:

In the motherboard package you can find one audio daughter-card and one optical-fiber cable.



#### Front Panel Audio Connection Header:

This header provides the front panel connection for HD (High Definition) Audio, yet for AC'97 Audio CODEC connection, you must carefully check the pin assignment before connecting from the front panel module. An incorrect connection may cause malfunction or even damage the motherboard.

% Please do not connect the "Ground" cable or "USB VCC" cable from the front panel module to the Pin 4 "AVCC" of this header.

	Pin	Pin Assignment (HD AUDIO)	Pin	Pin Assignment (AC'97 AUDIO)
	1	MIC2 L	1	MIC In
97531	2	AGND	2	GND
	3	MIC2 R	3	MIC Power
└┱┘╼╁╤┧╤┧	4	AVCC	4	NC
10 642	5	FRO-R	5	Line Out (R)
FP-AUDIO1	6	MIC2_JD	6	NC
	7	F_IO_SEN	7	NC
	9	FRO-L	9	Line Out (L)
	10	LINE2_JD	10	NC

#### Driver Configuration for AC'97 audio connection:

The audio driver is originally configured to support HD Audio. For AC'97 audio connection, you may:

1. Right-click the "Realtek HD Audio Manager" icon 🗿 in system tray.



2. Click "Audio I/O" tab, and then click "Connector Settings".



# 3. Click "**Disabled front panel jack detection**", and then click "OK" to confirm.



#### 2.6 Connecting Peripheral Devices

#### 2.6.1 Floppy and IDE Disk Drive Connectors



The FDC1 connector connects up to two floppy drives with a 34-wire, 2-connector floppy cable. Connect the single end at the longer length of ribbon cable to the FDC1 on the board, the two connectors on the other end to the floppy disk drives connector. Generally you need only one floppy disk drive in your system.

# **\*** The red line on the ribbon cable must be aligned with pin-1 on both the FDC1 port and the floppy connector.

Each of the IDE port connects up to two IDE drives at Ultra ATA/100 mode by one 40-pin, 80-conductor, and 3-connector Ultra ATA/66 ribbon cables.

Connect the single end (blue connector) at the longer length of ribbon cable to the IDE port of this board, the other two ends (gray and black connector) at the shorter length of the ribbon cable to the connectors of your hard drives.



Make sure to configure the "Master" and "Slave" relation before connecting two drives by one single ribbon cable. The red line on the ribbon cable must be aligned with pin-1 on both the IDE port and the hard-drive connector.

#### 2.6.2 Serial ATA Connectors

Each SATA connector serves as one single channel to connect one SATA device by SATA cable.

The disk array for RAID configuration is also provided:

- SATA1~SATA4: Available for RAID 0, RAID 1, RAID 0+1, or RAID 5 configuration.
- SATA5~SATA6: Available for RAID 0 or RAID 1 configuration. (For model "AW9D-MAX" only)
- SATA7, eSATA1: Available for RAID 0 or RAID 1 configuration. (For model "AW9D-MAX" only)





#### To connect SATA device:

- 1. Attach either end of the signal cable to the SATA connector on motherboard. Attach the other end to SATA device.
- 2. Attach the SATA power cable to the SATA device and connect the other end from the power supply.



Each header supports 2x additional USB 2.0 ports by connecting bracket or cable to the rear I/O panel or the front-mounted USB ports of your chassis.



**%** Make sure the connecting cable bears the same pin assignment.

#### 2.6.4 Additional IEEE1394 Port Header

Each header supports 1x additional IEEE1394 port by connecting bracket or cable to the rear I/O panel or the front-mounted IEEE1394 port of your chassis. (For model "**AW9D-MAX**" only)



Pin Assignment	Pin	Pin Assignment
TPA0 +	2	TPA0 -
Ground	4	Ground
TPB0 +	6	TPB0 -
+12V	8	+12V
	10	Ground
	Pin Assignment TPA0 + Ground TPB0 + +12V	Pin Assignment     Pin       TPA0 +     2       Ground     4       TPB0 +     6       +12V     8       10     10

**%** Make sure the connecting cable bears the same pin assignment.

Hardware Setup

#### 2.6.5 PCI Express X1 Add-on Slots

These slots provide the connection of add-on cards that comply with PCI Express specifications.



#### 2.6.6 PCI Add-on Slot

This motherboard remains one standard PCI slot for the add-on card of PCI compliant.



#### 2.6.7 GURU Panel Connection Header

This header is reserved for connecting ABIT's exclusive GURU Panel. For more information, please refer to the included GURU Panel Installation Guide.



AW9D-MAX, AW9D

pin-1

#### 2.7 Onboard Indicators and Buttons

#### 2.7.1 POST Code Displayer

This is an LED device to display the "**POST**" Code, the acronym for **P**ower **O**n **S**elf **T**est. The computer will execute the POST action whenever you power on the computer. The POST process is controlled by the BIOS. It is used to detect the status of the computer's main components and peripherals. Each POST Code corresponds to different checkpoints that are also defined by the BIOS in advance. For example, "memory presence test" is an important checkpoint and its POST Code is "C1". When the BIOS execute any POST item, it will write the corresponding POST Code into the address 80h. If the POST passes, the BIOS will process the next POST item and write the next POST Code into the address 80h. If the POST fails, we can check the POST Code in address 80h to find out where the problem lies.

This LED device also displays the "POST" Code of AC2005, an "uGuru" chipset developed exclusively by Universal ABIT.



#### **※** The decimal point lights up during the AC2005 POST action.

See Appendix for both AWARD and AC2005 POST Code definitions.

#### 2.7.2 Power Source Indicators

These indicators work as a reminding device to display the power status of this motherboard with power source connected.

- **5VSB:** This LED lights up when the power supply is connected with power source.
- VCC: This LED lights up when the system power is on.



※ You may find several LEDs located at the bottom side of the motherboard. Refer to item "LED Effect Control" in the BIOS Setup Menu if you want to select the displaying mode. (For model "AW9D-MAX" only)

#### 2.7.3 Onboard Buttons

These buttons provide onboard power-on and reset control.

- **POWER ON:** Push this button to power on the system.
- **RESET:** Push this button to reset the system.



#### 2.8 Connecting Rear Panel I/O Devices

The rear I/O part of this motherboard provides the following I/O ports:



- Silent OTES: The Silent OTES (Silent Outside Thermal Exhaust System) is a device . specifically designed to silently cool the motherboard's North Bridge chipset. (Keep the area for outgoing heat wave open.)
- eSATA1: This connector supports the external SATA connection. (For model "AW9D-MAX" only)
- Mouse: Connects to PS/2 mouse.
- Keyboard: Connects to PS/2 keyboard. .
- LAN1: Connects to Local Area Network.
- LAN2: Connects to Local Area Network. (For model "AW9D-MAX" only) .
- USB1/USB2: Connects to USB devices such as scanner, digital speakers, monitor, mouse, . keyboard, hub, digital camera, joystick etc.

AW	'9D-	·MAX,	AW	'9D
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# 3. BIOS Setup

This motherboard provides a programmable EEPROM so that you can update the BIOS utility. The BIOS (Basic Input/Output System) is a program that deals with the basic level of communication between processor and peripherals. Use the BIOS Setup program only when installing motherboard, reconfiguring system, or prompted to "Run Setup". This chapter explains the Setup Utility of BIOS utility.

After powering up the system, the BIOS message appears on the screen, the memory count begins, and then the following message appears on the screen:

#### PRESS DEL TO ENTER SETUP

If this message disappears before you respond, restart the system by pressing <Ctrl> + <Alt> + <Del> keys, or by pressing the Reset button on computer chassis. Only when these two methods fair should you restart the system by powering it off and then back on.

After pressing <Del> key, the main menu screen appears.

Phoenix - AwardBIOS CMOS Setup Utility				
▶ uGuru Utility	PnP/PCI Configurations			
Standard CMOS Features	Load Fail-Safe Defaults			
Advanced BIOS Features	Load Optimized Defaults			
► Advanced Chipset Features	Set Password			
▶ Integrated Peripherals	Save & Exit Setup			
Power Management Setup	Exit Without Saving			
Esc: Quit       ↑↓→←       : Select Item         F10: Save & Exit Setup       (i975-W627DHG-6A79IA1CC-00)         F6 : Save PROFILE To BIOS       F7 : Load PROFILE From BIOS				
Change CPU's Clock & Voltage				

In order to increase system stability and performance, our engineering staff is constantly improving the BIOS menu. The BIOS setup screens and descriptions illustrated in this manual are for your reference only, and may not completely match with what you see on your screen.

# 3.1 µGuru<sup>™</sup> Utility

There are two setup menus in this  $\mu$ Guru utility. You may switch between these two by clicking the left or right arrow key on keyboard:

# 3.1.1 OC Guru

µGuru Utility v1.00C			
OC Guru			
Genuine Intel(R) CPU 3.40GHz Frequency : 3468MHz		Item Help ►	
CPU Operating Speed X - External Clock X - Multiplier Factor X - Estimated New CPU Clock X - N/B Strap CPU As X - DRAM Spec. (CPU:DRAM) X - Estimated New DRAM Clock	3400 (200) 204MHz X 17 3468MHz By CPU By SPD 272MHz		
Voltages Control X - CPU Core Voltage X - DDR SDRAM Voltage X - MCH & PCIe 1.5V Voltage Power Cycle Statistics	Auto Detect 1.3000V 1.80 V 1.50 V Press Enter		
↑↓→←:Move Enter:Select +/-/PU/P	D:Value F8:OC On The Fl	y F10:Save ESC:Exit	

#### Genuine Intel(R) CPU

This item displays the CPU model name installed on this motherboard.

#### Frequency

This item displays the processor speed of the CPU installed on this motherboard.

#### **CPU Operating Speed**

This item displays the CPU operating speed according to the type and speed of your CPU. You can also select the [User Define] option to enter the manual option.

#### **User Define:**

- The wrong settings of the multiplier and external clock in certain circumstances may cause CPU damage. Setting the working frequency higher than the PCI chipset or processor specs, may cause abnormal memory module functioning, system hangs, hard disk drive data lose, abnormal functioning of the VGA card, or abnormal functioning with other add-on cards. Using non-specification settings for your CPU is not the intention of this explanation. These should be used for engineering testing, not for normal applications.
- **\*** There will be no guaranty for the settings beyond specification. Any damage of any component on this motherboard or peripherals resulting therein is not our responsibility.

#### - External Clock

This item selects the external clock frequency. Due to the specification limit of the CPU you installed, the speed you set over its standard bus speed is supported, but not guaranteed.

#### - Multiplier Factor

This item displays the multiplier factor for the CPU you installed.

#### - Estimated New CPU Clock

This item displays an estimated CPU processor speed.

#### - N/B Strap CPU As

This item sets the external hardware reset strap assigned to MCH (Memory Controller Hub).

#### - DRAM Spec. (CPU:DRAM)

This item determines the DRAM frequency.

#### - Estimated New DRAM Clock

This item displays an estimated DRAM speed.

#### Voltages Control

This option allows you to switch between the default and user-defined voltages. Leave this setting at default unless the current voltage setting cannot be detected or is not correct. The option "**User Define**" enables you to select the following voltages manually.

- CPU Core Voltage
- DDR SDRAM Voltage
- MCH & PCIE 1.5V Voltage

### **Power Cycle Statistics**

Click <Enter> key to enter its submenu:

	µGuru Utilit	y v1.00C			
OC Guru					
Power Cycle Statistics					
PC Up Time	0	Hours	I	tem Help 🕨	*
PC Up Time Total	119	Hours			
PC Power Cycles	538	Cycles			
PC Reset Button Cycles	123	Cycles			
AC Power On Total Time	288	Hours			
AC Power Cycles	228	Cycles			
<b>↑↓→←:Move Enter:Select</b> +	+/-/PU/PD:Value	F8:OC On	The Fly	F10:Save	ESC:Exit

These items display the power cycle statistics for each element.

# 3.1.2 ABIT EQ

Click right-arrow  $\langle \rightarrow \rangle$  key to switch from OC Guru setup menu to ABIT EQ setup menu:

µGuru Utility v1.00C				
ABIT EQ				
ABIT EQ Beep Control LED Effect Control > Temperature Monitoring > Voltage Monitoring > Fan Speed Monitoring > FanEQ Control	Enabled MODE 1 Press Enter Press Enter Press Enter Press Enter	Item Help ►		
↑↓→←:Move Enter:Select	+/-/PU/PD:Value	F10:Save ESC:Exit		

### **ABIT EQ Beep Control**

This item allows you to enable or disable ABIT EQ Beep Control function.

#### LED Effect Control (For model "AW9D-MAX" only)

This item selects the displaying mode for all the LEDs located at the bottom side of the motherboard.

[All OFF]: All the LED lights off.

[All ON]: All the LED lights on without any flashing effect.

[MODE 1 ~ MODE 6]: All the LED lights on with the flashing effect by one of these modes.

### **Solution** State S

Click <Enter> key to enter its submenu:

µGuru Utility v1.00C						
ABIT EQ	ABIT EQ					
Temperatur	e Monitori	.ng				
	Reading	Shutdown	Shutdown	Веер	Веер	
		Enable	Temp.	Enable	e Temp.	
(*)CPU Temperature	59°C/138°F	' (*)	85°/185°F	(*)	75°C/167°F	
(*)System Temperature	42°C/107°F	'()	65°C/149°F	(*)	55°C/131°F	
(*)PWM1 Temperature	60°C/140°F	'()	90°C/194°F	(*)	80°C/176°F	
(*) PWM2 Temperature	60°C/140°F	' ( )	90°C/194°F	(*)	80°C/176°F	
(*)PWM3 Temperature	60°C/140°F	' ( )	90°C/194°F	(*)	80°C/176°F	
(*) PWM4 Temperature	60°C/140°F	' ( )	90°C/194°F	(*)	80°C/176°F	
_						
↑↓→←:Move Enter:Select	+/-/PU/PI	):Value	F10	:Save	ESC:Exit	

#### CPU Temperature/System Temperature/PWM1~4 Temperature

These items display the temperature of CPU, System, and Power Module.

#### - Shutdown Enable

Use <Space> key to enable system shutdown function. If the CPU/System/PWM's temperature exceeds the shutdown temperature limit, the system will shutdown automatically.

#### - Shutdown Temp.

This items sets the temperature that will shutdown the system automatically in order to prevent system overheating.

#### - Beep Enable

Use <Space> key to enable warning beeps function. Once the system has detected that the CPU/System/PWM's temperature exceeded the beep temperature limit, warning beeps will sound.

#### - Beep Temp.

This item selects the warning temperature limit.

#### **%** The shutdown temperature must be set above the warning temperature.

### ♥ Voltage Monitoring

Click <Enter> key to enter its submenu:

pGuru Utility v1.00C					
ABIT EQ					
Voltage Monitor	ing				
	Reading Shutdown	Веер	High	Low	
	Enable	Enable	Limit	Limit	
(*)CPU Core Voltage	1.27 V (*)	(*)	1.55	v 0.00 v	
(*)DDR Voltage	1.80 V ( )	(*)	2.15	V 1.45 V	
(*)DDR VTT Voltage	0.89 V ( )	(*)	1.05	V 0.70 V	
(*)CPU VTT 1.2V Voltage	1.20 V ( )	(*)	1.45	V 0.95 V	
(*)MCH & PCIe 1.5V Voltage	1.50 V ( )	(*)	1.80	V 1.20 V	
(*)MCH 2.5V Voltage	2.50 V ( )	(*)	3.00	V 2.00 V	
(*)ICH 1.05V Voltage	1.05 V ( )	(*)	1.25	V 0.85 V	
(*)ATX +12V (24-Pin Connector)	12.00 V ( )	(*)	14.40	V 9.60 V	
(*)ATX +12V (4-Pin Connector)	12.00 V ( )	(*)	14.40	V 9.60 V	
(*)ATX +5V	5.00 V ( )	(*)	6.00	V 4.00 V	
(*)ATX +3.3V	3.30 V ( )	(*)	3.95	V 2.65 V	
(*)ATX 5VSB	5.00 V ( )	(*)	6.00	V 4.00 V	
<pre>↑↓→←:Move Enter:Select +/-/</pre>	PU/PD:Value	F10:S	ave ES	C:Exit	

#### All Voltages

These items display the voltage of each element.

#### - Shutdown Enable

Use <Space> key to enable system shutdown function. If the voltage of corresponding element is higher/lower than the high/low limit, the system will automatically shutdown.

#### - Beep Enable

Use <Space> key to enable warning beeps function. If the voltage of corresponding element is higher/lower than the high/low limit, warning beeps will sound.

#### High/Low Limit

These items set the high and low voltage limit.

#### **%** The value of high limit must be set above the one of low limit.

# Seed Monitoring

Click <Enter> key to enter its submenu:

µGuru Utility v1.00C				
ABIT EQ				
Fan Speed Mon	itoring			
	Reading	Shutdown Enable	Beep Enable	Low Limit
(*)CPU FAN Speed	1440 RPM	(*)	(*)	300 RPM
()NB FAN Speed	N/A	()	()	300 RPM
()SYS FAN Speed	N/A	()	()	300 RPM
( )AUX1 FAN Speed	N/A	()	()	300 RPM
()AUX2 FAN Speed	N/A	()	()	300 RPM
()AUX3 FAN Speed	N/A	()	()	300 RPM
( )OTES1 FAN Speed	N/A	()	()	300 RPM
( )OTES2 FAN Speed	N/A	()	()	300 RPM
↑↓→←:Move Enter:Select +/	'-/PU/PD:Valu	e	F10:Save	ESC:Exit

#### CPU/NB/SYS/AUX1~3/OTES1~2 FAN Speed

These items display the speed of the fans connected to CPU, NB, SYS, AUX1~3, and OTES1~2 FAN connectors.

#### - Shutdown Enable

Use <Space> key to enable system shutdown function. Once the system has detected that the fan speed is lower than the low limit value, system will shutdown automatically.

#### - Beep Enable

Use <Space> key to enable warning beeps function. If the fan speed is lower than the low limit value, warning beeps will sound.

#### - Low Limit

These items set the low limit of fan speed.

## Section 5 FanEQ Control

			μ	Guru Utilit	y v1.00C		
			ABIT EQ				
			FanEQ Conti	col			
	lst FanEQ	Q Group	•	Press	Enter	Item He	lp 🍽
► :	2nd FanE(	) Group	)	Press	Enter		
	3rd FanE(	) Group	•	Press	Enter		
<u> </u>							
	↑↓→←:м	ove Er	ter:Select	+/-/PU/PD:	Value	F10:Save	ESC:Exit

# ♥ 1st FanEQ Group

Click <Enter> key to enter its submenu (1st FanEQ Group):

µGuru Utility v1.00C				
ABIT EQ				
1st FanEQ G	roup			
CPU FanEQ Control	Enabled	Item Help ►►►		
-Reference Temperature	CPU Temperature			
-Control Temperature High	80°C/176°F			
-Control Temperature Low	70°C/158°F			
-Fan PWM Duty Cycle High	100 %			
- Fan PWM Duty Cycle Low	30 %			
NB FanEQ Control	Enabled			
-Reference Temperature	SYS Temperature			
-Control Temperature High	40°C/104°F			
-Control Temperature Low	30°C/86°F			
-DC Fan Voltage High	12.0 V			
-DC Fan Voltage Low	8.0 V			
SYS FanEQ Control	Enabled			
-Reference Temperature	SYS Temperature			
-Control Temperature High	40°C/104°F			
-Control Temperature Low	30°C/86°F			
-DC Fan Voltage High	12.0 V			
-DC Fan Voltage Low	8.0 V			
↑↓→←:Move Enter:Select	+/-/PU/PD:Value	F10:Save ESC:Exit		

### CPU/NB/SYS FanEQ Control

When set to [Enabled], these items control the CPU, NB, and/or SYS fan speed by the following setting combinations.

#### - Reference Temperature

This item selects the reference point for taking temperature among the available options of CPU, SYS, and PWM Temperature, but there is only one "CPU Temperature" item to choose for the "CPU FanEQ Control".

#### - Control Temperature High/Low

These items set the high and low temperature limit that you want to do the fan speed control.

#### - Fan PWM Duty Cycle High/Low

These items set the high and low limit of PWM duty cycle that you want to provide the fan with.

#### DC Fan Voltage High/Low

These items set the high and low voltage limit that you want to provide the fan with.

#### **※** The value of high limit must be set above the one of low limit.

### Section 2 Sectio

Click <Enter> key to enter its submenu (2nd FanEQ Group):

μG	Guru Utility V1.00C	
ABIT EQ		
2nd FanEQ Gr	roup	
AUX1 FanEQ Control -Reference Temperature -Control Temperature High -Control Temperature Low -DC Fan Voltage High -DC Fan Voltage Low AUX2 FanEQ Control AUX3 FanEQ Control	Enabled SYS Temperature 40°C/104°F 30°C/86°F 12.0 V 8.0 V Disabled Disabled	Item Help ►►►
<pre>↑↓→←:Move Enter:Select</pre>	+/-/PU/PD:Value	F10:Save ESC:Exit

#### AUX1~AUX3 FanEQ Control

When set to [Enabled], these items control the AUX1 $\sim$ AUX3 fan speed by the following setting combinations.

#### - Reference Temperature

This item selects the reference point for taking temperature among the available options of CPU, SYS, and PWM Temperature, but there is only one "CPU Temperature" item to choose for the "CPU FanEQ Control".

#### - Control Temperature High/Low

These items set the high and low temperature limit that you want to do the fan speed control.

#### DC Fan Voltage High/Low

These items set the high and low voltage limit that you want to provide the fan with.

#### **%** The value of high limit must be set above the one of low limit.

### Srd FanEQ Group

Click <Enter> key to enter its submenu (3rd FanEQ Group):

pGuru Utility V1.00C				
ABIT EQ				
3rd FanEQ G	roup			
OTES1 FanEQ Control -Reference Temperature -Control Temperature High -Control Temperature Low -DC Fan Voltage High -DC Fan Voltage Low OTES2 FanEQ Control	Enabled SYS Temperature 40°C/104°F 30°C/86°F 12.0 V 8.0 V Disabled	Item Help ►►►		
↑↓→←:Move Enter:Select	+/-/PU/PD:Value	F10:Save ESC:Exit		

#### OTES1~OTES2 FanEQ Control

When set to [Enabled], these items control the OTES1 $\sim$ OTES2 fan speed by the following setting combinations.

#### - Reference Temperature

This item selects the reference point for taking temperature among the available options of CPU, SYS, and PWM Temperature, but there is only one "CPU Temperature" item to choose for the "CPU FanEQ Control".

#### - Control Temperature High/Low

These items set the high and low temperature limit that you want to do the fan speed control.

#### - DC Fan Voltage High/Low

These items set the high and low voltage limit that you want to provide the fan with.

#### **%** The value of high limit must be set above the one of low limit.

# **3.2 Standard CMOS Features**

Phoenix - AwardBIOS CMOS Setup Utility				
	Standard CMOS Features			
Date (mm:dd:yy)	Thu. Jul 27 2006	Item Help		
Time (hh:mm:ss)	12 : 34 : 56			
IDE Channel 1 Master	None			
IDE Channel 1 Slave	None			
IDE Channel 2 Master	None			
IDE Channel 2 Slave	None			
IDE Channel 3 Master	None			
IDE Channel 3 Slave	None			
IDE Channel 4 Master	None			
▶ IDE Channel 4 Slave	None			
Drive A	1.44M, 3.5 in.			
Drive B	None			
Floppy 3 Mode Support	Disabled			
Halt On	All, But keyboard			
	C 4 0			
Base Memory	640K			
Extended Memory	1046520K			
Total Memory	104/552K			
→←:Move Enter:Select	+/-/PU/PD:Value F10:Save ESC:	Exit F1:GeneralHelp		
F5: Previous Values	F6: Fail-Safe Defaults F7: Op	ptimized Defaults		

#### Date (mm:dd:yy)

This item sets the date you specify (usually the current date) in the format of [Month], [Date], and [Year].

#### Time (hh:mm:ss)

This item sets the time you specify (usually the current time) in the format of [Hour], [Minute], and [Second].

# IDE Channel 1 Master/Slave, IDE Channel 2 Master/Slave, IDE Channel 3 Master/Slave, IDE Channel 4 Master/Slave

Click <Enter> key to enter its submenu:

Phoenix - AwardBIOS CMOS Setup Utility IDE Channel 1 Master					
IDE HDD Auto-Detection	Press	Enter	Item Help		
IDE Channel 1 Master	Auto				
Access Mode	Auto				
Capacity	0	MB			
Culinder	٥				
Road	0				
Precomp	0				
Landing Zone	0				
Sector	0 0				
Hat Move Enter: Select	+/_/II	o E10.8000	ESCIENT ELCODOROL Hold		
F5. Previous Values	F6. Fail-Safe	e riu:Save Defaulte B	F7: Optimized Defaults		

#### \* The items "IDE Channel 3 Master/Slave" and "IDE Channel 4 Master/Slave" appear only when the item "On-Chip SATA" in the "On-Chip IDE Device" menu is set to [Enhanced Mode], or set to [Auto] when SATA ports are connected with devices.

#### **IDE HDD Auto-Detection**

This item allows you to detect the parameters of IDE drives by pressing <Enter> key. The parameters will be shown on the screen automatically.

#### IDE Channel 1 Master/Slave, IDE Channel 2 Master/Slave, Extended IDE Drive

When set to [Auto], the BIOS will automatically check what kind of IDE drive you are using. If you want to define your own drive by yourself, set it to [Manual] and make sure you fully understand the meaning of the parameters. Please refer to the instruction manual provided by the device's manufacturer to get the setting right.

#### Access Mode

This item selects the mode to access your IDE devices. Leave this item at its default [Auto] setting to detect the access mode of your HDD automatically.

#### Capacity

This item displays the approximate capacity of the disk drive. Usually the size is slightly greater than the size of a formatted disk given by a disk-checking program.

#### Cylinder

This item configures the numbers of cylinders.

#### Head

This item configures the numbers of read/write heads.

#### Precomp

This item displays the number of cylinders at which to change the write timing.

#### Landing Zone

This item displays the number of cylinders specified as the landing zone for the read/write heads.

#### Sector

This item configures the numbers of sectors per track.

## **Back to Standard CMOS Features Setup Menu**

#### Drive A & Drive B

This item sets the type of floppy drives (usually only Drive A) installed.

#### Floppy 3 Mode Support

This item allows you to use "3 Mode Floppy Drive" in Japanese computer systems by selecting drive A, B, or both. Leave this item at its default [Disabled] setting if you are not using this Japanese standard floppy drive.

#### Halt On

This item determines whether the system stops if an error is detected during system boot-up.

[All Errors]: The system-boot will stop whenever the BIOS detect a non-fatal error.

[No Errors]: The system-boot will not stop for any error detected.

[All, But Keyboard]: The system-boot will stop for all errors except a keyboard error.

[All, But Diskette]: The system-boot will stop for all errors except a diskette error.

[All, But Disk/Key]: The system-boot will stop for all errors except a diskette or keyboard error.

#### **Base Memory**

This item displays the amount of base memory installed in the system. The value of the base memory is typically 640K for system with 640K or more memory size installed on the motherboard.

#### **Extended Memory**

This item displays the amount of extended memory detected during system boot-up.

#### **Total Memory**

This item displays the total memory available in the system.

# **3.3 Advanced BIOS Features**

	Phoenix - AwardBIOS CMOS Setup Utility Advanced BIOS Features					
	Hyper-Threading Technology	Enabled	Item Help			
	Quick Power On Self Test	Enabled				
►	CPU Feature	Press Enter				
►	Hard Disk Boot Priority	Press Enter				
	First Boot Device	Floppy				
	Second Boot Device	Hard Disk				
	Third Boot Device	IDE CDROM				
	Boot Other Device	Enabled				
	Swap Floppy Drive	Disabled				
	Boot Up Floppy Seek	Disabled				
	Boot Up NumLock Status	On				
	Security Option	Setup				
	MPS Version Ctrl For OS	1.4				
	Report No FDD for OS	No				
	Delay IDE Initial (Secs)	0				
	Full Screen LOGO Show	Enabled				
	Disable Unused PCI Clock	Yes				
ł ł·	→←:Move Enter:Select +/-/PU/PI	:Value F10:Save ESC:	Exit F1:GeneralHelp			
	F5: Previous Values F6: Fail	-Safe Defaults F7: Op	otimized Defaults			

#### Hyper-Threading Technology

This item is used to enable the functionality of the processor with Hyper-Threading Technology and will appear only when using such processor.

The Hyper-Threading Technology helps your PC work more efficiently by maximizing processor resources and enabling a single processor to run two separate threads of software simultaneously, bringing forth greater performance and system responsiveness when running multiple applications at once.

#### **Quick Power On Self Test**

When set to [Enabled], this item speeds up the Power On Self Test (POST) after powering on the system. The BIOS shorten or skip some check during the POST.

### ✤ CPU Feature

Click <Enter> key to enter its submenu:

Phoen	ix - AwardBIOS CMOS Setup Utilit	ty
Thermal Management	Thermal Monitor 1	Item Help
	10 v	item heip
X - TM2 BUS Ratio	12 X	
x - TM2 Bus VID	0.83750	
Limit CPUID MaxVal	Disabled	
C1E Function	Enabled	
Execute Disable Bit	Enabled	
EIST Function	Enabled	
→←:Move Enter:Select	+/-/PU/PD:Value F10:Save ESC:	Exit F1:GeneralHelp
F5: Previous Values	F6: Fail-Safe Defaults F7: Op	ptimized Defaults

#### **Thermal Management**

This item selects the type of thermal monitoring.

#### - TM2 Bus Ratio

This item represents the frequency (bus ratio) of the throttled performance state that will be initiated when the on-die sensor goes from not hot to hot.

#### - TM2 Bus VID

This item represents the voltage of the throttled performance state that will be initiated when the on-die sensor goes from not hot to hot.

#### Limit CPUID MaxVal

When set to [Enabled], this item limits the CPUID maximum value to 3, which is usually required for older OS like Windows NT4.0.

Leave this item at its default [Disabled] settings for OS like Windows XP.

#### **C1E** Function

This item appears only for certain processors with the C1E (Enhanced Halt State) Function. When set to [Enabled], the processor will further reduce the total power consumption.

#### **Execute Disable Bit**

This item appears only for certain processors with the Execute Disable Bit (XD bit) feature. When set to [Enabled], this item allows the processor to prevent data pages from being used by malicious software to execute code and provide memory protection.

#### EIST Function

This item appears only for certain processors with the EIST (Enhanced Intel SpeedStep Technology) Function. When set to [Enabled], EIST will dynamically switch between multiple frequency and voltage points to optimize the power and performance balance of the processor and system based on demand.

### **Back to Advanced BIOS Features Setup Menu**

#### Hard Disk Boot Priority

This item selects the hard disks booting priority. By pressing <Enter> key, you can enter its submenu where the hard disks detected can be selected for the booting sequence to boot up system.

This item functions only when there is the option of [Hard Disk] in any one of the First/Second/Third Boot Device items.

#### First Boot Device / Second Boot Device / Third Boot Device / Boot Other Device

Select the drive to boot first, second and third in the [First Boot Device], [Second Boot Device], and [Third Boot Device] items respectively. The BIOS will boot the operating system according to the sequence of the drive selected. Set [Boot Other Device] to [Enabled] if you wish to boot from another device other than these three items.

#### Swap Floppy Drive

When set to [Enabled], and the system is booting from the floppy drive, the system will boot from drive B instead of the regular drive A. There must be two floppy drives connected in the system to use this function.

#### **Boot Up Floppy Seek**

When set to [Enabled], the BIOS will check whether the floppy disk drive is installed or not.

#### Boot Up NumLock Status

This item determines the default state of the numeric keypad at system booting up.

**[On]:** The numeric keypad functions as number keys.

**[Off]:** The numeric keypad functions as arrow keys.

#### **Security Option**

This item determines when the system will prompt for password - every time the system boots or only when enters the BIOS setup.

**[Setup]:** The password is required only when accessing the BIOS Setup.

**[System]:** The password is required each time the computer boots up.

#### Don't forget your password. If you forget the password, you will have to open the computer case and clear all information in the CMOS before you can start up the system. But by doing this, you will have to reset all previously set options.

#### **MPS Version Ctrl For OS**

This item specifies which version of MPS (Multi-Processor Specification) this motherboard will use. Leave this item at its default setting.

#### Report No FDD For OS

When set to [Yes], this item allows you to run some older operating system without floppy disk drive. Leave this item at its default setting.

#### Delay IDE Initial (Secs)

This item allows the BIOS to support some old or special IDE devices by prolonging this delay time. A larger value will give more delay time to the device for which to initialize and to prepare for activation.

#### Full Screen LOGO Show

This item determines to show the full screen logo when booting.

#### **Disable Unused PCI Clock**

This option disables the clock of PCI slot that is not in use.

**[Yes]:** The system automatically detect the unused DIMM and PCI slots, and stop sending clock signal to these unused PCI slots.

[No]: The system always send clock signal to all PCI slots.

Set this option to [No] setting if there are adapters that cannot be automatically detected by the system and will cause malfunction.

# **3.4 Advanced Chipset Features**

Phoenix - AwardBIOS CMOS Setup Utility						
Advanced	Advanced Chipset Features					
DRAM Timing Selectable	By SPD	Item Help				
x - CAS Latency Time (tCL)	4					
x - RAS# to CAS# Delay(tRCD)	4					
x - RAS# Precharge (tRP)	4					
x - Precharge Delay (tRAS)	11					
PCI Express Root Port Func	Press Enter					
PEG Force X1	Disabled					
Init Display First	PCI Slot					
→←:Move Enter:Select +/-/PU/PE	:Value F10:Save ESC:	Exit F1:GeneralHelp				
F5: Previous Values F6: Fail	-Safe Defaults F7: Op	timized Defaults				

#### **DRAM Timing Selectable**

This item sets the optimal timings for the following four items, depending on the memory module you are using. The default setting "By SPD" configures these four items by reading the contents in the SPD (Serial Presence Detect) device. The EEPROM on the memory module stores critical parameter information about the module, such as memory type, size, speed, voltage interface, and module banks.

#### - CAS Latency Time (tCL)

This item controls the latency between the DRAM read command and the time that the data becomes actually available.

#### RAS# to CAS# Delay (tRCD)

This item controls the latency between the DRAM active command and the read/write command.

#### RAS# Precharge (tRP)

This item controls the idle clocks after issuing a precharge command to the DRAM.

#### - Precharge Delay (tRAS)

This item controls the number of DRAM clocks used for the DRAM parameters.

# ✤ PCI Express Root Port Func

Click <Enter> key to enter its submenu:

Phoenix - AwardBIOS CMOS Setup Utility					
PCI Express Root Port Func					
PCI Express Slot 1 Auto	Item Help				
PCI Express Slot 1 Auto					
PCI-E Compliancy Mode v1.0a					
$\downarrow \rightarrow \leftarrow$ :Move Enter:Select +/-/PU/PD:Value F10:Save ESC:	Exit F1:GeneralHelp				
F5: Previous Values F6: Fail-Safe Defaults F7: Or	timized Defaults				

#### PCI Express Slot 1 / PCI Express Slot 2

This option enables or disables the PCI Express port function.

#### **PCI-E Compliancy Mode**

This item selects the mode for PCI Express add-on card.

## **Back to Advanced Chipset Features Setup Menu**

### PEG Force X1

When set to [Enabled], this item forces the PEG port down to x1 mode.

#### **Init Display First**

This item allows you to choose the primary display card.

# **3.5 Integrated Peripherals**

Phoen	ix - AwardBIOS CMOS Setup Utili	ty
	Integrated Peripherals	_
▶ On-Chip IDE Device	Press Enter	Item Help
On-Chip PCI Device	Press Enter	
Super-IO Device	Press Enter	
<ul> <li>Onboard PCI Device</li> </ul>	Press Enter	
	$+/-/PII/PD \cdot Value F10 \cdot Save FSC \cdot$	Exit Fl:GeneralHelp
F5: Previous Values	F6. Fail-Safe Defaults F7. O	ctimized Defaults
EJ. FIEVIOUS Values	ro. raii-saie Delauits F7. O	PLIMIZED DELAUIUS

# ✤ On-Chip IDE Device

Click <Enter> key to enter its submenu:

Phoenix - AwardBIOS CMOS Setup Utility						
On-Chip IDE Device						
IDE Bus Master	Enabled	Item Help				
On-Chip IDE-1 Controller	Press Enter					
*** On-Chip Serial ATA Setting	***					
X On-Chip SATA Mode	IDE					
X - SATA RAID ROM	Enabled					
On-Chip SATA	Auto					
X - PATA IDE Mode	IDE-1					
*** IDE Channel Configuration	***					
IDE Channel 1 Master	Auto					
IDE Channel 1 Slave	Auto					
IDE Channel 2 Master	Auto					
IDE Channel 2 Slave	Auto					
IDE Channel 3 Master	Auto					
IDE Channel 3 Slave	Auto					
IDE Channel 4 Master	Auto					
IDE Channel 4 Slave	Auto					
	Value F10:Save ESC:	Exit F1:GeneralHelp				
F5: Previous Values F6: Fail	-Safe Defaults F7: Op	timized Defaults				

### **IDE Bus Master**

This option enables or disables the IDE bus mastering capability under the DOS environment.

#### **On-Chip IDE-1 Controller**

This item selects whether to enable or disable the IDE-1 controller.

#### **On-Chip SATA Mode**

This item determines the mode for on-chip Serial ATA.

**[IDE]:** The on-chip Serial ATA served as IDE mode.

[RAID]: The on-chip Serial ATA served as RAID mode.

**[AHCI]:** The on-chip Serial ATA served as AHCI (Advanced Host Controller Interface) mode for advanced performance and usability.

#### - SATA RAID ROM

This item allows you to use the boot ROM of on-chip Serial ATA RAID to boot up system.

#### **On-Chip SATA**

This item determines the function for on-chip Serial ATA.

[Disabled]: Disable the Serial ATA controller.

[Auto]: Allows the Serial ATA controller to be arranged by BIOS automatically.

**[Combined Mode]:** Parallel ATA and Serial ATA are combined together. Supports up to 4 IDE drives.

[Enhanced Mode]: Enable both Parallel ATA and Serial ATA. Supports up to 6 IDE drives.

[SATA Only]: The SATA is operating in legacy mode.

**\*\*** This option is configurable only when the item [On-Chip SATA Mode] is set to [IDE].

Mode	IDE Channel 1 Master	IDE Channel 1 Slave	IDE Channel 2 Master	IDE Channel 2 Slave	IDE Channel 3 Master	IDE Channel 3 Slave	IDE Channel 4 Master	IDE Channel 4 Slave
Enhanced	IDE Master	IDE Slave	None	None	SATA1	SATA3	SATA2	SATA4
Combined	IDE Master	IDE Slave	SATA2	SATA4	None	None	None	None
Combined	SATA1	SATA3	IDE Master	IDE Slave	None	None	None	None
SATA Only	SATA1	SATA3	SATA2	SATA4	None	None	None	None

# % The option [Enhanced Mode] does not support the Windows 98/ME operating system.

#### - PATA IDE Mode

This item determines the function mode for "IDE1" connector.

**[IDE-1]:** "IDE1" connector served as [Primary Master] and [Primary Slave] channel. "SATA2" and "SATA4" connector served as [Secondary Master] and [Secondary Slave] channel. The remaining "SATA1" and "SATA3" connectors are disabled.

**[IDE-2]:** "IDE1" connector served as [Secondary Master] and [Secondary Slave] channel. "SATA1" and "SATA3" connector served as [Primary Master] and [Primary Slave] channel. The remaining "SATA2" and "SATA4" connectors are disabled.

ΡΑΤΑ IDF	IDE	IDE	IDE	IDE	IDE	IDE	IDE	IDE
Mode	Channel 1 Master	Channel 1 Slave	Channel 2 Master	Channel 2 Slave	Channel 3 Master	Channel 3 Slave	Channel 4 Master	Channel 4 Slave
IDE-1	IDE Master	IDE Slave	SATA2	SATA4	None	None	None	None
IDE-2	SATA1	SATA3	IDE Master	IDE Slave	None	None	None	None

Refer to the following table for the relationships between IDE and SATA ports.

### ✤ On-Chip PCI Device

Click <Enter> key to enter its submenu:

Phoenix - AwardBIOS CMOS Setup Utility						
On-Chip PCI Device						
On-Chip USB Controller	Enabled	Item Help				
- USB 2.0 Controller	Enabled					
- USB Keyboard Support Via	OS					
- USB Mouse Support Via	OS					
On-Chip Audio Controller	Auto					
	:Value F10:Save ESC:E	xit F1:GeneralHelp				
F5: Previous Values F6: Fail	-Safe Defaults F7: Op	timized Defaults				

#### **On-Chip USB Controller**

This option enables or disables the USB controller.

#### - USB 2.0 Controller

This option enables or disables the USB 2.0 controller.

#### - USB Keyboard Support via

Select [**BIOS**] for the legacy operating system (such as DOS) that does not support USB keyboard.

<sup>%</sup> This option is configurable only when the item [On-Chip SATA] is set to [Combined Mode].

#### - USB Mouse Support via

Select  $\left[ \textbf{BIOS}\right]$  for the legacy operating system (such as DOS) that does not support USB mouse.

#### **On-Chip Audio Controller**

This option enables or disables the audio controller.

### Super-IO Device

Click <Enter> key to enter its submenu:

Phoenix - AwardBIOS CMOS Setup Utility Super-IO Device					
Floppy Disk Controlle:	r Enabled	Item Help			
LaciMorra Entor: Soloct	$\pm / - / \text{PU} / \text{PD} \cdot \text{Value} = \text{F10} \cdot \text{Same} = \text{FC} \cdot \text{I}$	Frit Fl: Conoral Holp			
F5: Provious Values	F6. Fail-Safe Defaults F7. On	timized Defaults			
F5. FIEVIOUS Values	ro. raii-saie Deraults F7. Op	Jumized Delauits			

#### **Floppy Disk Controller**

This option enables or disables the floppy disk controller.

## ♦ Onboard PCI Device

Click <Enter> key to enter its submenu:

Phoenix - AwardBIOS CMOS Setup Utility					
Onboard PCI Device					
IEEE 1394 Controller	Enabled	Item Help			
Serial1 ATA Controller	Enabled				
- SATA RAID Mode	Disabled				
- SATA Option ROM	Enabled				
Serial2 ATA Controller	Enabled				
- SATA RAID Mode	Disabled				
- SATA Option ROM	Enabled				
LAN1 Controller	Enabled				
- Invoke Boot Agent	Disabled				
LAN2 Controller	Enabled				
- Invoke Boot Agent	Disabled				
. Hove Enter:Select +/-	-/PU/PD:Value F10:Save ESC:	Exit F1:GeneralHelp			
F5: Previous Values F6	: Fail-Safe Defaults F7: Op	timized Defaults			

#### **IEEE 1394 Controller** (for model AW9D-MAX only)

This option enables or disables the IEEE 1394 controller.

Serial1 ATA Controller (for Connector "SATA5" and "SATA6" on model AW9D-MAX only)

This option enables or disables the Serial ATA controller.

#### - SATA RAID Mode

This item determines to enable or disable the RAID mode for onboard SATA controller.

#### - SATA Option ROM

This item allows you to use the boot ROM of onboard Serial ATA RAID to boot up system.

Serial2 ATA Controller (for Connector "SATA7" and "eSATA1" on model AW9D-MAX only)

This option enables or disables the Serial ATA controller.

#### - SATA RAID Mode

This item determines to enable or disable the RAID mode for onboard SATA controller.

#### - SATA Option ROM

This item allows you to use the boot ROM of onboard Serial ATA RAID to boot up system.

### LAN 1 Controller

This option enables or disables the LAN1 controller.

#### - Invoke Boot Agent

This item allows you to use the boot ROM (instead of a disk drive) to boot up the system and access the local area network directly.

#### LAN 2 Controller (for model AW9D-MAX only)

This option enables or disables the LAN2 controller.

#### - Invoke Boot Agent

This item allows you to use the boot ROM (instead of a disk drive) to boot up the system and access the local area network directly.

# 3.6 Power Management Setup

Phoenix - AwardBIOS CMOS Setup Utility			
Power Management Setup			
ACPI Suspend Type	S3(Suspend To RAM)	Item Help	
- Resume by USB From S3	Enabled		
Power Button Function	Instant-Off		
Wake Up by PME# of PCI	Disabled		
Wake Up by WAKE# of PCIe	Disabled		
Wake Up by Onboard LAN1	Disabled		
Wake Up by Onboard LAN2	Disabled		
Wake Up by Onboard 1394	Disabled		
Wake Up by Alarm	Disabled		
X - Date(of month) Alarm	0		
X - Time(hh:mm:ss) Alarm	0 : 0 : 0		
Power On Function	Button Only		
X - KB Power On Password	Enter		
X - Hot Key Power On	Ctrl-F1		
Restore On AC Power Loss	Power Off		
→←:Move Enter:Select +/-/PU/PD	:Value F10:Save ESC:	Exit F1:GeneralHelp	
F5: Previous Values F6: Fail	-Safe Defaults F7: Op	timized Defaults	

#### ACPI Suspend Type

This item selects the type of Suspend mode.

[S1(PowerOn Suspend)]: Enables the Power On Suspend function.

[S3(Suspend To RAM)]: Enables the Suspend to RAM function.

#### - Resume by USB From S3

When set to [Enabled], this item allows you to use a USB device to wake up a system that is in the S3 (STR - Suspend To RAM) state. This item can be configured only if the item "ACPI Suspend Type" is set to [S3(STR)].

#### **Power Button Function**

This item selects the method of powering off your system:

**[Delay 4 Sec.]:** Pushing the power button for more than 4 seconds will power off the system. This will prevent the system from powering off in case you accidentally hit or pushed the power button.

**[Instant-Off]:** Pressing and then releasing the power button at once will immediately power off the system.

#### Wake Up by PME# of PCI

When set to [Enabled], access through the add-on PCI card can remotely wake up the system that was in Soft-Off condition. The PCI card must support the wake up function.

#### Wake Up by WAKE# of PCIe

When set to [Enabled], access through the add-on PCI Express card can remotely wake up the system that was in Soft-Off condition. The PCI Express card must support the wake up function.

#### Wake Up by Onboard LAN1

When set to [Enabled], access through the onboard LAN1 port can remotely wake up the system that was in Soft-Off condition.

#### Wake Up by Onboard LAN2 (for model AW9D-MAX only)

When set to [Enabled], access through the onboard LAN2 port can remotely wake up the system that was in Soft-Off condition.

#### Wake Up by Onboard 1394 (for model AW9D-MAX only)

When set to [Enabled], access through the onboard IEEE 1394 device can remotely wake up the system that was in Soft-Off condition.

#### Wake Up by Alarm

When set to [**Enabled**], you can set the date and time you would like the Soft-Off PC to power-on in the "**Date (of Month) Alarm**" and "**Time (hh:mm:ss) Alarm**" items. However, if the system is being accessed by incoming calls or the network (Resume On Ring/LAN) prior to the date and time set in these items, the system will give priority to the incoming calls or network instead.

#### - Date (of Month) Alarm

**[0]:** This option power-on the system everyday according to the time set in the "Time (hh:mm:ss) Alarm" item.

**[1-31]:** This option selects a date you would like the system to power-on. The system will power-on on the date set, and the time set in the "Time (hh:mm:ss) Alarm" item.

#### - Time (hh:mm:ss) Alarm

This item sets the time you would like the system to power-on.

#### **Power On Function**

This item selects the way you want your system to power on.

**[Password]:** Use a password to power on the system, select this option then press <Enter>. Enter your password. You can enter up to 5 characters. Type in exactly the same password to confirm, and then press <Enter>.

[Hot KEY]: Use any of the function keys between <F1> to <F12> to power on the system.

[Mouse Left]: Double click the mouse left button to power on the system.

[Mouse Right]: Double click the mouse right button to power on the system.

[Any KEY]: Use any keyboard keys to power on the system.

[Button Only]: Use only the power button to power on the system.

[Keyboard 98]: Use the power-on button on the "Keyboard 98" compatible keyboard to power on the system.

- The mouse wake up function can only be used with the PS/2 mouse, not with the COM port or USB type. Some PS/2 mice cannot wake up the system because of compatible problems. If the specs of your keyboard are too old, it may fail to power on.
- KB Power ON Password

This item sets the password required in order to power on your computer.

\* Do not forget your password, or you will have to clear the CMOS and reset all parameters in order to utilize this function again.

#### - Hot Key Power ON

This item powers on the system by pressing <Ctrl> key plus one of each function key (<F1>  $\sim$  <F12>) simultaneously.

#### **Restore On AC Power Loss**

This item selects the system action after an AC power failure.

**[Power Off]:** When power returns after an AC power failure, the system's power remains off. You must press the Power button to power-on the system.

**[Power On]:** When power returns after an AC power failure, the system's power will be powered on automatically.

**[Last State]:** When power returns after an AC power failure, the system will return to the state where you left off before power failure occurs. If the system's power is off when AC power failure occurs, it will remain off when power returns. If the system's power is on when AC power failure occurs, the system will power-on when power returns.

# 3.7 PnP/PCI Configurations

Phoenix	- AwardBIOS CMOS Setup Utilit PnP/PCI Configurations	ч
Resources Controlled By	Auto	Item Help
X - IRQ Resources	Press Enter	
PCI/VGA Pallete Snoop PCI Latency Timer(CLK)	Disbaled 32	
** PCT Express relative	items **	
Maximum Pavload Size	4096	
1 1 1		
. Hove Enter:Select +	/-/PU/PD:Value F10:Save ESC:	Exit F1:GeneralHelp
F5: Previous Values F	6: Fail-Safe Defaults F7: Op	timized Defaults

#### **Resources Controlled By**

This item configures all of the boot and Plug-and-Play compatible devices.

[Auto(ESCD)]: The system will automatically detect the settings.

[Manual]: Choose the specific IRQ resources in the "IRQ Resources" menu.

#### - IRQ Resources

Click <Enter> key to enter its submenu:

This item sets each system interrupt to either [PCI Device] or [Reserved].

Pho	enix - AwardBIOS CMOS Setup Utilit	-y
	IRQ Resources	_
IRQ-4 assigned to	PCI Device	Item Help
IRQ-5 assigned to	PCI Device	
IRQ-7 assigned to	PCI Device	
IRQ-10 assigned to	PCI Device	
IRQ-11 assigned to	PCI Device	
→←:Move Enter:Seled	Ct +/-/PU/PD:value F10:Save ESC:	Exit FI:General Help
F5: Previous Value	s ro: rail-sale Defaults F7: Op	cimized Defaults

#### PCI/VGA Palette Snoop

This item determines whether the MPEG ISA/VESA VGA cards can work with PCI/VGA or not.

[Enabled]: MPEG ISA/VESA VGA cards work with PCI/VGA.

[Disabled]: MPEG ISA/VESA VGA cards do not work with PCI/VGA.

#### PCI Latency Timer (CLK)

This item controls how long each PCI device can hold the bus before another takes over. When set to higher values, every PCI device can conduct transactions for a longer time and thus improve the effective PCI bandwidth. For better PCI performance, you should set the item to higher values.

#### Maximum Payload Size

This item sets the maximum TLP payload size for the PCI Express devices.

# 3.8 Load Fail-Safe Defaults

This option loads the BIOS default values for the most stable, minimal-performance system operations.

# **3.9 Load Optimized Defaults**

This option loads the BIOS default values that are factory settings for optimal-performance system operations.

# 3.10 Set Password

This option protects the BIOS configuration or restricts access to the computer itself.

# 3.11 Save & Exit Setup

This option saves your selections and exits the BIOS setup menu.

# 3.12 Exit Without Saving

This option exits the BIOS setup menu without saving any changes.

AW	'9D-	·MAX,	AW	'9D
----	------	-------	----	-----

# 4. Driver & Utility CD

The "Driver & Utility CD" that came packed with this motherboard contains drivers, utilities and software applications required for its basic and advanced features.

Place the "Driver & Utility CD" into the CD-ROM drive in your system. The following installation auto-run screen appears. If not, browse the root directory of the CD-ROM via the File Manager, and double click the "AUTORUN" file.



- [Drivers]: Click to enter the driver installation menu.
- [Manual]: Click to enter the user's manual menu.
- [Utility]: Click to enter the utilities installation menu.
- [ABIT Utility]: Click to enter the installation menu of utilities exclusively developed by ABIT.
- [ **Browse CD**]: Click to browse the contents of this "Driver & Utility CD".
- [ **Close**]: Click to exit this installation menu.

#### AW9D-MAX, AW9D

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# 4.1 Intel Chipset Software Installation Utility

This utility installs Windows **[INF]** files to the target system. These files outline to the operating system how to configure the Intel chipset components in order to ensure all the features function properly.

#### To install this utility:

- 1. Click on the [Drivers] tab in the installation menu screen.
- 2. Click the [Intel Chipset Software Installation Utility] item. The installation screen appears:

Intel(R) Chipset Software Installation Utility 7.2.2.1006			
int <sub>e</sub> l.	Welcome to the Intel(R) Chipset Software Installation Utility.		
	This program will install the Plug and Play components for the Intel(R) chipset that is on this system. It is strongly recommended that you exit all Windows programs before continuing.		
	< <u>Back</u> <u>Cancel</u> <u>Cancel</u> Intel(R) Installation Frameworks		

- 3. Follow the prompts on the screen to complete installation.
- 4. Restart the system for the driver to take effect.

# 4.2 Intel Matrix Storage Technology Driver

This driver provides functionality for the on-chip SATA Controller.

\* This driver installation is necessary for connectors SATA1~SATA4 only when after having enabled the RAID function in the BIOS setup menu.

The path to enable the RAID function in the BIOS setup menu is: Integrated Peripherals $\rightarrow$ On-Chip IDE Device $\rightarrow$ On-Chip SATA Mode $\rightarrow$ Select "RAID".

#### To install this driver:

- 1. Click on the [Drivers] tab in the installation menu screen.
- 2. Click the [Intel Matrix Storage Technology Driver] item. The installation screen appears:

Intel(R) Matrix Storage Manager 6.0.0.1022			
(intel)	Welcome to the setup for the Intel(R) Matrix Storage Manager.		
	This setup program will install Intel(R) Matrix Storage Manager onto your computer. It is strongly recommended that you exit all Windows programs before continuing setup.		
	< <u>Back</u> <u>Cancel</u> <u>Cancel</u> <u>Intel(R)</u> Installation Frameworks		

- 3. Follow the prompts on the screen to complete installation.
- 4. Restart the system for the driver to take effect.

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# 4.3 Realtek Audio Driver

This driver provides functionality for the onboard High Definition Audio Codec.

### To install this driver:

- 1. Click on the [Drivers] tab in the installation menu screen.
- 2. Click the [Audio Driver] item. The installation screen appears:

Realtek High Definition Aug	lio Driver Setup (2.09)	×
Realtek High Definition Auc	in Driver Setup (2,09) Welcome to the InstallShield Wizard for Realtek High Definition Audio Driver The InstallShield Wizard will install Realtek High Definition Audio Driver on your computer. To continue, click Next.	×
InstallShield	< Back Next > Cance	

- 3. Follow the prompts on the screen to complete installation.
- 4. Restart the system for the driver to take effect.
# 4.4 Realtek LAN Driver

This driver provides functionality for the onboard PCIE Gigabit and Fast Ethernet NIC Controller.

### To install this driver:

- 1. Click on the [Drivers] tab in the installation menu screen.
- 2. Click the [LAN Driver] item. The installation screen appears:



- 3. Follow the prompts on the screen to complete installation.
- 4. Restart the system for the driver to take effect.

# 4.5 Silicon Image 3132 SATA Driver

This driver provides functionality for the onboard SATA Controller.

### To install this driver:

- 1. Click on the [Drivers] tab in the installation menu screen.
- 2. Click the [Sil3132 SATA Driver] item. The installation screen appears:



- 3. Follow the prompts on the screen to complete installation.
- 4. Restart the system for the driver to take effect.

# 4.6 Silicon Image 3132 SATA RAID Driver

This driver provides functionality for the onboard SATA RAID Controller.

\* The installation of this driver is necessary only after having enabled the RAID function in the BIOS setup menu.

### To install this driver:

- 1. Click on the [Drivers] tab in the installation menu screen.
- 2. Click the [Sil3132 SATA RAID Driver] item under the [Drivers] tab. The following screen appears:



- 3. Follow the prompts on the screen to complete installation.
- 4. Restart the system for the driver to take effect.

# 4.7 USB 2.0 Driver

\* There is no need to install this driver for Windows 2000 with Service Pack 4, Windows XP with Service Pack 1, or their later version.

# 4.8 ABIT µGuru Utility

The  $\mu$ Guru Utility combined with the optional Guru Clock allows you to access and select system performance of your system while playing games, listening music, browsing Internet or office applications in full screen with no need to stop or close the running application.

### To install this utility:

- 1. Click on the [ABIT Utility] tab in the installation menu screen.
- 2. Click the [ABIT Guru] item. The installation screen appears:



- 3. Follow the prompts on the screen to complete installation.
- 4. Restart the system for the driver to take effect.

## 4.9 Intel SATA RAID Driver Disk Maker

This procedure is necessary if you want to install operating system to a RAID configuration connected among "SATA1~SATA4" connectors:

- 1. Prepare a 3.5" floppy disk drive and connect it to "FDC1" connector on this motherboard.
- 2. Start install operating system.
- 3. Insert this driver disk into floppy disk drive when the screen instruction prompts you to install a third-party SCSI or RAID driver.
- 4. Press <F6> key, and then follow the screen instruction to complete the installation.

### To create a driver disk:

- 1. Click on the [Utility] tab in the installation menu screen.
- 2. Click the [Intel SATA RAID Driver Disk Maker] item. The installation screen appears:

Floppy Disk Generator	X
Type: Intel SATA RAID Driver Disk V1.02	
Source CheckSum: 0x9F00	Build
floppy disk.	About
A:\	Exit

Insert one blank floppy disk to the selected floppy drive and click [Build].

3. Click [OK] to finish building the SATA Driver Disk.



4. Click [Exit] to exit the Floppy Disk Generator.

## 4.10 Sil3132 SATA RAID Driver Disk Maker

This procedure is necessary if you want to install operating system to a RAID configuration connected between "SATA5+SATA6" or "SATA7+eSATA1" connectors:

- 1. Prepare a 3.5" floppy disk drive and connect it to "FDC1" connector on this motherboard.
- 2. Start install operating system.
- 3. Insert this driver disk into floppy disk drive when the screen instruction prompts you to install a third-party SCSI or RAID driver.
- 4. Press <F6> key, and then follow the screen instruction to complete the installation.

### To create a driver disk:

- 1. Click on the [Utility] tab in the installation menu screen.
- 2. Click the [Sil3132 SATA RAID Driver Disk Maker[32bit]] item. The installation screen appears:

Floppy Disk Generator	×
Type: Sil3132 SATA RAID 32bit Driver Disk	
Source CheckSum: 0x10F3	Build
floppy disk.	About
A:\	Exit

Insert one blank floppy disk to the selected floppy drive and click [Build].

3. Click [OK] to finish building the SATA Driver Disk.



4. Click [Exit] to exit the Floppy Disk Generator.

# 5. Appendix

# **5.1 POST Code Definitions**

### 5.1.1 AWARD POST Code Definitions

POST (hex)	Description
CF	Test CMOS R/W functionality
CO	Early chipset initialization: -Disable shadow RAM -Disable L2 cache (socket 7 or below) -Program basic chipset registers
C1	-Auto-detection of DRAM size, type and ECC -Auto-detection of L2 cache (socket 7 or below)
C3	Expand compressed BIOS code to DRAM
C5	Call chipset hook to copy BIOS back to E000 & F000 shadow RAM
01	Expand the Xgroup codes locating in physical address 1000:0
03	Initial Superio_Early_Init switch
05	<ol> <li>Blank out screen</li> <li>Clear CMOS error flag</li> </ol>
07	1. Clear 8042 interface 2. Initialize 8042 self-test
08	<ol> <li>Test special keyboard controller for Winbond 977 series Super I/O chips</li> <li>Enable keyboard interface</li> </ol>
0A	<ol> <li>Disable PS/2 mouse interface (optional)</li> <li>Auto detect ports for keyboard &amp; mouse followed by a port &amp; interface swap (optional)</li> <li>Reset keyboard for Winbond 977 series Super I/O chips</li> </ol>
OE	Test F000h segment shadow to see whether it is R/W-able or not. If test fails, keep beeping the speaker
10	Auto detect flash type to load appropriate flash R/W codes into the run time area in F000 for ESCD & DMI support
12	Use walking 1's algorithm to check out interface in CMOS circuitry. Also set real-time clock power status, and then check for override
14	Program chipset default values into chipset. Chipset default values are MODBINable by OEM customers
16	Initial onboard clock generator if Early_Init_Onboard_Generator is defined. See also POST 26.
18	Detect CPU information including brand, SMI type (Cyrix or Intel) and CPU level (586 or 686)
1B	Initial interrupts vector table. If no special specified, all H/W interrupts are directed to SPURIOUS_INT_HDLR & S/W interrupts to SPURIOUS_soft_HDLR.
1D	Initial EARLY_PM_INIT switch
1F	Load keyboard matrix (notebook platform)
21	HPM initialization (notebook platform)
23	<ol> <li>Check validity of RTC value: e.g. a value of 5Ah is an invalid value for RTC minute.</li> <li>Load CMOS settings into BIOS stack. If CMOS checksum fails, use default value instead.</li> </ol>
24	Prepare BIOS resource map for PCI & PnP use. If ESCD is valid, take into consideration of the ESCD's legacy information.

Appendix

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<ul> <li>-Enumerate PCI bus number.</li> <li>-Assign memory &amp; I/O resource</li> <li>-Search for a valid VGA device &amp; VGA BIOS, and put it into C000:0</li> <li>If Early_Init_Onboard_Generator is not defined Onboard clock generator initialization.</li> <li>Disable respective clock resource to empty PCI &amp; DIMM slots.</li> <li>Init onboard PWM</li> <li>Initialize INT 09 buffer</li> <li>Program CPU internal MTRR (P6 &amp; PII) for 0-640K memory address.</li> <li>Initialize INT 09 buffer</li> <li>Program early chipset according to CMOS setup. Example: onboard IDE controller.</li> <li>Measure CPU speed.</li> <li>Initialize the APIC for Pentium class CPU.</li> <li>Program early chipset according to CMOS setup. Example: onboard IDE controller.</li> <li>Measure CPU speed.</li> <li>Initialize to double-byte language font (Optional)</li> <li>Put information on screen display, including Award title, CPU type, CPU speed, full screen logo.</li> <li>Reset keyboard if Early_Reset_KB is defined e.g. Winbond 977 series Super I/O chips. See also POST 63.</li> <li>Test DMA Channel 0</li> <li>Test DMA Channel 1.</li> <li>Test DMA Channel 1</li> <li>Test 2529 interrupt mask bits for channel 1</li> <li>Test 8259 interrupt mask bits for channel 1</li> <li>Test 8259 interrupt mask bits for channel 2</li> <li>Test 8259 interrupt mask bits for channel 2</li> <li>Test 8259 interrupt mask bits for channel 1</li> <li>Calculate total memory by testing the last double word of each 64K page</li> <li>Program WTRR of MI CPU</li> <li>Initialize L2 cache for P6 class CPU &amp; program CPU with proper cacheable ranges between each CPU are not identical</li> <li>Mitalize UA FG Cor P6 class CPU</li> <li>Initialize UA FG Cor P6 class CPU</li> <li>Initialize UA FAF Cor P6 class CPU</li> <li>Ini</li></ul>		Early PCI Initialization:
<ul> <li>Assign memory &amp; I/O resource -Search for a valid VGA device &amp; VGA BIOS, and put it into C000:0</li> <li>1. If Early_Init_Onboard_Generator is not defined Onboard clock generator initialization. Disable respective clock resource to empty PCI &amp; DIMM slots.</li> <li>2. Init onboard H/W monitor devices</li> <li>27 Initialize INT 09 buffer</li> <li>1. Program CPU internal MTRR (P6 &amp; PII) for 0-640K memory address.</li> <li>2. Initialize the APIC for Pentium class CPU.</li> <li>3. Program early chipset according to CMOS setup. Example: onboard IDE controller.</li> <li>4. Measure CPU speed.</li> <li>1. Invoke Video BIOS</li> <li>1. Initialize double-byte language font (Optional)</li> <li>2. Put information on screen display, including Award title, CPU type, CPU speed, full screen logo.</li> <li>33 Reset keyboard if Early_Reset_KB is defined e.g. Winbond 977 series Super I/O chips. See also POST 63.</li> <li>35 Test DMA Channel 0</li> <li>37 Test DMA Channel 1.</li> <li>39 Test DMA Channel 1.</li> <li>39 Test 2554</li> <li>32 Test 255 interrupt mask bits for channel 1</li> <li>40 Test 8259 interrupt mask bits for channel 2</li> <li>41 Initialize EISA slot</li> <li>42 Program writes allocation for AMD KS CPU</li> <li>4. On MP platform, adjust the cacheable range to smaller one in case the cacheable range between each CPU are not identical</li> <li>44 Initialize L2 cache for P6 class CPU 4.</li> <li>4. On MP platform, adjust the cacheable range to smaller one in case the cacheable range between each CPU are not identical</li> <li>52 Test all memory (clear all extended memory to 0)</li> <li>53 Clear password according to H/W jumper (Optional)</li> <li>54 Display number of processors (multi-processor platform)</li> <li>55 Display number of processors (multi-processor platform)</li> <li>56 Display number of processors (multi-processor platform)</li> <li>57 Early ISA PP P initialization -Assign CSN to</li></ul>	25	-Enumerate PCI bus number.
<ul> <li>-Search for a valid VGA device &amp; VGA BLOS, and put it into C000:0</li> <li>1. If Early_Init_Onboard_Generator is not defined Onboard clock generator initialization. Disable respective clock resource to empty PCI &amp; DIMM slots.</li> <li>2. Init onboard HVM</li> <li>3. Init onboard HVM monitor devices</li> <li>Initialize INT 09 buffer</li> <li>1. Program CPU internal MTRR (P6 &amp; PII) for 0-640K memory address.</li> <li>2. Initialize the APIC for Pentium class CPU.</li> <li>3. Program early chipset according to CMOS setup. Example: onboard IDE controller.</li> <li>4. Measure CPU speed.</li> <li>20</li> <li>20</li> <li>21. Initialize duble-byte language font (Optional)</li> <li>22. Put information on screen display, including Award title, CPU type, CPU speed, full screen logo.</li> <li>Reset keyboard if Early_Reset_KB is defined e.g. Winbond 977 series Super I/O chips. See also POST 63.</li> <li>35 Test DMA Channel 0</li> <li>37 Test DMA Channel 1</li> <li>39 Test DMA page registers.</li> <li>32 Test 259 interrupt mask bits for channel 1</li> <li>34 Test 8259 interrupt mask bits for channel 1</li> <li>35 Test 8259 interrupt mask bits for channel 2</li> <li>36 Test 8259 interrupt mask bits for channel 2</li> <li>37 Test 253 slott</li> <li>39 Lacluate total memory by testing the last double word of each 64K page</li> <li>2. Program writes allocation for AMD KS CPU</li> <li>39 Initialize LSA slot</li> <li>30 Initialize the APIC for P6 class CPU 4. On MP platform, adjust the cacheable range to smaller one in case the cacheable range between each CPU are not identical</li> <li>30 Initialize total memory by testing the last double word of each 64K page</li> <li>2. Program writes allocation for AMD KS CPU</li> <li>31. Initialize total memory by testing the last double word of each 64K page</li> <li>32. Frequent words all stended memory to 0)</li> <li>33 Clear password according to H/W jumper (</li></ul>	25	-Assign memory & I/O resource
<ul> <li>1. If Early_Init_Onboard_Generator is not defined Onboard clock generator initialization. Disable respective clock resource to empty PCI &amp; DIMM slots.</li> <li>2. Init onboard HVM</li> <li>3. Init onboard HVM monitor devices</li> <li>1. Program CPU internal MTRR (P6 &amp; PII) for 0-640K memory address.</li> <li>2. Initialize INT 09 buffer</li> <li>1. Program CPU internal MTRR (P6 &amp; PII) for 0-640K memory address.</li> <li>2. Initialize the APIC for Pentium class CPU.</li> <li>3. Program early chipset according to CMOS setup. Example: onboard IDE controller.</li> <li>4. Measure CPU speed.</li> <li>2. Put information on screen display, including Award title, CPU type, CPU speed, full screen logo.</li> <li>2. Put information on screen display, including Award title, CPU type, CPU speed, full screen logo.</li> <li>3. Reset keyboard if Early_Reset_KB is defined e.g. Winbond 977 series Super I/O chips. See also POST 63.</li> <li>35. Test DMA Channel 0</li> <li>37. Test DMA Channel 1.</li> <li>39. Test 259 interrupt mask bits for channel 1</li> <li>40. Test 8259 interrupt mask bits for channel 1</li> <li>41. Calculate total memory by testing the last double word of each 64K page</li> <li>2. Program writes allocation for AMD KS CPU</li> <li>4. Initialize EISA slot</li> <li>4. Calculate total memory by testing the last double word of each 64K page</li> <li>2. Program WTRR of M1 CPU</li> <li>2. Initialize total memory by testing the last double word of each 64K page</li> <li>32. Test all memory (clear all extended memory to 0)</li> <li>53. Test all memory (clear all extended memory to 0)</li> <li>54. Test all memory (clear all extended memory to 0)</li> <li>55. Display number of processors (multi-processor platform)</li> <li>Display PN Piogo</li> <li>57. Early ISA PN P initialization         <ul> <li>Assign CSN to every ISA PN P device</li> <li>59. Initialize the Combined Trend Ant</li></ul></li></ul>		-Search for a valid VGA device & VGA BIOS, and put it into C000:0
26       Disable respective clock resource to empty PCI & DIMM slots.         2       Init onboard PWM         3. Init onboard PWM         3. Init onboard PWM         27       Initialize INT 09 buffer         1. Program CPU internal MTRR (P6 & PII) for 0-640K memory address.         29       3. Initialize the APIC for Pentium class CPU.         3. Program early chipset according to CMOS setup. Example: onboard IDE controller.         4. Measure CPU speed.         20       1. Initialize double-byte language font (Optional)         20       2. Put information on screen display, including Award title, CPU type, CPU speed, full screen logo.         33       Reset keyboard if Early_Reset_KB is defined e.g. Winbond 977 series Super I/O chips. See also POST 63.         35       Test DMA Channel 0         37       Test DMA Channel 1.         39       Test DMA Channel 1.         40       Test 8259 interrupt mask bits for channel 1         40       Test 8259 interrupt mask bits for channel 2         43       Test 8259 interrupt mask bits for AMD KS CPU         1. Calculate total memory by testing the last double word of each 64K page         2. Program Writes allocation for AMD KS CPU         1. Program TRR of M1 CPU         2. Initialize L2 cache for P6 class CPU with proper cacheable range         3. Initialize L		1. If Early_Init_Onboard_Generator is not defined Onboard clock generator initialization.
<ul> <li>2. Init onboard PWM</li> <li>3. Init onboard H/W monitor devices</li> <li>Initialize INT 09 buffer</li> <li>1. Program CPU internal MTRR (P6 &amp; PII) for 0-640K memory address.</li> <li>2. Initialize the APIC for Pentium class CPU.</li> <li>3. Program early chipset according to CMOS setup. Example: onboard IDE controller.</li> <li>4. Measure CPU speed.</li> <li>Initialize double-byte language font (Optional)</li> <li>2. Put information on screen display, including Award title, CPU type, CPU speed, full screen logo.</li> <li>3. Test DMA Channel 0</li> <li>Test DMA Channel 0</li> <li>Test DMA Channel 1.</li> <li>Test DMA Channel 1.</li> <li>Test DMA Channel 1.</li> <li>Test DMA Channel 1.</li> <li>Test S254</li> <li>Test S259 interrupt mask bits for channel 1</li> <li>Test S259 interrupt mask bits for channel 1</li> <li>Test S254</li> <li>Test S259 interrupt mask bits for channel 2</li> <li>Test S254</li> <li>Test S259 interrupt mask bits for channel 2</li> <li>Test S254 intribute CPU verse (CPU verse)</li> <li>Program Writes allocation for AMD K5 CPU</li> <li>Program Writes allocation for AMD K5 CPU</li> <li>Program Writes allocation for AMD K5 CPU</li> <li>Program MTRR of M1 CPU</li> <li>Initialize the APIC for P6 class CPU &amp; program CPU with proper cacheable range between each CPU are not identical</li> <li>Initialize USB</li> <li>Clear password according to H/W jumper (Optional)</li> <li>Display number of processors (multi-processor platform)</li> <li>Display PN Plogo</li> <li>Early ISA PNP initialization -Assign CSN to every ISA PNP device</li> <li>Initialize Init_Onboard_Super_IO</li> <li>Initialize Init_Onboard_Super_IO</li> <li>Initialize Init_Onboard_Super_IO</li> <li>Initialize Init_Onboard_AU</li></ul>	26	Disable respective clock resource to empty PCI & DIMM slots.
<ul> <li>3. Init onboard H/W monitor devices</li> <li>27 Initialize INT 09 buffer</li> <li>Program CPU internal MTRR (P6 &amp; PII) for 0-640K memory address.</li> <li>2. Initialize the APIC for Pentium class CPU.</li> <li>3. Program early chipset according to CMOS setup. Example: onboard IDE controller.</li> <li>4. Measure CPU speed.</li> <li>28 Invoke Video BIOS         <ol> <li>Initialize double-byte language font (Optional)</li> <li>Put information on screen display, including Award title, CPU type, CPU speed, full screen log.</li> <li>Reset keyboard if Early_Reset_KB is defined e.g. Winbond 977 series Super I/O chips. See also POST 63.</li> </ol> </li> <li>Test DMA Channel 0         <ol> <li>Test DMA Channel 1.</li> <li>Test S254</li> <li>Test S259 interrupt mask bits for channel 1</li> </ol> </li> <li>Test S259 interrupt mask bits for channel 1         <ol> <li>Test S259 interrupt mask bits for channel 1</li> <li>Test S259 interrupt mask bits for Channel 2         </li></ol> <li>Test S259 interrupt mask bits for Channel 2         <ol> <li>Calculate total memory by testing the last double word of each 64K page</li> <li>Program writes allocation for AMD KS CPU             <ol> <li>Initialize L2 cache for P6 class CPU &amp; smaller one in case the cacheable range</li> <li>Initialize the APIC for P6 class CPU</li> <li>On MP platform, adjust the cacheable range to smaller one in case the cacheable ranges between each CPU are not identical</li> </ol> </li> <li>So Initialize USB incord of CMU by jumper (Optional)</li> <li>Display number of processors (multi-processor platform)</li> <li>Display</li></ol></li></li></ul>		2. Init onboard PWM
<ul> <li>Initialize INI 09 buffer</li> <li>Program CPU internal MTRR (P6 &amp; PII) for 0-640K memory address.</li> <li>Initialize the APIC for Pentium class CPU.</li> <li>Program early chipset according to CMOS setup. Example: onboard IDE controller.</li> <li>Measure CPU speed.</li> <li>Invoke Video BIOS</li> <li>Initialize duoble-byte language font (Optional)</li> <li>Put information on screen display, including Award title, CPU type, CPU speed, full screen logo.</li> <li>Reset keyboard if Early_Reset_KB is defined e.g. Winbond 977 series Super I/O chips. See also POST 63.</li> <li>Test DMA Channel 0</li> <li>Test DMA Channel 1.</li> <li>Test DMA Channel 1.</li> <li>Test S259 interrupt mask bits for channel 1</li> <li>Test 8259 interrupt mask bits for channel 1</li> <li>Test 8259 interrupt mask bits for channel 2</li> <li>Program writes allocation for AMD K5 CPU</li> <li>Initialize L2 cache for P6 class CPU &amp; program CPU with proper cacheable range</li> <li>Initialize L2 cache for P6 class CPU &amp; program CPU with proper cacheable ranges between each CPU are not identical</li> <li>On MP platform, adjust the cacheable range to smaller one in case the cacheable ranges between each CPU are not identical</li> <li>Clear password according to H/W jumper (Optional)</li> <li>Display number of processors (multi-processor platform)</li> <li>Display number of processors (multi-processor platform)</li> <li>Display PN logo</li> <li>Initialize the combined Trend Anti-Virus code</li> <li>(Optional Feature) Show message for entering AWDFLASH.EXE from FDD (optional)</li> <li>Initialize the combined Trend Anti-Virus code</li> <li>Initialize thit_Onboard_AUDIO</li> </ul>		3. Init onboard H/W monitor devices
<ul> <li>1. Program CPU internal MTRR (P6 &amp; PII) for 0-640K memory address.</li> <li>2. Initialize the APIC for Pentium class CPU.</li> <li>3. Program early chipset according to CMOS setup. Example: onboard IDE controller.</li> <li>4. Measure CPU speed.</li> <li>28 Invoke Video BIOS         <ol> <li>1. Initialize double-byte language font (Optional)</li> <li>2. Put information on screen display, including Award title, CPU type, CPU speed, full screen logo.</li> <li>33 Reset keyboard if Early_Reset_KB is defined e.g. Winbond 977 series Super I/O chips. See also POST 63.</li> </ol> </li> <li>35 Test DMA Channel 0         <ol> <li>37 Test DMA Channel 1.</li> <li>39 Test DMA page registers.</li> <li>32 Test 254</li> <li>32 Test 259 interrupt mask bits for channel 1</li> <li>40 Test 8259 interrupt mask bits for channel 1</li> <li>41 Test 8259 interrupt mask bits for channel 2</li> <li>43 Test 8259 interrupt mask bits for channel 2</li> <li>43 Test 8259 interrupt mask bits for Channel 2</li> <li>44 Test 8259 interrupt mask bits for Channel 2</li> <li>45 Calculate total memory by testing the last double word of each 64K page 2. Program writes allocation for AMD K5 CPU</li> <li>1. Program MTRR of M1 CPU</li> <li>2. Initialize L2 cache for P6 class CPU &amp; program CPU with proper cacheable range 3. Initialize the APIC for P6 class CPU</li> <li>4. On MP platform, adjust the cacheable range to smaller one in case the cacheable ranges between each CPU are not identical</li> <li>50 Initialize USB</li> <li>51 Test all memory (Clear all extended memory to 0)</li> <li>53 Clear password according to H/W jumper (Optional)</li> <li>55 Display number of processors (multi-processor platform)</li> <li>56 Display PnP logo</li> <li>57 Test NJ</li></ol></li></ul>	27	Initialize INT 09 buffer
29       2. Initialize the APIC for Pentium class CPU.         3. Program early chipset according to CMOS setup. Example: onboard IDE controller.         4. Measure CPU speed.         28       Invoke Video BIOS         1. Initialize double-byte language font (Optional)         20       2. Put information on screen display, including Award title, CPU type, CPU speed, full screen logo.         33       Reset keyboard if Early_Reset_KB is defined e.g. Winbond 977 series Super I/O chips. See also POST 63.         35       Test DMA Channel 0         37       Test DMA Channel 1.         39       Test DMA page registers.         3C       Test 8254         3E       Test 8259 interrupt mask bits for channel 1         40       Test 8259 interrupt mask bits for channel 2         43       Test 8259 interrupt mask bits for channel 2         44       Initialize EISA slot         49       1. Calculate total memory by testing the last double word of each 64K page         2. Program writes allocation for AMD KS CPU       1. Program MTRR of M1 CPU         2. Initialize L2 cache for P6 class CPU & program CPU with proper cacheable ranges between each CPU are not identical         50       Initialize USB         52       Test all memory (clear all extended memory to 0)         53       Clear password according to H/W jumper (Optional)		1. Program CPU internal MTRR (P6 & PII) for 0-640K memory address.
<ul> <li>A. Measure CPU speed.</li> <li>Invoke Video BIOS</li> <li>I. Initialize double-byte language font (Optional)</li> <li>Put information on screen display, including Award title, CPU type, CPU speed, full screen logo.</li> <li>Reset keyboard if Early_Reset_KB is defined e.g. Winbond 977 series Super I/O chips. See also POST 63.</li> <li>Test DMA Channel 0</li> <li>Test DMA Channel 1.</li> <li>Test DMA page registers.</li> <li>Test 8259 interrupt mask bits for channel 1</li> <li>Test 8259 interrupt mask bits for channel 1</li> <li>Test 8259 interrupt mask bits for channel 2</li> <li>Initialize EISA slot</li> <li>Calculate total memory by testing the last double word of each 64K page</li> <li>Program Writes allocation for AMD K5 CPU</li> <li>Initialize L2 cache for P6 class CPU &amp; program CPU with proper cacheable range</li> <li>Initialize L058</li> <li>Calculate total nemory to 0</li> <li>Initialize USB</li> <li>Test all memory (clear all extended memory to 0)</li> <li>Clear password according to H/W jumper (Optional)</li> <li>Display number of processors (multi-processor platform)</li> <li>Display</li></ul>	29	2. Initialize the APIC for Pentium class CPU.
<ul> <li>4. Measure CPU speed.</li> <li>1. Initialize double-byte language font (Optional)</li> <li>2. Put information on screen display, including Award title, CPU type, CPU speed, full screen logo.</li> <li>33. Reset keyboard if Early_Reset_KB is defined e.g. Winbond 977 series Super I/O chips. See also POST 63.</li> <li>35. Test DMA Channel 0</li> <li>37. Test DMA Channel 1.</li> <li>39 Test DMA Channel 1.</li> <li>39 Test 254</li> <li>32 Test 8259 interrupt mask bits for channel 1</li> <li>40 Test 8259 interrupt mask bits for channel 1</li> <li>41 Test 8259 interrupt mask bits for channel 2</li> <li>43 Test 8259 interrupt mask bits for channel 2</li> <li>44 Tinitialize EISA slot</li> <li>1. Calculate total memory by testing the last double word of each 64K page</li> <li>2. Program Writes allocation for AMD K5 CPU</li> <li>1. Program MTRR of M1 CPU</li> <li>2. Initialize L2 cache for P6 class CPU &amp; program CPU with proper cacheable range</li> <li>3. Initialize the APIC for P6 class CPU</li> <li>4. On MP platform, adjust the cacheable range to smaller one in case the cacheable ranges between each CPU are not identical</li> <li>50 Initialize USB</li> <li>52 Test all memory (clear all extended memory to 0)</li> <li>53 Clear password according to H/W jumper (Optional)</li> <li>55 Display number of processors (multi-processor platform)</li> <li>Display number of processors (multi-processor platform)</li> <li>Display number of processors (multi-processor platform)</li> <li>54 Initialize the combined Trend Anti-Virus code</li> <li>59 Initialize Init_Onboard_Super_IO</li> <li>20 Initialize Init_Onboard_Super_IO</li> <li>21 Initialize Init_Onboard_AUDIO</li> </ul>		3. Program early chipset according to CMOS setup. Example: onboard IDE controller.
2B       Inivoke Video BJOS         2D       1. Initialize double-byte language font (Optional)         2D       2. Put information on screen display, including Award title, CPU type, CPU speed, full screen logo.         33       Reset keyboard if Early_Reset_KB is defined e.g. Winbond 977 series Super I/O chips. See also POST 63.         35       Test DMA Channel 0         37       Test DMA Channel 1.         39       Test DMA page registers.         3C       Test 8259 interrupt mask bits for channel 1         40       Test 8259 interrupt mask bits for channel 1         41       Test 8259 interrupt mask bits for channel 2         43       Test 8259 interrupt mask bits for Channel 2         44       Initialize EISA slot         49       1. Calculate total memory by testing the last double word of each 64K page         2. Program Writes allocation for AMD K5 CPU       2. Initialize L2 cache for P6 class CPU         4       On MP platform, adjust the cacheable range to smaller one in case the cacheable ranges between each CPU are not identical         50       Initialize USB         52       Test all memory (clear all extended memory to 0)         53       Clear password according to H/W jumper (Optional)         55       Display number of processors (multi-processor platform)         Display number of processors (multi-processor	20	4. Measure CPU speed.
<ul> <li>1. Initialize double-byte language font (Uptional)</li> <li>2. Put information on screen display, including Award title, CPU type, CPU speed, full screen logo.</li> <li>Reset keyboard if Early_Reset_KB is defined e.g. Winbond 977 series Super I/O chips. See also POST 63.</li> <li>Test DMA Channel 0</li> <li>Test DMA Channel 1.</li> <li>Test DMA Channel 1.</li> <li>Test 8254</li> <li>Test 8259 interrupt mask bits for channel 1</li> <li>Test 8259 interrupt mask bits for channel 2</li> <li>Test 8259 interrupt mask bits for channel 2</li> <li>Test 8259 functionality</li> <li>Tinitialize EISA slot</li> <li>1. Calculate total memory by testing the last double word of each 64K page 2. Program writes allocation for AMD KS CPU</li> <li>1. Program MTRR of M1 CPU</li> <li>2. Initialize L2 cache for P6 class CPU &amp; program CPU with proper cacheable range</li> <li>3. Initialize USB</li> <li>Test all memory (clear all extended memory to 0)</li> <li>Clear password according to H/W jumper (Optional)</li> <li>Display number of processors (multi-processor platform)</li> <li>Display PNP logo</li> <li>Early ISA PNP initialization -Assign CSN to every ISA PNP device</li> <li>Initialize the combined Trend Anti-Virus code</li> <li>Initialize Init_Onboard_Super_IO</li> <li>2. Initialize Init_Onboard_Super_IO</li> <li>2. Initialize Init_Onboard_Super_IO</li> </ul>	28	Invoke video BIOS
<ul> <li>20 2. Pdf information on screen display, including Award due, CPO type, CPO speed, full screen logo.</li> <li>33 Reset keyboard if Early_Reset_KB is defined e.g. Winbond 977 series Super I/O chips. See also POST 63.</li> <li>35 Test DMA Channel 0</li> <li>37 Test DMA page registers.</li> <li>3C Test 8254</li> <li>3E Test 8259 interrupt mask bits for channel 1</li> <li>40 Test 8259 interrupt mask bits for channel 2</li> <li>43 Test 8259 interrupt mask bits for channel 2</li> <li>44 T Initialize EISA slot</li> <li>1. Calculate total memory by testing the last double word of each 64K page</li> <li>2. Program Writes allocation for AMD KS CPU</li> <li>1. Program MTRR of M1 CPU</li> <li>2. Initialize L2 cache for P6 class CPU &amp; program CPU with proper cacheable range</li> <li>36 Initialize USB</li> <li>52 Test all memory (clear all extended memory to 0)</li> <li>53 Clear password according to H/W jumper (Optional)</li> <li>55 Display number of processors (multi-processor platform)</li> <li>56 Display PnP logo</li> <li>57 Early ISA PnP initialization <ul> <li>-Assign CSN to every ISA PnP device</li> <li>59 Initialize Ita Conbined Trend Anti-Virus code</li> <li>58 (Optional Feature) Show message for entering AWDFLASH.EXE from FDD (optional)</li> <li>50 Initialize Init_Onboard_AUDIO</li> </ul> </li> </ul>	20	1. Initialize double-byte language font (Optional)
33       Reset keyboard if Early_Reset_KB is defined e.g. Winbond 977 series Super I/O chips. See also POST 63.         35       Test DMA Channel 0         37       Test DMA Channel 1.         39       Test DMA page registers.         3C       Test 8259 interrupt mask bits for channel 1         40       Test 8259 interrupt mask bits for channel 1         41       Test 8259 interrupt mask bits for channel 2         43       Test 8259 interrupt mask bits for channel 2         44       Test 8259 interrupt mask bits for channel 2         45       Test 8259 interrupt mask bits for channel 2         46       Test 8259 interrupt mask bits for channel 2         47       Initialize EISA slot         1       Calculate total memory by testing the last double word of each 64K page         2. Program writes allocation for AMD K5 CPU         1. Program MTRR of M1 CPU         2. Initialize L2 cache for P6 class CPU         4. On MP platform, adjust the cacheable range to smaller one in case the cacheable ranges between each CPU are not identical         50       Initialize USB         52       Test all memory (clear all extended memory to 0)         53       Clear password according to H/W jumper (Optional)         55       Display number of processors (multi-processor platform)         59       Displ	20	
<ul> <li>also POST 63.</li> <li>Test DMA Channel 0</li> <li>Test DMA Channel 1.</li> <li>Test DMA page registers.</li> <li>Test 8259 interrupt mask bits for channel 1</li> <li>Test 8259 interrupt mask bits for channel 2</li> <li>Test 8259 interrupt mask bits for channel 2</li> <li>Test 8259 functionality</li> <li>Test 8259 functionality</li> <li>I. Calculate total memory by testing the last double word of each 64K page</li> <li>Program MTRR of M1 CPU</li> <li>Initialize LISA slot</li> <li>I. Program MTRR of M1 CPU</li> <li>Initialize L2 cache for P6 class CPU &amp; program CPU with proper cacheable range</li> <li>Initialize USB</li> <li>Test all memory (clear all extended memory to 0)</li> <li>Clear password according to H/W jumper (Optional)</li> <li>Display PnP log0</li> <li>Early ISA PnP initialization</li></ul>		Reset keyboard if Farly, Reset, KB is defined e.g. Winbond 977 series Super I/O chins. See
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3E       Test 8259 interrupt mask bits for channel 1         40       Test 8259 interrupt mask bits for channel 2         43       Test 8259 functionality         47       Initialize EISA slot         49       1. Calculate total memory by testing the last double word of each 64K page         2. Program writes allocation for AMD K5 CPU         1. Program MTRR of M1 CPU         2. Initialize L2 cache for P6 class CPU & program CPU with proper cacheable range         3. Initialize the APIC for P6 class CPU         4. On MP platform, adjust the cacheable range to smaller one in case the cacheable ranges between each CPU are not identical         50       Initialize USB         52       Test all memory (clear all extended memory to 0)         53       Clear password according to H/W jumper (Optional)         55       Display number of processors (multi-processor platform)         57       Early ISA PnP initialization -Assign CSN to every ISA PnP device         59       Initialize the combined Trend Anti-Virus code         58       (Optional Feature) Show message for entering AWDFLASH.EXE from FDD (optional)         50       1. Initialize Init_Onboard_Super_IO         2. Initialize Init_Onboard_AUDIO       2. Initialize Init_Onboard_AUDIO	3C	Test 8254
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43       Test 8259 functionality         47       Initialize EISA slot         49       1. Calculate total memory by testing the last double word of each 64K page         2. Program writes allocation for AMD K5 CPU         41       1. Program MTRR of M1 CPU         2. Initialize L2 cache for P6 class CPU & program CPU with proper cacheable range         42       3. Initialize the APIC for P6 class CPU         4       0 MP platform, adjust the cacheable range to smaller one in case the cacheable ranges between each CPU are not identical         50       Initialize USB         52       Test all memory (clear all extended memory to 0)         53       Clear password according to H/W jumper (Optional)         55       Display number of processors (multi-processor platform)         57       Early ISA PnP initialization -Assign CSN to every ISA PnP device         59       Initialize the combined Trend Anti-Virus code         58       (Optional Feature) Show message for entering AWDFLASH.EXE from FDD (optional)         50       1. Initialize Init_Onbaard_Super_IO         2. Initialize Init_Onbaard_AUDIO       1. Initialize Init_Onbaard_AUDIO	40	Test 8259 interrupt mask bits for channel 2
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<ul> <li>49         <ol> <li>Calculate total memory by testing the last double word of each 64K page</li> <li>Program writes allocation for AMD K5 CPU</li> <li>Program MTRR of M1 CPU</li> <li>Initialize L2 cache for P6 class CPU &amp; program CPU with proper cacheable range</li> <li>Initialize the APIC for P6 class CPU</li> <li>On MP platform, adjust the cacheable range to smaller one in case the cacheable ranges between each CPU are not identical</li> </ol> </li> <li>50         <ol> <li>Initialize USB</li> <li>Test all memory (clear all extended memory to 0)</li> <li>Clear password according to H/W jumper (Optional)</li> <li>Display number of processors (multi-processor platform)</li> <li>Display PNP logo</li> <li>Early ISA PnP initialization</li></ol></li></ul>	47	Initialize EISA slot
<ul> <li>49</li> <li>2. Program writes allocation for AMD K5 CPU</li> <li>1. Program MTRR of M1 CPU</li> <li>2. Initialize L2 cache for P6 class CPU &amp; program CPU with proper cacheable range</li> <li>3. Initialize the APIC for P6 class CPU</li> <li>4. On MP platform, adjust the cacheable range to smaller one in case the cacheable ranges between each CPU are not identical</li> <li>50 Initialize USB</li> <li>52 Test all memory (clear all extended memory to 0)</li> <li>53 Clear password according to H/W jumper (Optional)</li> <li>55 Display number of processors (multi-processor platform)</li> <li>57 Early ISA PnP initialization         <ul> <li>-Assign CSN to every ISA PnP device</li> <li>59 Initialize the combined Trend Anti-Virus code</li> <li>58 (Optional Feature) Show message for entering AWDFLASH.EXE from FDD (optional)</li> <li>50 Initialize Init_Onboard_Super_IO</li> <li>2. Initialize Init_Onboard_AUDIO</li> </ul> </li> </ul>		1. Calculate total memory by testing the last double word of each 64K page
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57       Early ISA PhP Initialization -Assign CSN to every ISA PhP device         59       Initialize the combined Trend Anti-Virus code         58       (Optional Feature) Show message for entering AWDFLASH.EXE from FDD (optional)         5D       1. Initialize Init_Onboard_Super_IO 2. Initialize Init_Onboard_AUDIO		Display PhP logo
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59       Initialize the combined trend And-virus code         5B       (Optional Feature) Show message for entering AWDFLASH.EXE from FDD (optional)         5D       1. Initialize Init_Onboard_Super_IO         2. Initialize Init_Onboard_AUDIO	50	-Assign CSN to every ISA Phil device
SD         (Optional reactive) show message for entering AWDELASH.EXE from PDD (optional)           5D         1. Initialize Init_Onboard_Super_IO           2. Initialize Init_Onboard_AUDIO	59 EP	(Optional Egatura) Show macrage for optoring AW/DELASH EVE from EDD (optional)
5D 2. Initialize Init_Onboard_AUDIO	30	(Optional Factor) Show message for entering AWDFLASH.EXE from FDD (Optional)
	5D	1. Initialize Init_Onboard_Super_IO
Okay to optor Sotup utility i.e. not until this DOST stage can users onter the CMOS sotup		Okay to onter Setup utility i.e. not until this DOST stage can users onter the CMOS setup
60 diverse and being and setup durity, i.e. not unit this POST stage can users effer the CMOS setup	60	utility
63 Reset keyboard if Early Reset KB is not defined	63	Reset keyboard if Early Reset KB is not defined
65 Initialize PS/2 Mouse	65	Initialize PS/2 Mouse
67 Prepare memory size information for function call: INT 15h ax=E820h	67	Prepare memory size information for function call: INT 15h ax=E820h
69 Turn on L2 cache	69	Turn on L2 cache

6B	Program chipset registers according to items described in Setup & Auto-configuration table
	1. Assign resources to all ISA PnP devices
6D	<ol> <li>Auto assign ports to onboard COM ports if the corresponding item in Setup is set to "AUTO"</li> </ol>
65	1. Initialize floppy controller
ог	2. Set up floppy related fields in 40:hardware
75	Detect & install all IDE devices: HDD, LS120, ZIP, CDROM
	(Optional Feature)
76	Enter AWDFLASH.EXE if:
70	-AWDFLASH is found in floppy drive
	-ALT+F2 is pressed
77	Detect serial ports & parallel ports.
7A	Detect & install co-processor
7C	Init HDD write protect
	Switch back to text mode if full screen logo is supported
7F	-If errors occur, report errors & wait for keys
	-If no errors occur or F1 key is pressed to continue: Clear EPA or customization logo
	E8POST.ASM starts
	1. Call chipset power management hook
82	2. Recover the text font used by EPA logo (not for full screen logo)
	3. If password is set, ask for password
83	Save all data in stack back to CMOS
84	Initialize ISA PnP boot devices
05	1. USB final Initialization
85	2. Switch screen back to text mode
87	NET PC: Build SYSID Structure
80	1. Assign IRQs to PCI devices
89	2. Set up ACPI table at top of the memory.
8B	1. Invoke all ISA adapter ROMs
05	2. Invoke all PCI ROMs (except VGA)
80	1. Enable/Disable Parity Check according to CMOS setup
00	2. APM Initialization
8F	Clear noise of IRQs
93	Read HDD boot sector information for Trend Anti-Virus code
	1. Enable L2 cache
	2. Program Daylight Saving
	3. Program boot up speed
94	4. Chipset final initialization.
_	5. Power management final initialization
	6. Clear screen & display summary table
	7. Program K6 write allocation
05	8. Program Po class write combining
95	update keyboard LED & typematic rate
	1. Build MYP table
06	2. Dullu & upualle ESCD
90	5. Set CMOS tentury to 200 of 190
	5. Ruild MSTRO routing table
	5. Duild MISING TOULING LODE
<b>77</b>	ן בסטר מונפוווףר (101 1311)

POST (hex)	Description
	Power On Sequence
8.1.	Start power on sequence
8.2. Enable ATX power supply	
8.3.	ATX power supply ready
8.4.	DDR voltage ready
8.5.	Setup PWM for CPU core voltage
8.6.	Assert PWM for CPU core voltage
8.7.	Check CPU core voltage
8.8.	CPU core voltage ready
8.9.	Initial clock generator IC
8.A.	North Bridge chipset voltage ready
8.B.	AGP voltage ready
8.C.	3VDUAL voltage ready
8.D.	VDDA 2.5V voltage ready
8.D.	GMCHVTT voltage ready
8.E.	Check CPU fan speed
8.F.	Assert all power ready
9.0	Complete µGuru initial process
5.0.	AWARDBIOS take over booting job
	Power Off Sequence
9.1.	Start power off sequence
9.2.	De-Assert all power
9.3.	De-Assert power on
9.4.	De-Assert LDT Bus power
9.5.	De-Assert PWM for CPU core voltage
9.6.	De-Assert CPU core voltage
9.7.	Check CPU core voltage
9.8.	De-Assert ATX power supply
9.9.	Complete power off sequence
	Others
F.0.	Button reset
F.1.	SoftMenu reset
F.2.	Power on sequence timeout
F.3.	Power off sequence timeout

### 5.1.2 AC2005 POST Code Definitions

# 5.2 Troubleshooting (How to Get Technical Support?)

### 5.2.1 Q & A

- **Q:** Do I need to clear the CMOS before I use a new motherboard to assemble my new computer system?
- A: Yes, we highly recommend that you clear the CMOS before installing a new motherboard. Please move the CMOS jumper from its default 1-2 position to 2-3 for a few seconds, and then back. When you boot up your system for the first time, follow the instructions in the user's manual to load the optimized defaults.
- **Q:** If my system hangs when I update the BIOS or set the wrong CPU parameters, what should I do?
- A: Whenever you update the BIOS or if the system hangs due to wrong CPU parameters setting, always clear CMOS jumper before booting up again.
- Q: Why does the system fail to boot up again right after a mechanical power-off?
- A: Please keep a 30-second interval between each mechanical power On/Off.
- Q: Why does the system fail to boot up and nothing displays on the screen after I did some over-clocking or non-standard settings inside the BIOS?
- A: It should not cause hardware or permanent damage to motherboard when BIOS settings were changed from default to over-clocking or non-standard status.

We suggest the following three troubleshooting methods to discharge CMOS data, recover the hardware default status, and then making the motherboard work again. There is no need to bother returning the motherboard to where you bought it from or go through an RMA process.

**Step 1.**Switch off the power supply unit and then switch it on again after one minute. If there is no power-switch on the power supply unit, disconnect its power cord for one minute and then reconnect.

Press and hold the <Insert> key on the keyboard, and press the power-on button to boot up system. If it works, release the <Insert> key and hit <Del> key to enter the BIOS setup page to apply the correct settings.

If the situation remains the same, repeat the procedures in Step 1 for three times, or try Step 2.

**Step 2.**Switch off the power supply unit or disconnect the power cord. Open the chassis cover. Locate the CCMOS jumper near the button battery. Change the jumper position from default 1-2 to 2-3 for one minute to discharge the CMOS data, and then put it back to default 1-2 position.

Close the chassis and switch on the power supply unit or plug in the power cord. Press the power-on button to boot up system. If it works, hit <Del> key to enter the BIOS setup page to do the correct settings.

If the situation remains the same, try Step 3.

**Step 3.**The same procedure as Step 2, but while discharging the CMOS data, pull out the ATX power connectors from motherboard and remove the button battery during CMOS discharge.

Appendix

### Q: How to get a quick response for my request on technical support?

A: Please carry out a simple troubleshooting before sending "Technical Support Form":

### System boot-up fails after the system had been assembled:

Check the motherboard's supporting specifications first to see if all the key components attached in your system can meet.

To do so, you may:

- Remove all the unnecessary add-on devices (except the CPU, VGA card, DRAM, and Power Supply), and then reboot.
- ♥ If the trouble still exists, try another memory module of different brand/model.
- ♥ If the trouble still exists, try another CPU and Power Supply.

If the system runs successfully, shut it down and start re-installing the interface cards and devices that were previously installed in the system. Re-install and start the system one at a time until the system won't start.

### Malfunction in the OS:

If the system hangs after resuming from S3 or some testing program, if the CPU cannot be recognized properly, if the display resolution mixed, or if a certain program cannot be executed, etc, you may:

- ✤ Upgrade the motherboard's latest BIOS version.
- ✤ Upgrade the add-on device's latest driver version.
- ⇔ Check if there is any conflict in the "Control Panel/System Properties".

### Q: How to fill in the "Technical Support Form"?

- A: To fill in this "Technical Support Form", please refer to the following instructions:
  - **Region:** Type in your country name.
  - **E-mail:** Type in your contact E-mail information.
  - First name: Type in your first name.
  - Last name: Type in your last name.
  - **Subject:** Type in the model name and the problem of your motherboard. Example 1: AA8XE and SCSI 29160 malfunction Example 2: AA8XE boot fails, POST code AF Example 3: AA8XE (system hang when S3 resume)
  - **Motherboard:** Type in the model name and revision number of your motherboard. Example: AA8XE REV: 1.00
  - **BIOS Version:** Type in the BIOS version of your motherboard. (You can find it on the screen during the POST sequence.)
  - CPU: Type in the brand name and the speed (MHz) of your CPU. (Illustrate the over-clocking status if you had done so.)
     Example: Intel 650 3.4GHz (OC FSB=220MHz)
  - **Memory brand:** Type in the brand and model name of your memory module. Example: Memory brand: Kingston (KVR533D2N4/1G)

- **Memory size:** Type in the size of your memory module. Example: 512M\* 4PCS
- **Memory configuration:** Type in the memory configuration in BIOS setting. Example: Memory Timing: 2.5-3-3-7 @533MHz
- Graphics information: Note Graphics card's brand, model and driver version
- **Graphics card:** Type in the brand and model name of your graphics card. Example: ATI RADEON X850 XT PE
- **Graphics driver version:** Type in the driver version of your graphics card Example: Catalyst 5.12V
- Power supply maker: Type in the brand and model name of your power supply unit.
- Power supply wattage: Type in the power wattage of your power supply unit.
- Storage devices: Type in the brand and specifications of your HDD drive and quantity. Specify if it was inserted on IDE (Master or Slave) or SATA ports, including the RAID allocation status.
   Example 1: WD Caviar WD600 60GB (on IDE2 master), Maxtor DiamondMax 10 SATA 300GB (on SATA 3)
   Example 2: Maxtor DiamondMax 10 SATA 300GB \*2 (on SATA 3, SATA 4 RAID 1)
- **Optical devices:** Type in the brand and specifications of your optical drives and quantity. Specify if it was inserted on IDE (Master or Slave) or SATA ports.
- **Other devices:** Indicate which add-on cards or USB devices that you *absolutely sure* are related to the problem. If you cannot identify the problem's origin, indicate all the add-on cards or USB devices inserted on your system. Example: AHA 29160 (on PCI 2), Sandisk Cruzer mini 256MB USB Flash-disk.
- Operating system: Indicate which OS and language version Example: Microsoft Windows XP SP2, English version Example: Microsoft Media Center Edition 2005, Korean version
- **Problem description:** Describe the problem of your system configuration. Indicate the steps to duplicate problem if possible.

See the next page for a blank Technical Support Form, or visit our website to fill in the form on line (<u>http://www.abit.com.tw/page/en/contact/technical.php</u>).

# **Q.** Is the motherboard dead? Do I need to return it to where I bought from or go through an RMA process?

A: After you had gone through the troubleshooting procedures, yet the problem still exists, or you find an evident damage on the motherboard. Please contact our RMA center. (http://www2.abit.com.tw/page/en/contact/index.php?pFUN\_KEY=18000&pTITLE\_IMG)

### 5.2.2 Technical Support Form

Region:
E-mail:
First name:
Last Name:
Subject:
Motherboard:
BIOS Version:
CPU:
Memory brand:
Memory size:
Memory configuration:
Graphics card:
Graphics driver version:
Power supply maker:
Power supply wattage:
Storage devices:
Optical devices:
Other devices:
Operating system:
Problem description:

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### 5.2.3 Universal ABIT Contact Information

### **Taiwan Head Office**

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### UK, Ireland

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http://www.abit.com.tw

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