



Module

Instruction Sheet

# High-speed Counter DVP-01HC

# WARNING

- $\triangle$  Please carefully read this instruction thoroughly prior to use the DVP-01HC.
- $\triangle$  Make sure that power is OFF before wiring.
- A This is an OPEN TYPE PLC. The PLC should be kept in an enclosure away from airborne dust, humidity, electric shock risk and vibration. Also, it is equipped with protective methods such as some special tools or keys to open the enclosure, in order to prevent hazard to users or damage the PLC.
- △ Do NOT connect the AC main circuit power supply to any of the input/output terminals, or it may damage the PLC. Check all the wiring prior to power up

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### INTRODUCTION

# 2.1 Model Name Explanation and Peripherals

- Thank you for choosing DELTA'S PLC DVP series. DVP-01HC high-speed counter input module could accept 200KHz pulse from external counter. It uses instruction FROM/TO to read/write the data in module via EH MPU. There are 33 Controlled Registers (CR, each register is 16-bit) in each module. 32 bits data consists of two continuous CRs and allow executing itself after setting CR.
- The different counter modes, such as single-phase mode, two phases mode, 16-bit mode or 32-bit mode, can be designated by CR. The controlled registers are written by instruction TO via MPU. When wiring, connect 24V, A24+, B24+, P24+, D24+, A12+, B12+, A5+, B5+, P5+ and D5+ to positive potential. In the same way, 0V, A24-, B24-, P24-, D24-, A12-, B12-, A5-, B5-, P5- and D5- should be connected to negative potential.
- Input signal source could be 1-phase or 2-phase encoder and voltage level could be 5V, 12V and 24V. Besides, it also provides two terminals, PRESET and DISABLE. When terminal "PRE" is on, the data in CR#10 and CR#11 will be sent to CR#20 and CR#21. That also means current value of counter will be changed to be factory setting. When terminal "DIS" is on, "count" operation is disabled.
- There are two outputs, YH0 and YH1, in hardware input module of DVP-01HC. When counter value is equal to the setting, the corresponding output point will activate. The transistors of output points are independent and isolated.

#### Nameplate Explanation

HC: High-speed counter module



Production Model

Unit: mm

1. DIN rail track (35mm)	2. Mounting wire to connect extension module/extension unit
3. Model name	4. Status Indicator (Power, Run and ERROR)
5. DIN rail clip	6. Terminal
7. Mounting hole	8. Terminal layout
9. Extension port to connect extension module/unit	10. RS-485 communication port

# LED Display

2.2 Product Profile and Outline

1.	Power	: Power LED. When external +24V power is applied, it will be ON.
2.	L.V.	: Low voltage LED. When external power supply is lower than 19V, it will be ON.
3.	UP	: Count up LED
4.	DOWN	: Count down LED
5.	$\phi  A$	: When input point A is ON, it will light.
6.	$\phi  B$	: When input point B is ON, it will light.
7.	PRE	: PRESET LED. When external terminal PRE is ON, it will light.
8.	DIS	: DISABLE LED. When external terminal DIS is ON, it will light.
9.	YH0, YH1	: When output points YH0 and YH1 are ON, it will light.
_		





1. Please use O-type or Y-type terminals for I/O wiring terminals. The specification for the terminals is as shown on the left. Tighten PLC terminal screws to a torque of 5 to 8 kg-cm (4.3~6.9 in-lbs). 2. I/O signal wires or power supply should not run through the same

multi-wire cable or conduit.

3. Use copper conductor only, 60°C.

# External Wiring



- A Make sure the positive/negative pole of 01HC input terminal wiring is correct when using NPN encoder
- ▲ Start-up current for 01HC is I<sub>PEAK</sub>=0.8A and general working current is I<sub>MAX</sub>=0.2A(input voltage is +24V).





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Setting notices:

1 It must write with 32-bit

# FUNCTION SPECIFICATION

Environmental specifications

1. Operation: 0°C ~55°C (Temperature), 50~95% (Humidity), pollution degree 2
2. Storage: -25°C ~70°C (Temperature), 5~95% (Humidity)
Standard: IEC1131-2, IEC 68-2-6 (TEST Fc) / IEC1131-2 & IEC 68-2-27 (TEST
Ea)
All places between terminals and ground comply with the spec

#### CONTROLLED REGISTER (CR)

	DVP	-01HC H	igh-speed C	ounter Module			
Content		Setting Range					
Model type System us			System us	ed, read only, DVP-01HC model code=H'0120			
Count up/down mode setting		Setting range: 0-1, factory setting K0 1-phase 1 input(Software): count up/down mode setting, count up: K0, count down:K1					
Ring length 16		16-bit: factory setting is K65,536.					
K2 K200 K		K1	Write K200 into first extension module CR#2 and CR#3 (i.e. CR#3 = 0, CR #2 = 200). Setting range: K2 to K65,536.				
				When ring length is set to K200. The count value			

	_ <b>t</b>		When ring length is set to K200, The count value will be as shown on the left.
199	₀ ↑	1	Count up: when count value reaches 199, the next count value will be 0.
0	199	198	Count down: when count value reaches 0, the next count value will be 199.

2. It only can be write-in when writing value is greater or equal to current count value.

3. It only can be set when counter stops counting and count mode is 16-bit.

		#4	H4	162	$\times$	R/W instruction Instru			
		CR#	4	'0'(Off)		'1' (On)			
	b0	Cou	nt is	disabl	ed	Count is enabl	ed		
	b1		۲ŀ	YH0 output is disabled			YH0 output is enabled		
		b2		۲ŀ	ł1 o disa	utput is abled	6	YH1output is ena	abled
		b3	YH0 and YH1 activate independently		YH0 and YH1 affect each other (they cannot be ON/OFF simultaneously)				
	b4		Pre	set	disable	ed	Preset is enabl	led	
		b5~b	7				Re	eserved	
		b8			Not	used		Clear error flag	
		b9		Not used         Clear error flag           Not used         Clear YH0 output					
		b10			Not	used		Clear YH1 outp	out
		b11			Not	used		YH0 output set	ting
		b12			Not	used		YH1 output set	ting
b13~b15 Re				Re	eserved				

ction, factory setting: K0

- 1. When b0 is set to 1 and terminal "DIS" is off, count is enabled.
- 2. When b1 is set to 1, YH0 (hardware comparison output) output is enable 3. When b2 is set to 1, YH1 (hardware comparison
- output) output is enabled. 4. When b3 is set to 1, YH0 and YH1 affect each
- other (they cannot be ON/OFF simultaneously). In other words, when YH0=ON, YH1 must be OFF and when YH0=OFF, YH1 must be ON. When b3=0. YH0 and YH1 activate independently
- 5. When b4=0, terminal "PRE" is disabled.
- 6. When b8=1, all error flags (CR#29) will be cleared 7. When b9=1, YH0 output will be cleared to be OFF.
- 8. When b10=1, YH1 output will be cleared to be OFF
- 9. When b11=1, YH0 output will be ON. 10.When b12=1, YH1 output will be ON.

Setting notes:

- After setting CR#4\_b8~b12 will be cleared to 0
- 2. It needs to set to disable count(b0=0) before setting count mode (CR#5).

#5 H4163	K/W Count mode setting	Count mode K0~K1	1, factory setting	g is K0	
	Count mode	CR#5 settings			
		32 bits	5	16 bits	
2-phase 2 inputs	Normal frequency	K0		K1	
	Double frequency	K2		K3	
	Four times frequency	K4		K5	
1	-phase 2 inputs	K6		K7	
	Count mode		CR#5 setti	ings	
		32 bits	3	16 bits	
1-phase 1 input	Count Up/Down is controlled by Hardware (Note 1)	K8		К9	
	Count Up/Down is controlled by software (Note 2)	K10		K11	
Note1: count up/o Note2: count up/o	down control is controlled by extended by control is controlled by inte	ernal input control. rnal control register(	(CR#1).		
■ 16-bit mode When it is 16-bit m range is 0~65,536 be changed from u limit is set by CR#3	node, the count values are all po . When overflow event is occurre upper limit to 0 or from 0 to upp 3 and CR #2.	sitive value and its ed, count value will er limit. The upper	0	count leng CR#3, #2	
<ul> <li>32-bit mode</li> <li>When it is 32-bit</li> <li>2,147,483,647. Will</li> <li>be changed from</li> <li>upper limit. And u</li> <li>-2,147,483,648.</li> </ul>	t mode, the count range is - hen overflow event is occurred, upper limit to lower limit or fro upper limit is +2,147,483,647 a	2,147,483,648 - count value will om lower limit to nd lower limit is	-2,147,48	upper limit +2,147,483,647	

Setting notes:

- It only can be written when count is disabled (bit 0 of CR#4 is 0).
- After writing, it will initial controlled registers as follows: CR#1: 0. CR#2, 3: 65,536. CR#10: 0. CR#12, 13: 2 32,767. CR#14, 15: 32,767. CR#20, 21: 0. CR#22, 23: 0. CR#24, 25: 0.

1-phase 1 input (K8~K11)		1-phase 2 inputs (K6~K7)
External input count up/down control (K8~K9)	Internal controlled register count up/down control	1-phase 2 inputs counter (K6, K7)
A input Off (count up) On (count down) Count up count down value	CR#1K0 K1 A inputCount up count down valueCount down	A inputffpulse of count up B inputffffpulse of count down count value 1 2 3 3 3 2 1 0
2-phase 2 inputs (K0~K5)		
Normal frequency (K0, K1)	Double frequency (K2, K3)	Four times frequency (K4, K5)
A input	A input	
B input	B input count value 0 1 2 3 4 3 2 1 0	Binput

#6 ~ #9 R						Reserved		
	#11	#10 H4168 × R/W F				Factory setting	Factory setting for counter (#10: Lower word / #11: Upper word), factory setting: K0 Setting notes: in 16-bit mode, CR#11 will be cleared to 0 when writing factory setting.	
	#13	#12	H416A	$\times$	R/W	YH0 comparison value	YH0 output comparison value (#12: Lower word / #13: Upper word), factory setting: K32,767. Setting notes: in 16-bit mode, CR#13 will be cleared to 0 when writing YH0 comparison value.	
	#15 #14 H416C × R/W YH1 comparison value		YH1 comparison value	YH1 output comparison value (#14: Lower word / #15: Upper word), (factory setting: K32,767). Setting notes: in 16-bit mode, CR#15 will be cleared to 0 when writing YH1 comparison value.				

When current value of counter = comparison value, output current value YH0/YH1 will be ON and hold. User can clear output point by using b9 and b10 of CR#4.

If count value = comparison value by using PRESET or instruction TO, corresponding output YH0 or YH1 will be OFF. In other case that count value = comparison value does not use PRESET or instruction TO, corresponding output YH0 or YH1 will be ON.

Reserved

counter

R/W Max. count value

R/W Min. count value

'0'(Off)

Setting value ≦

current value

Setting value =

current value

Setting value ≥

current value

#27 H4179 O R Action status

A input is off

B input is off

R/W Error status

Reserved

Reserved

R System version

R/W Communication

address

R/W Baud Rate

Setting

'0'(Off)

Current value of

#16~ #19

#21 #20 H4172 × R/W

#23 #22 H4174

#25 #24 H4176

CR#26

YH0 b1

#26 H4178

b2

b0

CR#27

b0

b1

b2

b3

#29 H417B

CR#29

b0~ b3

b4

b5

b6

b7~ b15

#30 H417C

#31 H417D

#32 H417E

5



'1'(On)

Setting value > current

Setting value = current

value

Setting value < current

value

1'(On)

PRE input is On

DIS input is On

YH0 output is Or

value

Current value of counter (#20: Lower word / #21: Upper word),

In 16-bit mode, value that is written must be less than ring

In 16-bit mode, CR#21 will be cleared to 0 when writing into

Max. count value (#22: Lower word / #23: Upper word), factory

Min. count value (#24: Lower word / #25: Upper word), factory

'0'(Off)

Setting value ≤

current value

Setting value ≠

current value

Setting value  $\geq$ 

current value

b7 YH1 output is Off YH1 output is On

Data register that is used to save all error status. Refer to

Hexadecimal. displav current software version, such as version

RS-485 communication address, range set: 01~255, factory

Baud rate setting: 4800,9600,19200bps,38400 bps,57600 bps,

115200 bps. ASCII mode data format is always 7Bit, even bit,

and 1 stop bit (7 E 1). RTU mode data format is always 8Bit,

b0: 4800 bps(bit/sec.), b1: 9600 bps(bit/sec.) (default value) b2: 19200 bps(bit/sec.), b3: 38400 bps(bit/sec.)

b 4: 57600 bps(bit/sec.), b 5: 115200 bps(bit/sec.) b6~b14: reserved, b15: ASCII / RTU mode switch

The indication of count up/down and terminal status

PRE input is Off

YH0 output is Off

b5 DIS input is Off

Frror Status

Overflow indication, When count-up value exceeds upper limit(upper limit is CR#2 and #3

Overflow indication, When count-down value is less than lower limit(lower limit is 0 in

1.0A will be displayed as H'010A.

even bit, and 1 stop bit (8 E 1)

CR number that is designated by instruction FROM/TO exceeds the usage range

factory setting is K0.

it must write with 32-bit.

current value of counter

CR#26

YH1

b5

b4

CR#27 '0'(Off)

b4

b6

table below.

in 16-bit mode and it is K2,147,483,647 in 32-bit mode)

16-bit mode and it is K-2,147,483,648 in 32-bit mode)

Setting notes

length (CR#2).

setting is K0

setting is K0.

R Comparison result Comparison result

'1'(On)

Setting value >

current value

Setting value =

current value

Setting value <

current value

'1'(On)

Count up

Count down

A input is on

B input is on



- damage internal circuit.
- as following





Troubleshooting

please check:

- ☆ "POWER" LED

- ☆ <u>L.V. LED</u>
- 6 API FROM 78 D ♦ (m1): nu Instruction Explanation read. Writing sp Program write two Example X10 —I I-API то Ρ 79 D ♦ (m1): nu Instruction Explanatio wrote in Program Example X11

CR#0~CR#34: The corresponding addresses are H 415E-H 4180 for user to read/write by using RS-485.

default value: K1

- Baud rate could be 4800, 9600, 19200, 38400 and 57600bps. 1.
- 2. Communication protocol can be Modbus ASCII mode and RTU mode. For ASCII mode, data format is 7Bits, even, 1 stop bit (7 E 1). For RTU mode, data format is 8Bits, even, 1 stop bit (8 E 1).
- Function code: 03H: read register data. 06H: write one WORD data into register. 10H: write 3. multiple WORDs into register.

#### **TRIAL RUN & TROUBLESHOOTING**

- MPU connects to HC extension module
- 1. Make sure that the power of MPU and extension unit is OFF before wiring.
- 2. Open extension port of EH MPU and connect to HC extension unit with cable. There is no connection order for EH MPU to connect extension unit, mix connection is allowed.
- 3. The power supply of HC extension unit must be external +24VDC power supply.
- 4. Before power up, check if the load circuit of output points YH0 and YH1 is correct, especially the circuit between YH0+, YH0- and YH1+, YH1-. There is a Zener Diode that is connected between YH0+, YH0- and YH1+, YH1- in HC. If the positive/negative pole is wrong, it may cause unexpected result.



5. Before power up, check if A phase or B phase connects to correct voltage level. (there are three voltage level: +24, +12V and +5V) If +24V signal connects to +5V input terminal, it may

6. After power up MPU, it will start to detect extension module. If no external +24VDC power is applied to HC at this time or power is applied after EH MPU completing detecting extension module, the connection will fail. Therefore, power wiring and power supply timing should be

7. The maximum special extension modules number for EH MPU connects is 8 special extension modules. After power is ON, EH will save module codes of connected special modules in D1320~D1327 in order. The module code of 01HC is H'0120. It indicates communication is OK when H'0120 is displayed in the corresponding special D register by using HPP02 or other monitor software.

Judge the errors by the indicators on the front panel. When errors occurred on DVP PLC,

The "POWER" LED at the front of HC extension module will be lit (in green) if the power is on. If the indicator is not on when power up, please remove the wiring on terminals +24V. Once the indicator lights after this, it means that the 24V DC power supply of the PLC is overloaded. Please do not use the DC power supply from the +24V terminals, but use a DC24V power supply instead.

The "L.V." LED at the front of HC extension module will be lit if input voltage is not enough. The extension module won't active at this time.

RELATIVE INSTR	RELATIVE INSTRUCTIONS								
(m1) (m2) (D) (n)	Read Special Module CR Data								
nber of special module (m1=0~7). (m2): CR number of special module that will be D: address for saving reading data. (n): data number for reading once. ecial module #0 of CR#24 into D0 and special module #0 of CR#25 into D1. only ata once (n=2).									
FROM K0 K24 D	0 К2								
(m1) (m2) (S) (n) Special Module CR Data Write In									
mber of special module (m1=0~7). (m2): CR number of special module that will be (S): data to write in CR. (n): data number to write in once.									

◆ Using 32-bit instruction DTO to write D11 and D10 into special module#0 of CR#3 and CR#2. only write a data once (n=1).

_	DTO	K0	K2	D10	K1
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