# DELPHI SERIES





The Delphi Series Q48SR Quarter Brick, 48V input, adjustable single output, isolated, open frame DC/DC converters are the latest offering from a world leader in power systems technology and manufacturing — Delta Electronics, Inc. This product family provides up to 165 watts of power or up to 60A of output current in an industry standard footprint. This product represents the next generation of design technology which may be utilized to provide high levels of current at very low output voltages required by today's leading-edge circuitry. Utilizing an advanced patented thermal and electrical design technology; the Delphi Series Q48SR converters are capable of providing higher output current capability with excellent transient response and lower common mode noise. Featuring a wide operating output voltage range and high current at low output voltages, these units offer more useable power over a wide range of ambient operating conditions. The wide range trimmable output feature allows the user to both reduce and standardize part numbers across different and/or migrating voltage requirements. This model covers the output range of 0.8V to 1.9V at 40A.

#### **FEATURES**

- High efficiency: 87% @ 1.8V/40A
- Standard footprint:57.9x36.8x10.2mm (2.28"x1.45"x0.40")
- Industry standard pin out
- Fixed frequency operation
- Wide output trim range: 0.8V~1.9V
- Fully protected: OTP, OVP, OCP, UVLO
- No minimum load required
- Fast transient response
- Start up into pre-biased load
- Basic insulation
- ISO 9000, TL 9000, ISO 14001 certified manufacturing facility
- UL/cUL 60950 (US & Canada) Recognized, and TUV (EN60950) Certified
- CE mark meets 73/23/EEC and 93/68/EEC directives

#### **OPTIONS**

- Short lead lengths
- Non-latching over voltage protection
- Negative trim
- Positive on/off logic

#### **APPLICATIONS**

- Telecom/DataCom
- Wireless Networks
- Optical Network Equipment
- Server and Data Storage
- Industrial/Test Equipment

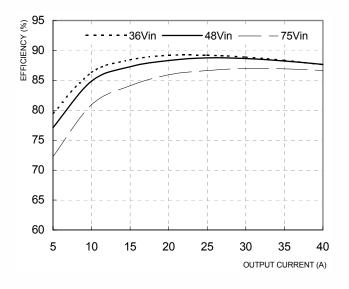
Datasheet DS\_Q48SR1R840\_12212004



# **TECHNICAL SPECIFICATIONS**

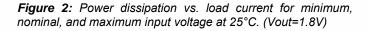
(TA=25°C, airflow rate=300 LFM, Vin=48Vdc, nominal Vout unless otherwise noted; mounted on board.)

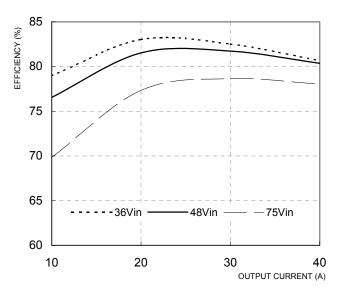
PARAMETER	NOTES and CONDITIONS	Q48SR1R840NR A				
		Min.	Тур.	Max. Units		
ABSOLUTE MAXIMUM RATINGS						
Input Voltage				00	) ( d -	
Continuous	100ma			80 100	Vdc	
Transient (100ms) Operating Temperature	100ms  Refer to Figure 24 for the measuring point	-40		114	Vdc °C	
Storage Temperature	Refer to Figure 24 for the measuring point	- <del>4</del> 0 -55		125	°C	
Input/Output Isolation Voltage	1 minute	1500		125	Vdc	
INPUT CHARACTERISTICS	1 minute	1300			Vuc	
Operating Input Voltage		36	48	75	Vdc	
Input Under-Voltage Lockout			.0			
Turn-On Voltage Threshold		33	34	35	Vdc	
Turn-Off Voltage Threshold		31	32	33	Vdc	
Lockout Hysteresis Voltage		1	2	3	Vdc	
Maximum Input Current	100% Load, 36Vin			2.7	Α	
No-Load Input Current			50	100	mA	
Off Converter Input Current			10	20	mA	
Inrush Current(I <sup>2</sup> t)			0.03		A <sup>2</sup> S	
Input Reflected-Ripple Current	P-P thru 12µH inductor, 5Hz to 20MHz		20		mA	
Input Voltage Ripple Rejection	120 Hz		70		dB	
OUTPUT CHARACTERISTICS						
Output Voltage Set Point	Vin=48V, Io=Io.max, Ta=25C	1.75	1.8	1.85	Vdc	
Output Voltage Regulation						
Over Load	lo=lo,min to lo,max		±2	±5	mV	
Over Line	Vin=36V to 75V		±2	±5	mV	
Over Temperature	Ta=-40C to 85C		±30	±100	mV	
Total Output Voltage Range	over sample load, line and temperature	1.69		1.91	V	
Output Voltage Ripple and Noise	5Hz to 20MHz bandwidth					
Peak-to-Peak	Full Load, 1µF ceramic, 10µF tantalum		20	100	mV	
RMS	Full Load, 1μF ceramic, 10μF tantalum		10	20	mV	
Operating Output Current Range	0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	0		40	A	
Output DC Current-Limit Inception DYNAMIC CHARACTERISTICS	Output Voltage 10% Low	105		130	%	
Output Voltage Current Transient	48V, 10μF Tan & 1μF Ceramic load cap, 0.1A/μs					
Positive Step Change in Output Current	50% lo,max to 75% lo,max		50	150	mV	
Negative Step Change in Output Current	75% lo,max to 50% lo,max		50	150	mV	
Settling Time (within 1% Vout nominal)	73 /0 10,111ax to 30 /0 10,111ax		100	130	uS	
Turn-On Transient			100		uo	
Start-Up Time, From On/Off Control			15	30	mS	
Start-Up Time, From Input			15	30	mS	
Maximum Output Capacitance	Full load; 5% overshoot of Vout at startup		.0	20000	μF	
EFFICIENCY					p	
100% Load			87		%	
60% Load			88.5		%	
ISOLATION CHARACTERISTICS						
Input to Output		1500			Vdc	
Isolation Resistance		10			ΜΩ	
Isolation Capacitance			2000		pF	
FEATURE CHARACTERISTICS						
Switching Frequency			260		kHz	
ON/OFF Control, (Logic Low-Module ON)						
Logic Low	Von/off at Ion/off=1.0mA	0		0.8	V	
Logic High	Von/off at Ion/off=0.0 μA			15	V	
ON/OFF Current	Ion/off at Von/off=0.0V			1	mA	
Leakage Current	Logic High, Von/off=15V			50	uA	
Output Voltage Trim Range	Across Pins 9 & 5, Pout <= max rated power	0.8		1.9	V	
Output Voltage Remote Sense Range	Pout <= max rated power		4	10	%	
Output Over-Voltage Protection	Over full temp range; % of nominal Vout	115	130	145	%	
GENERAL SPECIFICATIONS	15-000/ of 15-man To 0500 of 5-man Cool 5-14		0.0		M	
MTBF	lo=80% of Io, max; Ta=25°C, airflow rate=300 LFM		2.3		M hour	
Weight	D.C. J. El. C.C. J.		32		grams	
Over-Temperature Shutdown	Refer to Figure 24 for the measuring point		125		°C	



24.0 POWER DISSIPATION (W) -36Vin 75Vin 48Vin 20.0 16.0 12.0 8.0 4.0 0.0 5 10 15 20 25 30 35 40 OUTPUT CURRENT(A)

**Figure 1:** Efficiency vs. load current for minimum, nominal, and maximum input voltage at 25°C. (Vout=1.8V)





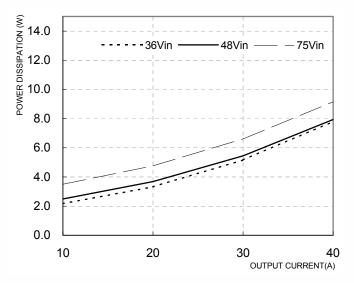


Figure 3: Efficiency vs. load current for minimum, nominal, and maximum input voltage at 25°C. (Vout=0.8 V)

**Figure 4:** Power dissipation vs. load current for minimum, nominal, and maximum input voltage at 25°C. (Vout=0.8V)

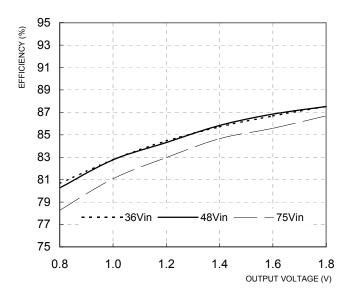


Figure 5: Efficiency vs. output voltage for minimum, nominal, and maximum input voltage at 25°C. (lout=40A)

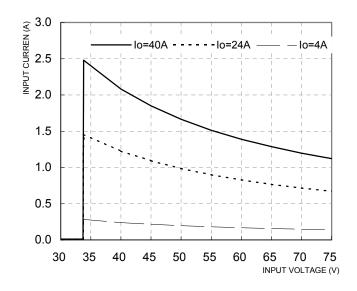


Figure 7: Typical input characteristics at room temperature

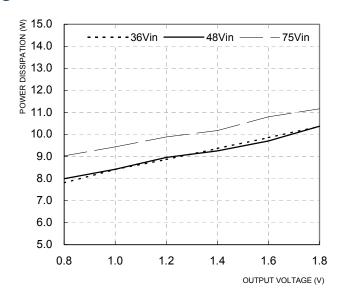
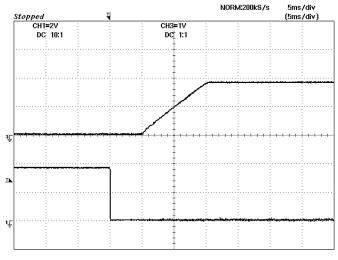
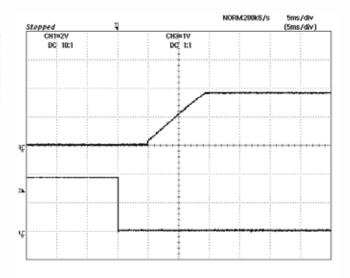


Figure 6: Power dissipation vs. output voltage for minimum, nominal, and maximum input voltage at 25°C. (lout=40A)



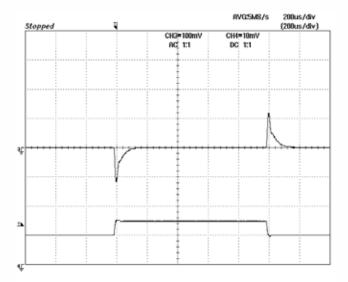
**Figure 8:** Turn-on transient at full rated load current (resistive load) (5 ms/div). Top Trace: Vout; 1V/div; Bottom Trace: ON/OFF input: 2V/div



Stopped T CH3=50mV CH4=10mV DC 1:1

**Figure 9:** Turn-on transient at zero load current (5 ms/div). Top Trace: Vout: 1V/div; Bottom Trace: ON/OFF input: 2V/div

**Figure 10:** Output voltage response to step-change in load current (75%-50%-75% of Io, max; di/dt = 0.1A/μs). Load cap: 10μF, tantalum capacitor and 1μF ceramic capacitor. Top Trace: Vout (50mV/div), Bottom Trace: lout (20A/div). Scope measurement should be made using a BNC cable (length shorter than 20 inches). Position the load between 51 mm to 76 mm (2 inches to 3 inches) from the module..



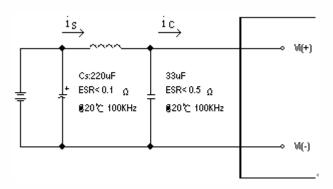
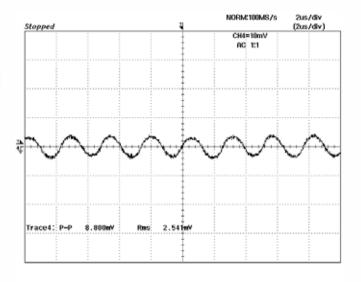


Figure 11: Output voltage response to step-change in load current (75%-50%-75% of Io, max: di/dt = 2.5A/μs). Load cap: 470μF, 35mΩ ESR solid electrolytic capacitor and 1μF ceramic capacitor. Top Trace: Vout (100mV/div), Bottom Trace: lout (20A/div). Scope measurement should be made using a BNC cable (length shorter than 20 inches). Position the load between 51 mm to 76 mm (2 inches to 3 inches) from the module.

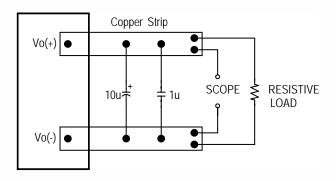
**Figure 12:** Test set-up diagram showing measurement points for Input Terminal Ripple Current and Input Reflected Ripple Current. Note: Measured input reflected-ripple current with a simulated source Inductance ( $L_{TEST}$ ) of 12  $\mu$ H. Capacitor Cs offset possible battery impedance. Measure current as shown above.



**Figure 13:** Input Terminal Ripple Current, i<sub>c</sub>, at full rated output current and nominal input voltage with 12μH source impedance and 33μF electrolytic capacitor (500 mA/div).

**Figure 14:** Input reflected ripple current, i<sub>s</sub>, through a 12µH source inductor at nominal input voltage and rated load current (5 mA/div).

NORM:100MS/s 2us/div



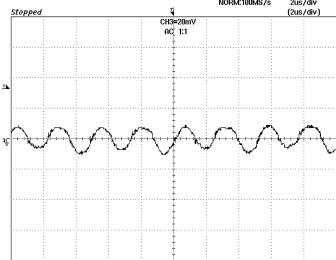
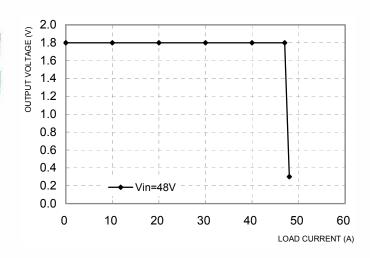


Figure 15: Output voltage noise and ripple measurement test setup

Figure 16: Output voltage ripple at nominal input voltage and rated load current (20 mV/div). Load capacitance: 1µF ceramic capacitor and 10µF tantalum capacitor. Bandwidth: 20 MHz. Scope measurement should be made using a BNC cable (length shorter than 20 inches). Position the load between 51 mm to 76 mm (2 inches to 3 inches) from the module.



**Figure 17:** Output voltage vs. load current showing typical current limit curves and converter shutdown points.

#### **DESIGN CONSIDERATIONS**

# **Input Source Impedance**

The impedance of the input source connecting to the DC/DC power modules will interact with the modules and affect the stability. A low ac-impedance input source is recommended. If the source inductance is more than a few  $\mu H,$  we advise adding a 10 to 100  $\mu F$  electrolytic capacitor (ESR < 0.7  $\Omega$  at 100 kHz) mounted close to the input of the module to improve the stability.

#### **Layout and EMC Considerations**

Delta's DC/DC power modules are designed to operate in a wide variety of systems and applications. For design assistance with EMC compliance and related PWB layout issues, please contact Delta's technical support team. An external input filter module is available for easier EMC compliance design. Application notes to assist designers in addressing these issues are pending release.

#### **Safety Considerations**

The power module must be installed in compliance with the spacing and separation requirements of the enduser's safety agency standard, i.e., UL60950, CAN/CSA-C22.2 No. 60950-00 and EN60950:2000 and IEC60950-1999, if the system in which the power module is to be used must meet safety agency requirements.

When the input source is 60 Vdc or below, the power module meets SELV (safety extra-low voltage) requirements. If the input source is a hazardous voltage which is greater than 60 Vdc and less than or equal to 75 Vdc, for the module's output to meet SELV requirements, all of the following must be met:

- The input source must be insulated from any hazardous voltages, including the ac mains, with reinforced insulation.
- One Vi pin and one Vo pin are grounded, or all the input and output pins are kept floating.
- The input terminals of the module are not operator accessible.
- A SELV reliability test is conducted on the system where the module is used to ensure that under a single fault, hazardous voltage does not appear at the module's output.

Do not ground one of the input pins without grounding one of the output pins. This connection may allow a non-SELV voltage to appear between the output pin and ground.

The power module has extra-low voltage (ELV) outputs when all inputs are ELV.

This power module is not internally fused. To achieve optimum safety and system protection, an input line fuse is highly recommended. The safety agencies require a normal-blow fuse with 20A maximum rating to be installed in the ungrounded lead. A lower rated fuse can be used based on the maximum inrush transient energy and maximum input current.

## **Soldering and Cleaning Considerations**

Post solder cleaning is usually the final board assembly process before the board or system undergoes electrical testing. Inadequate cleaning and/or drying may lower the reliability of a power module and severely affect the finished circuit board assembly test. Adequate cleaning and/or drying is especially important for un-encapsulated and/or open frame type power modules. For assistance on appropriate soldering and cleaning procedures, please contact Delta's technical support team.

#### FEATURES DESCRIPTIONS

#### **Over-Current Protection**

The modules include an internal output over-current protection circuit, which will endure current limiting for an unlimited duration during output overload. If the output current exceeds the OCP set point, the modules will automatically shut down (hiccup mode).

The modules will try to restart after shutdown. If the overload condition still exists, the module will shut down again. This restart trial will continue until the overload condition is corrected.

#### **Over-Voltage Protection**

The modules include an internal output over-voltage protection circuit, which monitors the voltage on the output terminals. If this voltage exceeds the over-voltage set point, the module will shut down and latch off. The over-voltage latch is reset by cycling the input power for one second.

#### **Over-Temperature Protection**

The over-temperature protection consists of circuitry that provides protection from thermal damage. If the temperature exceeds the over-temperature threshold the module will shut down.

The module will try to restart after shutdown. If the overtemperature condition still exists during restart, the module will shut down again. This restart trial will continue until the temperature is within specification.

#### Remote On/Off

The remote on/off feature on the module can be either negative or positive logic. Negative logic turns the module on during a logic low and off during a logic high. Positive logic turns the modules on during a logic high and off during a logic low.

Remote on/off can be controlled by an external switch between the on/off terminal and the Vi(-) terminal. The switch can be an open collector or open drain.

For negative logic if the remote on/off feature is not used, please short the on/off pin to Vi(-). For positive logic if the remote on/off feature is not used, please leave the on/off pin floating.

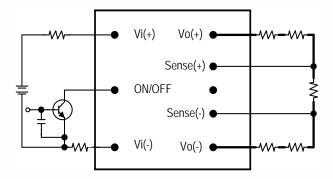


Figure 18: Remote on/off implementation

#### **Remote Sense**

Remote sense compensates for voltage drops on the output by sensing the actual output voltage at the point of load. The voltage between the remote sense pins and the output terminals must not exceed the output voltage sense range given here:

$$[Vo(+) - Vo(-)] - [SENSE(+) - SENSE(-)] \le 10\% \times Vout$$

This limit includes any increase in voltage due to remote sense compensation and output voltage set point adjustment (trim).

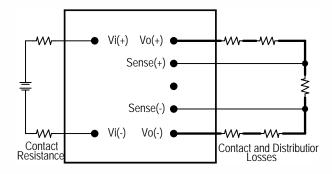


Figure 19: Effective circuit configuration for remote sense operation

If the remote sense feature is not used to regulate the output at the point of load, please connect SENSE(+) to Vo(+) and SENSE(-) to Vo(-) at the module.

The output voltage can be increased by both the remote sense and the trim; however, the maximum increase is the larger of either the remote sense or the trim, not the sum of both.

When using remote sense and trim, the output voltage of the module is usually increased, which increases the power output of the module with the same output current.

Care should be taken to ensure that the maximum output power does not exceed the maximum rated power.

# **FEATURES DESCRIPTIONS (CON.)**

# **Output Voltage Adjustment (TRIM)**

To increase or decrease the output voltage set point, connect an external resistor between the TRIM pin and either the SENSE(+) or SENSE(-). The TRIM pin should be left open if this feature is not used.

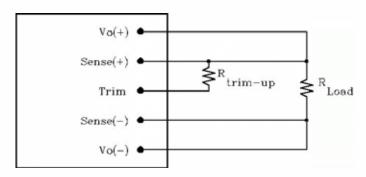


Figure 20: Circuit configuration for trim-up (increase output voltage)

If the external resistor is connected between the TRIM and SENSE (+) pins, the output voltage set point increases (Fig. 20). The external resistor value required to obtain a percentage of output voltage change % is defined as:

Rtrim\_up(
$$\Delta$$
) =  $\left[\frac{Vonom(1+\Delta) - Vref}{Vref\Delta}\right] 10k\Omega - 11k\Omega$ 

where

Vonom = nominal Vout (3.3V or 1.8V)

Vref = 1.225V

 $\Delta$  = trim expressed as decimal fraction, i.e. 10% is written as 0.1

Ex. When trim up to 1.9V from 1.8V Vonom = 1.8V Vref = 1.225V  $\Delta$  = (1.9-1.8)/1.8 = 0.05556 (1.8×1.05556 -1.225

$$\Rightarrow Rtrim - up = \frac{(1.8 \times 1.05556 - 1.225)}{1.225 * 0.05556} \times 10 K\Omega$$

 $-11K\Omega = 88.18K\Omega$ 

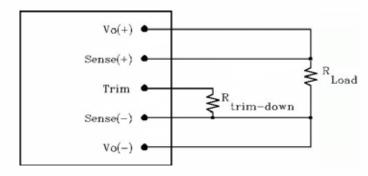


Figure 21: Circuit configuration for trim-down (decrease output voltage)

If the external resistor is connected between the TRIM and SENSE (-) the output voltage set point decreases (Fig. 21). The external resistor value required to obtain a percentage output voltage change % is defined as:

Rtrim\_down(
$$\Delta$$
) =  $\frac{10K\Omega}{\Delta}$  - 11 $k\Omega$ 

where

Vonom = nominal Vout (3.3V or 1.8V)

 $\Delta$  = trim expressed as decimal fraction, i.e. 40% is written as 0.4

Ex. When trim down to 0.8V from 1.8V

Vonom = 1.8V  $\Delta = (1.8-0.8)/1.8 = 0.5556$ 10K

$$Rtrim - down = \frac{10K}{0.5556} - 11K\Omega = 7K\Omega$$

The output voltage can be increased by both the remote sense and the trim, however the maximum increase allowed is the larger of either the remote sense spec or the trim spec, not the sum of both.

When using remote sense and trim, the output voltage of the module is usually increased, which increases the power output of the module with the same output current.

Care should be taken to ensure that the maximum output power of the module remains at or below the maximum rated power.

Output voltage	Resistor value ( $k\Omega$ )
1.5V	49.00
1.2V	19.00
1.0V	11.50
0.9V	9.0
0.8V	7.0

**Figure 22:** Trim resistor value example for popular output voltages. Connect the resistor between the TRIM and SENSE (-) pins.

## THERMAL CONSIDERATIONS

Thermal management is an important part of the system design. To ensure proper, reliable operation, sufficient cooling of the power module is needed over the entire temperature range of the module. Convection cooling is usually the dominant mode of heat transfer.

Hence, the choice of equipment to characterize the thermal performance of the power module is a wind tunnel.

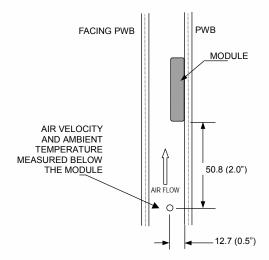
#### **Thermal Testing Setup**

Delta's DC/DC power modules are characterized in heated vertical wind tunnels that simulate the thermal environments encountered in most electronics equipment. This type of equipment commonly uses vertically mounted circuit cards in cabinet racks in which the power modules are mounted.

The following figure shows the wind tunnel characterization setup. The power module is mounted on a test PWB and is vertically positioned within the wind tunnel. The space between the neighboring PWB and the top of the power module is constantly kept at 6.35mm (0.25").

#### **Thermal Derating**

Heat can be removed by increasing airflow over the module. The module's maximum device temperature is 114 and the measured location is illustrated in Figure 24. To enhance system reliability, the power module should always be operated below the maximum operating temperature. If the temperature exceeds the maximum module temperature, reliability of the unit may be affected.



Note: Wind Tunnel Test Setup Figure Dimensions are in millimeters and (Inche Figure 23: Wind tunnel test setup

## THERMAL CURVES

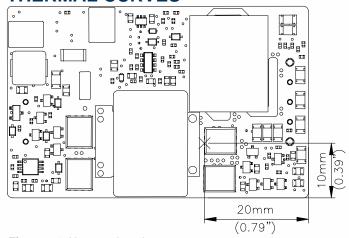
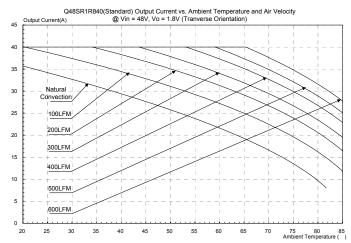


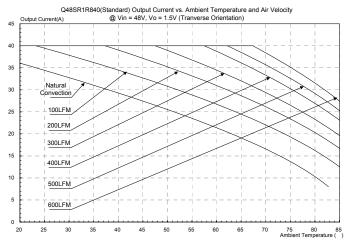
Figure 24: Hot spot location

\*The allowed maximum hot spot temperature is defined at 114

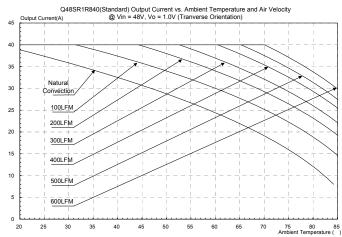
# **THERMAL CURVES (CON.)**



**Figure 25:** Output current vs. ambient temperature and air velocity ( $V_{in}$ =48V,  $V_{out}$ =1.8V, transverse orientation)

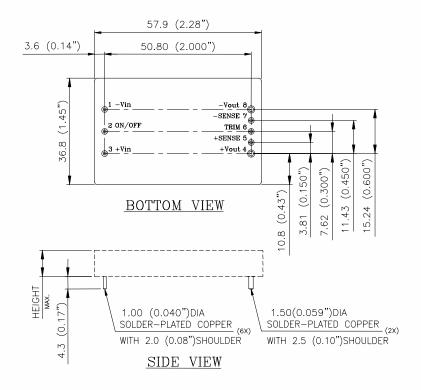


**Figure 26:** Output current vs. ambient temperature and air velocity (V<sub>in</sub>=48V, V<sub>out</sub>=1.5V, transverse orientation)



**Figure 27:** Output current vs. ambient temperature and air velocity ( $V_{in}$ =48V,  $V_{out}$ =1.0V, transverse orientation)

# **MECHANICAL DRAWING**



	HEIGHT
1R8	10.2 (0.40")
3R3	10.4 (0.41")

NOTES:

DIMENSIONS ARE IN MILLIMETERS AND (INCHES)
TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.)
X.XXmm±0.25mm(X.XXX in.±0.010 in.)

Pin No.	Name	Function
1	-Vin	Negative input voltage
2	ON/OFF	Remote ON/OFF
3	+Vin	Positive input voltage
4	+Vout	Positive output voltage
5	+SENSE	Positive remote sense
6	TRIM	Output voltage trim
7	-SENSE	Negative remote sense
8	-Vout	Negative output voltage

#### Notes:

- 1 Pins 1-3, 5-7 are 1.00mm (0.040") diameter
- 2 Pins 4 and 8 are 1.50mm (0.060") diameter
- 3 All pins are copper with tin plating

## PART NUMBERING SYSTEM

The part numbering system for Delta's Q48SR DC/DC converters have the following format:

					<u> </u>				
Q	48	S	R	1R8	40	N	R		Α
Form Factor	Input	Number of	Product	Output	Output	ON/OFF	Pin	Space	Option Code
	Voltage	Outputs	Series	Voltage	Current	Logic	Length		
Q - Quarter	48V	S - Single	R - Single	1R8 - 1.8V	40 - 40A	N - Negative	R - 0.170"		A - Standard
Brick			Board			P - Positive	N - 0.145"		Functions
							K - 0.110"		

## **MODEL LIST**

MODEL NAME	INF	INPUT		PUT	EFF @ 100% LOAD	
Q48SR1R840NR A	36V~75V	2.7A	0.8V - 1.9V	40A - 72W	87%	
Q48SR1R860NR A	36V~75V	4.0A	0.8V - 1.9V	60A - 108W	88%	
Q48SR3R335NR A	36V~75V	4.2A	1.7V - 3.6V	35A - 115W	90%	
Q48SR3R350NR A	36V~75V	5.9A	1.7V - 3.6V	50A - 165W	91%	

Note: Please contact us for factory pre-set fixed output voltages.

CONTACT: www.delta.com.tw/dcdc

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Telephone: Telephone: +41 31 998 53 11

East Coast: (888) 335 8201 West Coast: (888) 335 8208 Fax: (978) 656 3964

Email: DCDC@delta-corp.com

Asia & the rest of world:

Telephone: +886 3 4526107 x6220

Fax: +886 3 4513485 Email: <u>DCDC@delta.com.tw</u>

#### **WARRANTY**

Delta offers a two (2) year limited warranty. Complete warranty information is listed on our web site or is available upon request from Delta.

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